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				SHEET		1	2	3	4	5	6	7	8	9	10	11	12	13	14
PMIC N/A				PREPARED BY Larry T. Gauder						DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444									
STANDARD MICROCIRCUIT DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A				CHECKED BY Thomas M. Hess															
				APPROVED BY Thomas M. Hess															
				DRAWING APPROVAL DATE 95-01-26															
				REVISION LEVEL															
				SIZE A		CAGE CODE 67268				5962-95540									
				SHEET		1		OF		18									

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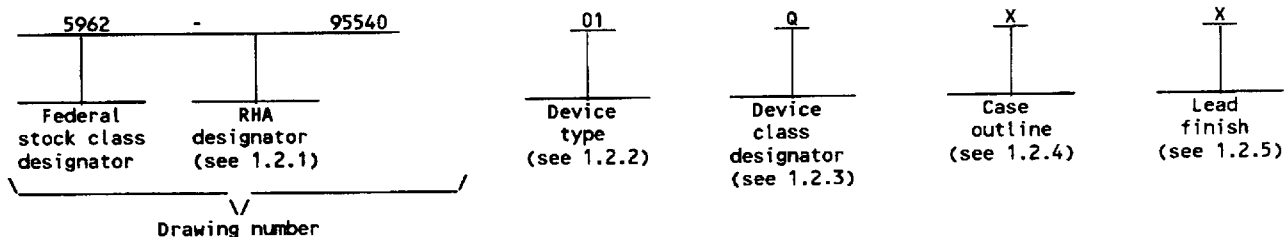
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1. SCOPE

1.1 Scope. This drawing forms a part of a one part - one part number documentation system (see 6.6 herein). Two product assurance classes consisting of military high reliability (device classes Q and M) and space application (device class V), and a choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). Device class M microcircuits represent non-JAN class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices". When available, a choice of Radiation Hardness Assurance (RHA) levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 RHA designator. Device class M RHA marked devices shall meet the MIL-I-38535 appendix A specified RHA levels and shall be marked with the appropriate RHA designator. Device classes Q and V RHA marked devices shall meet the MIL-I-38535 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	DS3884	BTL handshake transceiver

1.2.3 Device class designator. The device class designator shall be a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
M	Vendor self-certification to the requirements for non-JAN class B microcircuits in accordance with 1.2.1 of MIL-STD-883
Q or V	Certification and qualification to MIL-I-38535

1.2.4 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
X	See figure 1	48	flat package

1.2.5 Lead finish. The lead finish shall be as specified in MIL-STD-883 (see 3.1 herein) for class M or MIL-I-38535 for classes Q and V. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

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1.3 Absolute maximum ratings. 1/

Supply voltage (V_{CC})	6.5 V
Control input voltage	6.5 V
Driver input and receiver output	5.5 V
Bus termination voltage	2.4 V
Receiver input current	± 15 mA
Storage temperature range (T_{STG})	-65°C to +150°C
Maximum power dissipation at 25°C (P_D) 2/	1.7 W
Lead temperature (soldering, 4 seconds)	+260°C
Junction temperature (T_J)	+175°C
Thermal resistance, junction-to-case (θ_{JC})	5°C/W
Thermal resistance, junction-to-ambient (θ_{JA})	87°C/W

1.4 Recommended operating conditions.

Supply voltage range (V_{+})	+4.5 V dc to +5.5 V
Bus termination voltage (V_t)	2.06 V to 2.14 V
Ambient operating temperature range (T_A)	-55°C to +125°C

1.5 Digital logic testing for device classes Q and V.

Fault coverage measurement of manufacturing logic tests (MIL-STD-883, test method 5012) - - - - - 3/ XX percent

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, bulletin, and handbook. Unless otherwise specified, the following specification, standards, bulletin, and handbook of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-I-38535 - Integrated Circuits, Manufacturing, General Specification for.

STANDARDS

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.
MIL-STD-973 - Configuration Management.
MIL-STD-1835 - Microcircuit Case Outlines.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

HANDBOOK

MILITARY

MIL-HDBK-780 - Standardized Military Drawings.

(Copies of the specification, standards, bulletin, and handbook required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

- 1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
2/ Derate at 11.5 mW/°C above +25°C.
3/ Values will be added when they become available.

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2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device class M shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein. The individual item requirements for device classes Q and V shall be in accordance with MIL-I-38535, and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-STD-883 (see 3.1 herein) for device class M and MIL-I-38535 for device classes Q and V and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.2.3 Truth table. The truth table shall be as specified on figure 3.

3.2.4 Logic diagram. The logic diagram shall be as specified on figure 4.

3.2.5 Test circuits and switching waveforms. The test circuits and switching waveforms shall be as specified on figure 5.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. Marking for device class M shall be in accordance with MIL-STD-883 (see 3.1 herein). In addition, the manufacturer's PIN may also be marked as listed in MIL-BUL-103. Marking for device classes Q and V shall be in accordance with MIL-I-38535.

3.5.1 Certification/compliance mark. The compliance mark for device class M shall be a "C" as required in MIL-STD-883 (see 3.1 herein). The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-I-38535.

3.6 Certificate of compliance. For device class M, a certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.7.2 herein). For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.7.1 herein). The certificate of compliance submitted to DESC-EC prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device class M, the requirements of MIL-STD-883 (see 3.1 herein), or for device classes Q and V, the requirements of MIL-I-38535 and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required for device class M in MIL-STD-883 (see 3.1 herein) or for device classes Q and V in MIL-I-38535 shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change for device class M. For device class M, notification to DESC-EC of change of product (see 6.2 herein) involving devices acquired to this drawing is required for any change as defined in MIL-STD-973.

3.9 Verification and review for device class M. For device class M, DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

3.10 Microcircuit group assignment for device class M. Device class M devices covered by this drawing shall be in microcircuit group number 100 (see MIL-I-38535, appendix A).

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. For device class M, sampling and inspection procedures shall be in accordance with MIL-STD-883 (see 3.1 herein). For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-I-38535 and the device manufacturer's QM plan.

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TABLE 1. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C V _{CC} = 5 V ± 10% unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Driver and control inputs: (Dn, De, PS1 and PS2)							
Logical 1 input voltage	V _{IH}	V _{CC} = 4.5 V	1, 2, 3	01	2		V
Logical 0 input voltage	V _{IL}	V _{CC} = 4.5 V	1, 2, 3	01		0.8	V
Input leakage current	I _I	V _{CC} = 5.5 V, V _{IN} = 5.5 V	1, 2, 3	01		100	μA
Input high current	I _{IH}	V _{CC} = 5.5 V, V _{IN} = 2.4 V (Dn inputs)	1, 2, 3	01		40	μA
		V _{CC} = 5.5 V, V _{IN} = 2.4 V (DE/PS inputs)				40	
Input low current	I _{IL}	V _{CC} = 5.5 V, V _{IN} = 0.5 V	1, 2, 3	01	-100	100	μA
Input diode clamp voltage	V _{IC}	V _{CC} = 4.5 V, I _{CLAMP} = -12 mA	1, 2, 3	01		-1.2	V

Driver output/receiver input: (Bn)

Output low bus voltage	V _{OLB}	I _{OL} = 80 mA, Dn = 2.4 V, DE = 0 V 1/	1, 2	01	0.75	1.1	V
			3		0.75	1.15	
Output low bus current	I _{OLBZ}	V _{CC} = 5.5 V, Dn = 0.5 V Bn = 0.75 V, DE = 2.1 V	1, 2, 3	01	-100	100	μA
Output high bus current	I _{OHBZ}	V _{CC} = 5.5 V, Dn = 0.5 V Bn = 2.1 V, DE = 2.1 V	1, 2, 3	01	-100	100	μA
Receiver input threshold	V _{TH}	V _{CC} = 4.5 V, 5.5 V DE = 2.4 V	1, 2, 3	01	1.47	1.62	V

See footnotes at end of table.

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TABLE 1. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$ $V_{CC} = 5\text{ V} \pm 10\%$ unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Positive clamp diode voltage	V_{CLP}	$V_{CC} = 5.5\text{ V or }0\text{ V},$ $I_{Bn} = 1\text{ mA}$	1, 2, 3	01	2.4	4.5	V
		$V_{CC} = 5.5\text{ V or }0\text{ V},$ $I_{Bn} = 10\text{ mA}$	1, 2, 3		2.9	5.0	
Negative clamp diode voltage	V_{CLN}	$V_{CC} = 5.5\text{ V}, I_{clamp} = -12\text{ mA}$	1, 2, 3	01		-1.2	V
Output low bus current	I_{OLB}	$V_{CC} = 5.5\text{ V}, D_n = 0.5\text{ V}$ $B_n = 0.75\text{ V}, DE = 0\text{ V}$	1, 2, 3	01	-100	100	μA
Output high bus current	I_{OHB}	$V_{CC} = 5.5\text{ V}, D_n = 0.5\text{ V}$ $B_n = 2.1\text{ V}, DE = 0\text{ V}$	1, 2, 3	01	-100	100	μA

Receiver output: (FRn and Rn)

Logical 1 output voltage	V_{OH}	$V_{CC} = 4.5\text{ V}, B_n = 1.1\text{ V}$ $I_{OH} = -2\text{ mA}, DE = 2.4\text{ V}$	1, 2, 3	01	2.4		V
Logical 0 output voltage	V_{OL}	$V_{CC} = 4.5\text{ V}, B_n = 2.1\text{ V}$ $I_{OH} = 8\text{ mA}, DE = 2.4\text{ V}$	1, 2, 3	01		0.4	V
		$V_{CC} = 4.5\text{ V}, B_n = 2.1\text{ V}$ $I_{OH} = 24\text{ mA}, DE = 2.4\text{ V}$				0.5	
Output short circuit current	I_{OS}	$V_{CC} = 4.5\text{ V}, 5.5\text{ V}$ $B_n = 1.1\text{ V}, DE = 2.4\text{ V}$ 2/	1, 2, 3	01	-100	-40	mA
Functional test		$V_{CC} = 4.5\text{ V}, 5.5\text{ V}$ See 4.4.1b	7, 8				

See footnotes at end of table.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _a ≤ +125°C V _{CC} = 5 V ± 10% unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Supply current							
Supply current (V _{CC} , QV _{CC} and L _I)	I _{CC}	V _{CC} = 5.5 V, DE = 0.5 V all Dn = 2.4 V	1, 2, 3	01		80	mA
		V _{CC} = 5.5 V, DE = 2.4 V, all Bn = 2.1 V					
Live insertion current	I _{LI}	V _{CC} = 5.5 V, DE = 0.5 V all Dn = 2.4 V	1, 2, 3	01		5	mA
		V _{CC} = 5.5 V, DE = 2.4 V, all Bn = 2.1 V				3	
Driver							
DN to BN propagation delay	t _{PHLD}	DE = 0 V, R _L = 12.5Ω C _L = 30 pF	9, 10, 11	01	1	7	ns
DN to BN propagation delay	t _{PLHD}	DE = 0 V, R _L = 12.5Ω C _L = 30 pF	9, 10, 11	01	1	7	ns
DE to BN enable time	t _{PHLDE}	Dn = 3 V, R _L = 12.5Ω C _L = 30 pF	9, 10, 11	01	1	9	ns
DE to BN disable time	t _{PLHDE}	Dn = 3 V, R _L = 12.5Ω C _L = 30 pF	9, 10, 11	01	1	9	ns
Transition time 20% to 80%	t _r		9, 10, 11	01	1	3.5	ns
Transition time 80% to 20%	t _f		9, 10, 11	01	1	4.5	ns
Skew rate	S _R	from 1.3 V to 1.8 V	9, 10, 11	01	1		ns
Skew between drivers in the same package	t _{skewD}	3/	9, 10, 11	01		5	ns

See footnotes at end of table.

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TABLE 1. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T ≤ +125°C V _{CC} = 5 V ± 10% unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Receiver							
Bn to Rn propagation delay	t _{PHLR}	De = 3 V, R _L = 1KΩ C _L = 50 pF	9, 10, 11	01	1	8	ns
Bn to Rn propagation delay	t _{PLHR}	De = 3 V, C _L = 50 pF	9, 10, 11	01	1	8	ns
Skew between receivers in the same package	t _{skewR}	3/	9, 10, 11	01		5	ns
Filtered Receiver							
Bn to FRn propagation delay	t _{PHLFR}	DE = PS1 = PS2 = 3 V, REXT = 15KΩ, C _L = 50 pF, R _L = 1KΩ	9, 10, 11	01	22	66	ns
		DE = 3 V, PS1 = PS2 = 0 V, REXT = 15KΩ, C _L = 50 pF, R _L = 1KΩ			6	21	
		DE = PS1 = 3 V, PS2 = 0 V, REXT = 15KΩ, C _L = 50 pF, R _L = 1KΩ			16	41	
		DE = PS2 = 3 V, PS1 = 0 V, REXT = 15KΩ, C _L = 50 pF, R _L = 1KΩ			11	31	
Bn to FRn propagation delay	t _{PLHFR}	DE = 3 V, REXT = 15 KΩ 4/	9, 10, 11	01	2	8	ns
Glitch rejection	t _{GR}	DE = PS1 = PS2 = 3 V, REXT = 15 KΩ	9, 10, 11	01	20	65	ns
		DE = 3 V, PS1 = PS2 = 0 V, REXT = 15 KΩ			5	20	
		DE = PS1 = 3 V, PS2 = 0 V, REXT = 15 KΩ			14	40	
		DE = PS2 = 3 V, PS1 = 0 V, REXT = 15 KΩ			10	30	

1/ Referenced to appropriate signal ground. Do not exceed maximum power dissipation of package.

2/ Only one output at a time should be shorted and duration of short not to exceed one second.

3/ t_{skew} is an absolute value defined as differences seen in propagation delays between drivers/receivers in the same package.4/ Filtered receiver t_{PLH} is independent of filter setting.

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Case X

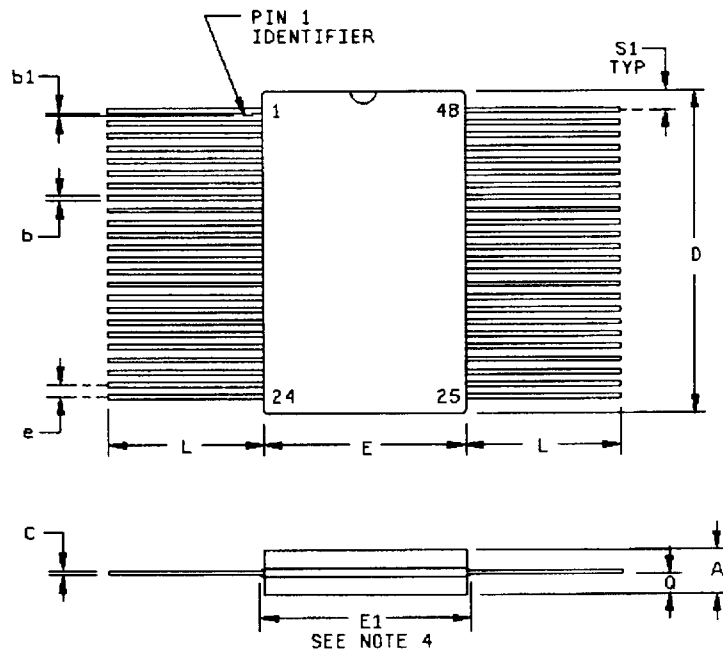


FIGURE 1. Case outline.

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Dimensions	Millimeters		Inches		Notes
	Min	Max	Min	Max	
A	1.78	2.54	.070	.100	
b	0.20	0.30	.008	.012	
b1	0.08	0.18	.003	.007	
c	0.10	0.23	.004	.009	
D		16.26		.640	
e	0.64 BSC		.025 BSC		
E	9.55	9.75	.376	.384	
E1		10.16		.400	4
L	6.35	8.89	.250	.350	
q	0.79	1.14	.031	.045	5
S1	0.13		.005		

NOTES:

1. The preferred unit of measurement is millimeters. However, this item was designed using inch-pound units of measurements. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Lead number 1 is identified by a tab located on the lead.
3. Lead numbers are shown for reference only and do not appear on package.
4. Dimensions E1 allow for glass meniscus.
5. Dimension Q shall be measured at the point of exit of the lead from the body.

FIGURE 1. Case outline.- continued

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Device type	All	Device type	All
Case outline	X	Case outline	X
Terminal number	Terminal symbol	Terminal number	Terminal symbol
1	Q GND	25	NC
2	L1	26	NC
3	D1	27	Q GND
4	R1	28	Q V _{CC}
5	FR 1	29	\overline{DE}
6	NC	30	B6 GND
7	NC	31	B6
8	D2	32	B5 GND
9	R2	33	B5
10	FR 2	34	B4 GND
11	GND	35	B4
12	V _{CC}	36	B3 GND
13	D3	37	B3
14	R3	38	B2 GND
15	FR 3	39	B2
16	D4	40	B1 GND
17	R4	41	B1
18	D5	42	NC
19	R5	43	NC
20	D6	44	PS 2
21	R6	45	PS 1
22	GND	46	GND
23	NC	47	V _{CC}
24	NC	48	REXT

FIGURE 2. Terminal connections.

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\overline{DE}	Dn	FRn	Rn	Bn
H	X	H	H	L
H	X	L	L	H
L	H	H	H	L
L	L	L	L	H

H = High state.
 L = Low state.
 X = High or low logic state.
 Z = High impedance state.

FIGURE 3. Truth table.

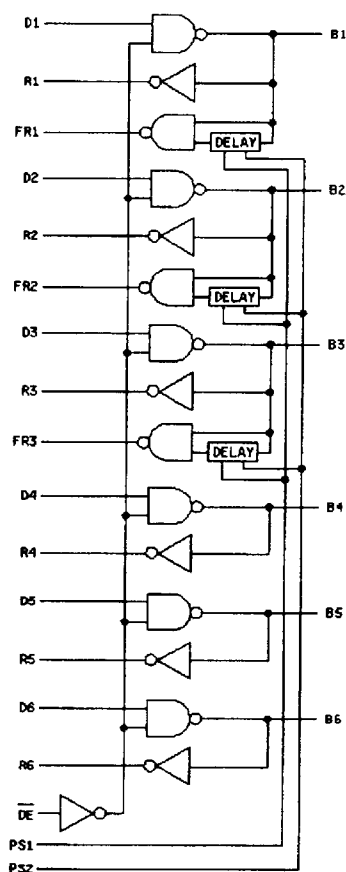
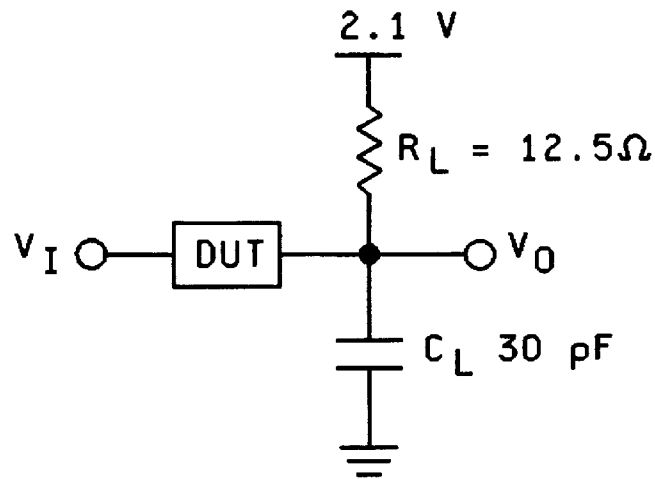


FIGURE 4. Logic diagram.

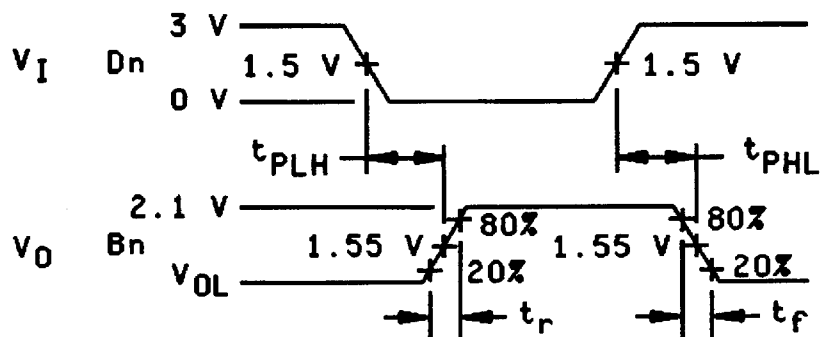
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Driver propagation delay set-up



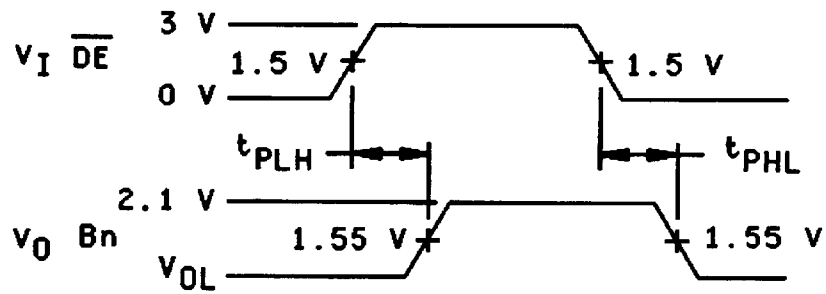
Driver: Dn to Bn, t_r , t_f

FIGURE 5. Test circuit and switching waveforms.

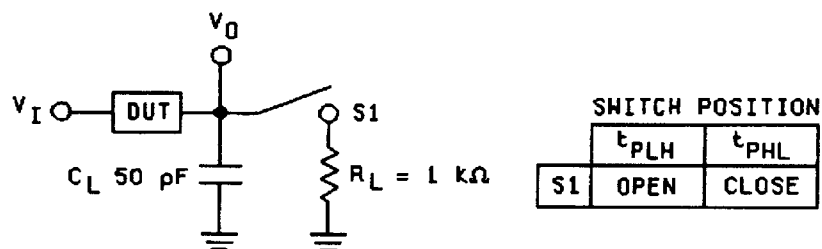
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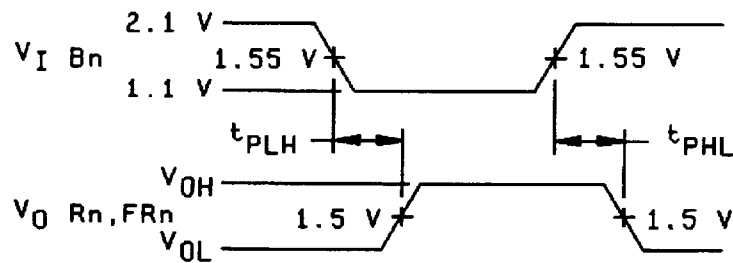
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Driver: \overline{DE} to B_n



Receiver propagation delay set-up



Receiver: B_n to FR_n , B_n to R_n

FIGURE 5. Test circuit and switching waveforms. - continued

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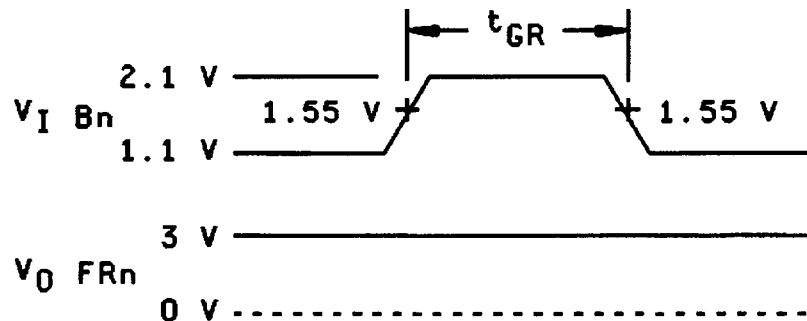
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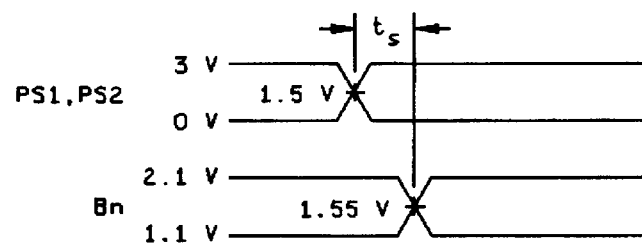
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Receiver: t_{GR} , FR_n (min) = 2 V



Receiver: PS_n to B_n

FIGURE 5. Test circuit and switching waveforms. - continued

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4.2 Screening. For device class M, screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. For device classes Q and V, screening shall be in accordance with MIL-I-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection.

4.2.1 Additional criteria for device class M.

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015.
 - (2) $T_A = +125^{\circ}\text{C}$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein.

4.2.2 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-I-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-I-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015.
- b. Interim and final electrical test parameters shall be as specified in table II herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in appendix B of MIL-I-38535.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-I-38535. Inspections to be performed shall be those specified in MIL-I-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Quality conformance inspection for device class M shall be in accordance with MIL-STD-883 (see 3.1 herein) and as specified herein. Inspections to be performed for device class M shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4). Technology conformance inspection for classes Q and V shall be in accordance with MIL-I-38535 including groups A, B, C, D, and E inspections and as specified herein except where option 2 of MIL-I-38535 permits alternate in-line control testing.

4.4.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. For device class M, subgroups 7 and 8 tests shall be sufficient to verify the truth table. For device classes Q and V, subgroups 7 and 8 shall include verifying the functionality of the device; these tests shall have been fault graded in accordance with MIL-STD-883, test method 5012 (see 1.5 herein).

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table II herein.

4.4.2.1 Additional criteria for device class M. Steady-state life test conditions, method 1005 of MIL-STD-883:

- a. Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005.
- b. $T_A = +125^{\circ}\text{C}$, minimum.
- c. Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE 11. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-STD-883, TM 5005, table I)	Subgroups (in accordance with MIL-I-38535, table III)	
	Device class M	Device class Q	Device class V
Interim electrical parameters (see 4.2)	1	1	1
Final electrical parameters (see 4.2)	1, 2, 3, 7, 8, <u>1</u> / 9, 10, 11	1, 2, 3, 7, <u>1</u> / 8, 9, 10, 11	1,2,3,7,8, <u>2</u> / 9,10,11
Group A test requirements (see 4.4)	1, 2, 3, 7, 8, 9, 10, 11	1, 2, 3, 7, 8, 9, 10, 11	1,2,3,7,8,9, 10,11
Group C end-point electrical parameters (see 4.4)	1, 2, 3	1, 2, 3	1, 2, 3
Group D end-point electrical parameters (see 4.4)	1, 2, 3	1, 2, 3	1, 2, 3
Group E end-point electrical parameters (see 4.4)	1	1	1

1/ PDA applies to subgroup 1.

2/ PDA applies to subgroups 1 and 7.

4.4.2.2 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-I-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB, in accordance with MIL-I-38535, and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table II herein.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels for device classes Q and V shall be M, D, R, and H and for device class M shall be M and D.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. For device class M, the devices shall be subjected to radiation hardness assured tests as specified in MIL-I-38535, appendix A, for the RHA level being tested. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-I-38535 for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at $T_A = +25^\circ\text{C} \pm 5^\circ\text{C}$, after exposure, to the subgroups specified in table II herein.
- c. When specified in the purchase order or contract, a copy of the RHA delta limits shall be supplied.

5. PACKAGING

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5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-STD-883 (see 3.1 herein) for device class M and MIL-I-38535 for device classes Q and V.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.1.2 Substitutability. Device class Q devices will replace device class M devices.

6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-973 using DD Form 1692, Engineering Change Proposal.

6.3 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and which SMD's are applicable to that system. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DESC-EC, telephone (513) 296-6047.

6.4 Comments. Comments on this drawing should be directed to DESC-EC, Dayton, Ohio 45444-5270, or telephone (513) 296-5377.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-I-38535 and MIL-STD-1331.

6.6 One part - one part number system. The one part - one part number system described below has been developed to allow for transitions between identical generic devices covered by the three major microcircuit requirements documents (MIL-H-38534, MIL-I-38535, and 1.2.1 of MIL-STD-883) without the necessity for the generation of unique PIN's. The three military requirements documents represent different class levels, and previously when a device manufacturer upgraded military product from one class level to another, the benefits of the upgraded product were unavailable to the Original Equipment Manufacturer (OEM), that was contractually locked into the original unique PIN. By establishing a one part number system covering all three documents, the OEM can acquire to the highest class level available for a given generic device to meet system needs without modifying the original contract parts selection criteria.

<u>Military documentation format</u>	<u>Example PIN under new system</u>	<u>Manufacturing source listing</u>	<u>Document listing</u>
New MIL-H-38534 Standardized Military Drawings	5962-XXXXXZZ(H or K)YY	QML-38534	MIL-BUL-103
New MIL-I-38535 Standardized Military Drawings	5962-XXXXXZZ(Q or V)YY	QML-38535	MIL-BUL-103
New 1.2.1 of MIL-STD-883 Standardized Military Drawings	5962-XXXXXZZ(M)YY	MIL-BUL-103	MIL-BUL-103

6.7 Sources of supply.

6.7.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in QML-38535. The vendors listed in QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DESC-EC and have agreed to this drawing.

6.7.2 Approved sources of supply for device class M. Approved sources of supply for class M are listed in MIL-BUL-103. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-EC.

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