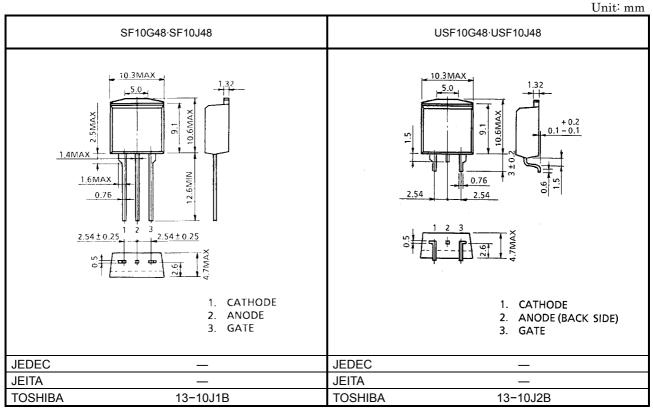


TOSHIBA THYRISTOR SILICON PLANAR TYPE

# SF10G48,SF10J48,USF10G48,USF10J48

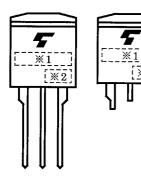
### MEDIUM POWER CONTROL APPLICATIONS

- Repetitive Peak Off-State Voltage : V<sub>DRM</sub> = 400,600V
- Repetitive Peak Reverse Voltage : V<sub>RRM</sub> = 400,600V
- Average On–State Current : I<sub>T</sub> (AV) = 10A
- Gate Trigger Current : I<sub>GT</sub> = 10mA MAX.



Weight: 1.7g

#### MARKING



 $\times 2$ 

	*1	MARK	F10G48	TYPE	SF10G48, USF10G48			
			F10J48	NAME	ASF10J48, USF10J48			
	*2	Lot Number Month (Starting from Alphabet A) Year (Last Decimal Digit of the Current Year)						
L			*`					

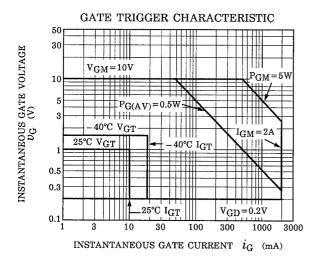
#### **MAXIMUM RATINGS**

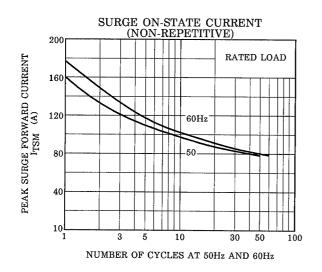
CHARACTERIS	STIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage and	SF10G48 USF10G48	Vdrm Vrrm	400	V	
Repetitive Peak Reverse Voltage	SF10J48 USF10J48		600	v	
Non-Repetitive Peak Reverse Voltage	SF10G48 USF10G48	Ň	500	V	
(Non−Repetitive <5ms, T <sub>j</sub> = 0~125°C)	SF10J48 USF10J48	V <sub>RSM</sub>	720	v	
Average On-State Curre	ent	I <sub>T (AV)</sub>	10	Α	
R.M.S On-State Current		I <sub>T (RMS)</sub>	16	А	
Peak One Cycle Surge (	Dn-State	I <sub>TSM</sub>	160 (50Hz)	А	
Current (Non-Repetitive)			176 (60Hz)	A	
I <sup>2</sup> t Limit Value		l <sup>2</sup> t	125	A <sup>2</sup> s	
Critical Rate of Rise of C Current	n−State (Note 1)	di / dt	100	A / µs	
Peak Gate Power Dissip	ation	P <sub>GM</sub>	5	W	
Average Gate Power Dis	sipation	P <sub>G (AV)</sub>	0.5	W	
Peak Forward Gate Volta	age	V <sub>FGM</sub>	10	V	
Peak Reverse Gate Volt	age	V <sub>RGM</sub>	-5	V	
Peak Forward Gate Curr	ent	I <sub>GM</sub>	2	А	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Ra	ange	T <sub>stg</sub>	-40~125	°C	

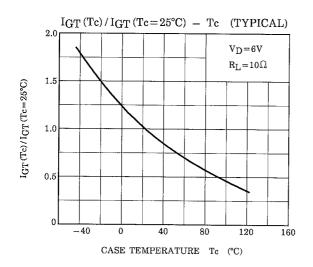
Note 1: V<sub>DRM</sub> = 0.5 × Rated,  $I_{TM} \le 30A$ ,  $t_{gw} \ge 10\mu s$ ,  $t_{gr} \le 250ns$ ,  $i_{gp} = I_{GT} \times 2.0$ 

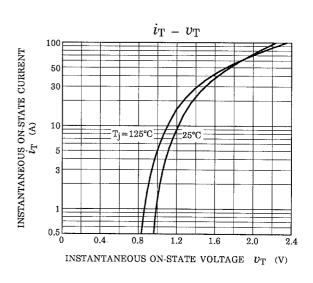
# ELECTRICAL CHARACTERISTICS (Ta = 25°C)

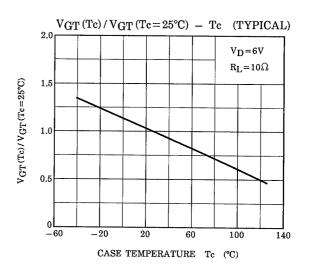
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I <sub>DRM</sub> I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub> = Rated	_	_	10	μA
Peak On-State Voltage	V <sub>TM</sub>	I <sub>TM</sub> = 30A	_		1.5	V
Gate Trigger Voltage	V <sub>GT</sub>	$V_{GT}$ $V_D = 6V, R_I = 10\Omega$		-	1.0	V
Gate Trigger Current			_	_	10	mA
Gate Non-Trigger Voltage	$V_{GD}$	V <sub>D</sub> = Rated × 2 / 3, Tc = 125°C	0.2	_	_	V
Critical Rate of Rise of Off-State Voltage	dv /dt	V <sub>DRM</sub> = Rated, Tc = 125°C Exponential Rise	_	50		V / µs
Holding Current	Ι <sub>Η</sub>	V <sub>D</sub> = 6V, I <sub>TM</sub> = 1A	_	-	40	mA
Latching Current	١L	$V_D$ = 6V, f = 50Hz t <sub>gw</sub> = 50µs, i <sub>G</sub> = 30mA	_	_	50	mA
Thermal Resistance	R <sub>th (j−c)</sub>	Junction to Case, DC	_	_	2.5	°C/W

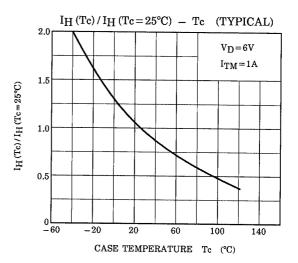


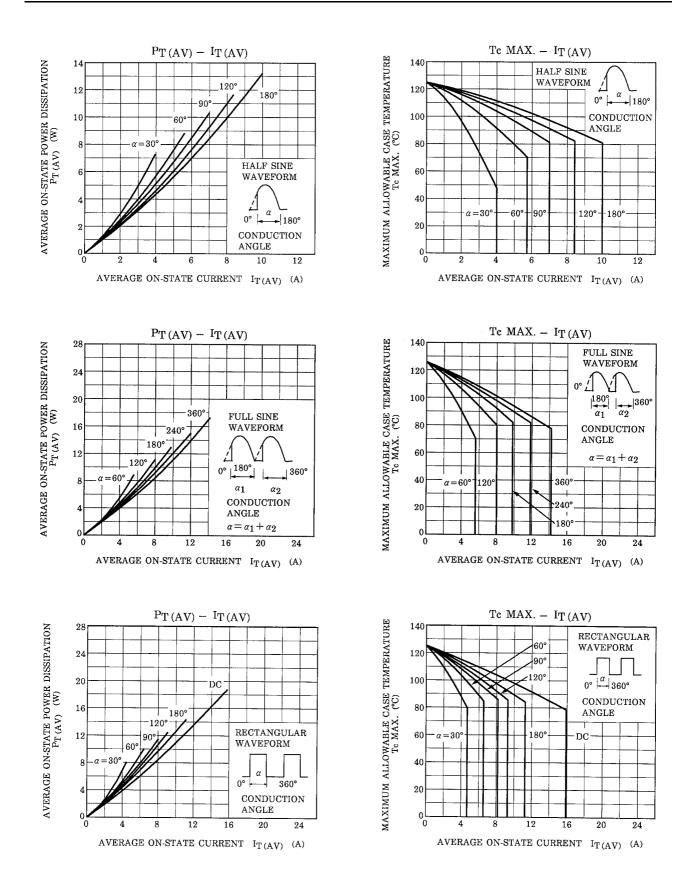


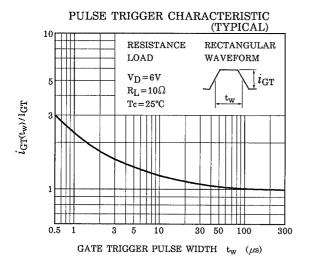


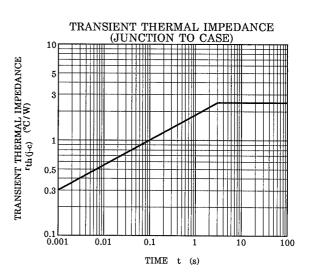












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