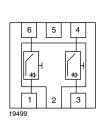
COMPLIANT

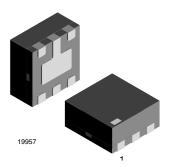
GREEN (5-2008)**



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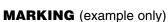
2-Channel EMI-Filter with ESD-Protection





FEATURES

- Ultra compact LLP75-6A package
- 2-channel EMI-filter and ESD-protection
- · Low leakage current
- Line resistance $R_S = 50 \Omega$
- Typical cut off frequency $f_{3dB} = 100 \text{ MHz}$
- ESD-protection acc. IEC 61000-4-2
 - ± 30 kV contact discharge
 - ± 30 kV air discharge
- e3 Sn
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC





Dot = pin 1 marking

YY = type code (see table below)

XX = date code

ORDERING INFORMATION				
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY	
VEMI255A-HS3	VEMI255A-HS3-GS08	3000	15 000	
VEMI255A-HS3	VEMI255A-HS3-GS08	10 000	10 000	

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VEMI255A-HS3	LLP75-6A	T1	5 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	All I/O pin to pin 2; acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	I _{PPM}	4	Α	
ESD immunity	Contact discharge acc. IEC61000-4-2; 10 pulses	V	± 30	- kV	
	Air discharge acc. IEC61000-4-2; 10 pulses	V_{ESD}	± 30		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T _{STG}	- 55 to + 150	°C	

^{**} Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

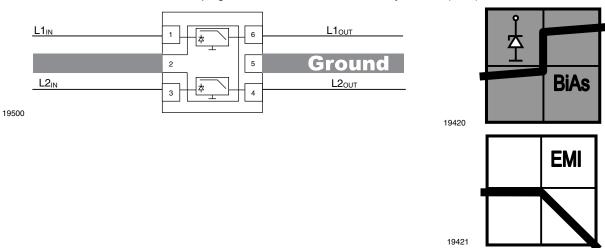
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2-Channel EMI-Filter with ESD-Protection



APPLICATION NOTE

With the VEMI255A-HS3 2 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behavior is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).

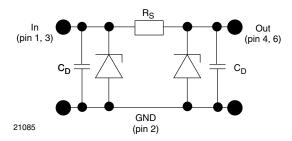


The 2 independent EMI-filter are placed between

pin 1 and pin 6, and pin 3 and pin 4.

They all are connected to the common ground pin 2. Pin 5 is internally not connected. Each filter is symmetrical so that all ports (pin 1, 3, 4, and 6) can be used as input or output.

The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level (V_{BR}) and the diode capacitance (C_D). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance R_S between input and output the device works as a low pass filter. Low frequency signals ($f < f_{3dB}$) pass the filter while high frequency signals ($f > f_{3dB}$) will be shorted to ground through the diode capacitances C_D .



Each filter is symmetrical so that both ports can be used as input or output.



2-Channel EMI-Filter with ESD-Protection

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ELECTRICAL CHARACTERISTICS VEMI255A-HS3							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of channels which can be protected	N _{channel}	-	-	2	channel	
Reverse stand off voltage	at I _R = 1 μA each input to pin 2	V_{RWM}	5	-	-	V	
Reverse current	at V _R = 5 V each input to pin 2	I _R	-	-	1	μΑ	
Reverse break down voltage	Each input to pin 2 at I _R = 1 mA	V_{BR}	6	-	-	V	
Pos. clamping voltage	at I _{PP} = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-	-	7.8	V	
	at $I_{PP} = I_{PPM} = 4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-	-	8	V	
Neg. clamping voltage	at I _{PP} = - 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	- 1	-	-	V	
	at I _{PP} = I _{PPM} = - 4 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	- 1.2	-	-	V	
Input capacitance	at V _R = 0 V; f = 1 MHz	C _{IN}	-	60	-	pF	
	at V _R = 2.5 V; f = 1 MHz	C _{IN}	-	37	-	pF	
ESD-clamping voltage	at ± 30 kV ESD-pulse acc. IEC 61000-4-2	V _{CESD}	-	7.5	-	V	
Line resistance	Measured between input and output; I _S = 10 mA	R _S	45	50	55	Ω	
Cut-off frequency	V_{IN} = 0 V; measured in a 50 Ω system	f _{3dB}	-	100	-	MHz	

Note

Ratings at 25 $^{\circ}$ C, ambient temperature unless otherwise specified.

TYPICAL CHARACTERISTICS

 T_{amb} = 25 °C, unless otherwise specified

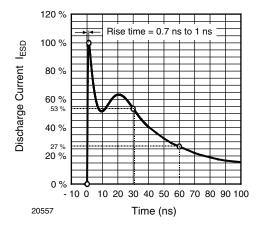


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 $\Omega \text{/}150 \text{ pF})$

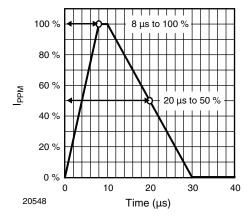


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

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2-Channel EMI-Filter with ESD-Protection



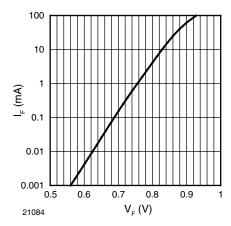


Fig. 3 - Typical Forward Current I $_{\text{F}}$ vs. Forward Voltage V_{F}

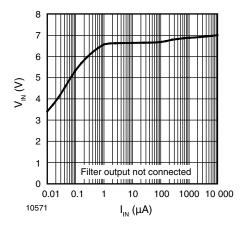


Fig. 4 - Typical Input Voltage V_{IN} vs. Input Current I_{IN}

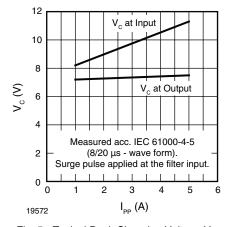


Fig. 5 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

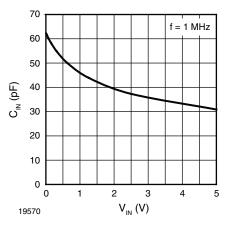


Fig. 6 - Typical Input Capacitance C_{IN} vs. Input Voltage V_{IN}

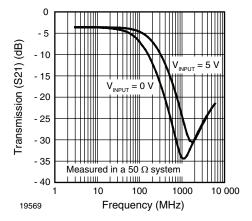


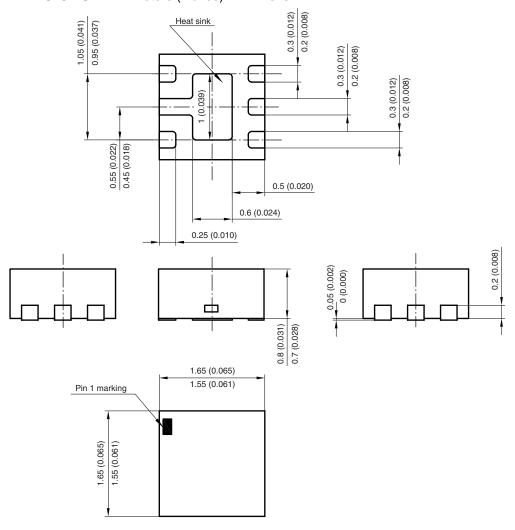
Fig. 7 - Typical Small Signal Transmission (S21) at $\,$ Z $_{O}$ = 50 $\,$ Ω



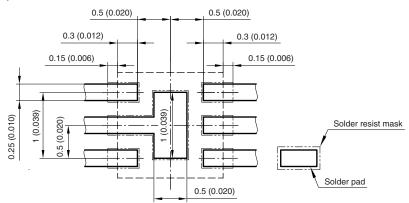
2-Channel EMI-Filter with ESD-Protection

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PACKAGE DIMENSIONS in millimeters (inches): LLP75-6A



Foot print recommendation:



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