# **Power MOSFET**

# -20 V, -400 mA, P-Channel SOT-23 Package

#### **Features**

• Low R<sub>DS(on)</sub> Provides Higher Efficiency and Extends Battery Life

 $R_{DSon} = 0.80 \Omega, V_{GS} = -10 V$ 

 $R_{DSon} = 1.10 \ \Omega, \ V_{GS} = -4.5 \ V$ 

- Miniature SOT-23 Surface Mount Package Saves Board Space
- Pb-Free Package is Available

#### **Applications**

- DC-DC Converters
- Computers
- Printers
- PCMCIA Cards
- Cellular and Cordless Telephones

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-to-Source Voltage - Continuous	V <sub>GS</sub>	± 20	V
Continuous Drain Current @ $T_A = 25$ °C Pulsed Drain Current ( $t_p \le 10 \mu s$ )	I <sub>D</sub> I <sub>DM</sub>	-0.4 -1.0	Α
Total Power Dissipation @ T <sub>A</sub> = 25°C (Note 1)	P <sub>D</sub>	225	mW
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C
Thermal Resistance – Junction–to–Ambient	$R_{\theta JA}$	556	°C/W
Source Current (Body Diode)	I <sub>S</sub>	0.4	Α
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 s	TL	260	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

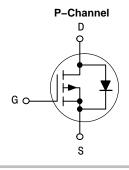
1. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2%.



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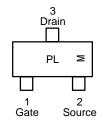
V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> TYP	I <sub>D</sub> MAX
–20 V	550 mΩ @ –10 V	–400 mA



#### MARKING DIAGRAM/ **PIN ASSIGNMENT**



SOT-23 **CASE 318** STYLE 21



= Specific Device Code PL

Μ = Date Code

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTR0202PLT1	SOT-23	3000 Tape & Reel
NTR0202PLT1G	SOT-23 (Pb-Free)	3000 Tape & Reel
NTR0202PLT3	SOT-23	10,000 Tape & Reel
NTR0202PLT3G	SOT-23 (Pb-Free)	10,000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic			Min	Тур	Max	Unit
OFF CHARACTERISTICS						_
Drain-to-Source Breakdown Voltage $(V_{GS}=0\ V,\ I_D=-10\ \mu A)$ (Positive Temperature Coefficient)		V <sub>(BR)DSS</sub>	-20	33		V mV/°C
Zero Gate Voltage Drain Current (V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 25°C) (V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150°C)		I <sub>DSS</sub>			-1.0 -10	μΑ
Gate–Body Leakage Current (V <sub>GS</sub> = ± 20 V, V <sub>DS</sub> = 0 V)					±100	nA
ON CHARACTERISTICS (Note 2)						_
Gate Threshold Voltage $ (V_{DS} = V_{GS}, \ I_D = -250 \ \mu A) $ (Negative Temperature Coefficient)		V <sub>GS(th)</sub>	-1.1	-1.9 3.0	-2.3	V mV/°C
Static Drain–to–Source On–Resistance $(V_{GS} = -10 \text{ V}, I_D = -200 \text{ mA})$ $(V_{GS} = -4.5 \text{ V}, I_D = -50 \text{ mA})$				0.55 0.80	0.80 1.10	Ω
Forward Transconductance (V <sub>DS</sub> = -10 V, I <sub>D</sub> = -200 mA)				0.5		Mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C <sub>iss</sub>		70		pF
Output Capacitance	$(V_{DS} = -5.0 \text{ V}, V_{GS} = 0 \text{ V}, F = 1.0 \text{ MHz})$	C <sub>oss</sub>		74		
Reverse Transfer Capacitance		C <sub>rss</sub>		26		
SWITCHING CHARACTERISTICS (N	lote 3)					
Turn-On Delay Time		t <sub>d(on)</sub>		3.0		ns
Rise Time	$(V_{DD} = -15 \text{ V}, I_D = -200 \text{ mA},$	t <sub>r</sub>		6.0		
Turn-Off Delay Time	$V_{GS} = -10 \text{ V}, R_G = 6.0 \Omega$	t <sub>d(off)</sub>		18		
Fall Time		t <sub>f</sub>		4		
Total Gate Charge		Q <sub>TOT</sub>		2.18		nC
Gate-Source Charge	$(V_{DS} = -15 \text{ V}, I_D = -200 \text{ mA}, V_{GS} = -10 \text{ V})$	Q <sub>GS</sub>		0.41		
Gate-Drain Charge	33 - /	$Q_{GD}$		0.40		
BODY-DRAIN DIODE CHARACTER	ISTICS (Note 2)					_
Diode Forward Voltage (Note 2) $ \begin{array}{c} (I_S = -400 \text{ mA},  V_{GS} = 0 \text{ V}) \\ (I_S = -400 \text{ mA},  V_{GS} = 0 \text{ V},  T_J = 150^{\circ}\text{C}) \end{array} $		V <sub>SD</sub>		-0.8 -0.65	-1.0	V
Reverse Recovery Time	$(I_S = -1.0 \text{ A}, V_{GS} = 0 \text{ V},$ $dI_S/dt = 100 \text{ A/}\mu\text{s})$	t <sub>rr</sub>		11.8		ns
		ta		9		
	a.g. a	t <sub>b</sub>		3		
Reverse Recovery Stored Charge	$(I_S = -1.0 \text{ A}, V_{GS} = 0 \text{ V}, \\ dI_S/dt = 100 \text{ A}/\mu\text{s})$	Q <sub>RR</sub>		0.007		μC

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

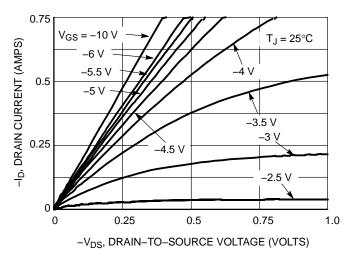


Figure 1. On-Region Characteristics

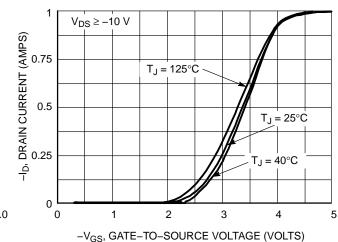


Figure 2. Transfer Characteristics

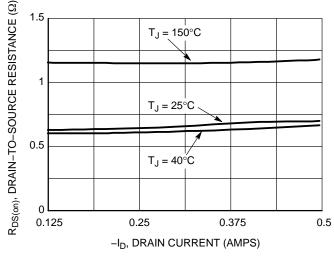


Figure 3. On-Resistance versus Drain Current

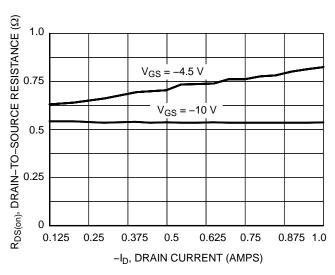


Figure 4. On–Resistance versus Drain Current and Gate Voltage

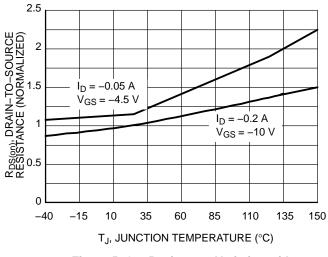


Figure 5. On–Resistance Variation with Temperature

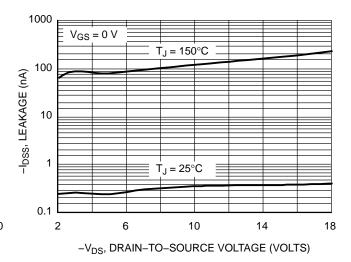
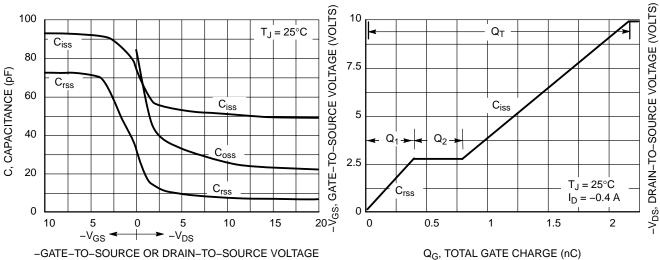


Figure 6. Drain-to-Source Leakage Current versus Voltage



-GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge

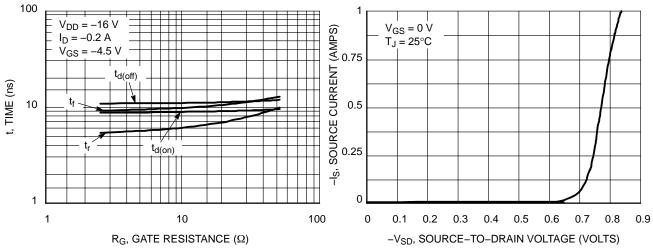
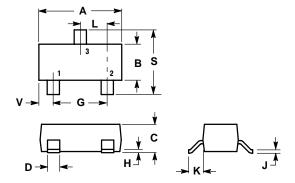


Figure 9. Resistive Switching Time Variation versus Gate Resistance

Figure 10. Diode Forward Voltage versus Current

#### **PACKAGE DIMENSIONS**

SOT-23 (TO-236)CASE 318-08 **ISSUE AK** 



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF PASE MATERIAL
- BASE MATERIAL.
  4. 318–01 THRU –07 AND –09 OBSOLETE, NEW STANDARD 318–08.

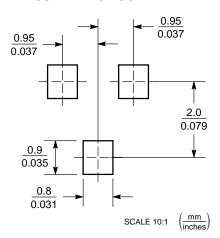
	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.1102	0.1197	2.80	3.04	
В	0.0472	0.0551	1.20	1.40	
С	0.0350	0.0440	0.89	1.11	
D	0.0150	0.0200	0.37	0.50	
G	0.0701	0.0807	1.78	2.04	
Н	0.0005	0.0040	0.013	0.100	
J	0.0034	0.0070	0.085	0.177	
K	0.0140	0.0285	0.35	0.69	
L	0.0350	0.0401	0.89	1.02	
S	0.0830	0.1039	2.10	2.64	
V	0.0177	0.0236	0.45	0.60	

STYLE 21:

PIN 1. GATE 2. SOURCE

- 3. DRAIN

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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