



Fast CMOS 3.3V 16-Bit Octal Buffer/Line Drivers

Product Features:

- Supports Mixed Signal Mode Operation
 - 5 Volt Input.
 - 5 Volt Output (when connected to a 5 Volt Bus).
 - Can serve as a 5 volt to 3 volt translator.
- Advanced Low Power CMOS Operation.
- Low Standby Current (Low power CMOS, not Bi-CMOS, so output drive transistors do not require bipolar standby current levels). Typical standby power 1 mW.
- Excellent output drive capability: Balanced drives (24 mA sink and source). Compatible with LVC™ class of products.
- Pin and functional compatible: Industry standard double-density pinouts.
- Low ground bounce outputs, hysteresis on all inputs.
- ESD Protection exceeds 2000 volts.
- Packaged in 48-pin plastic TSSOP and SSOP

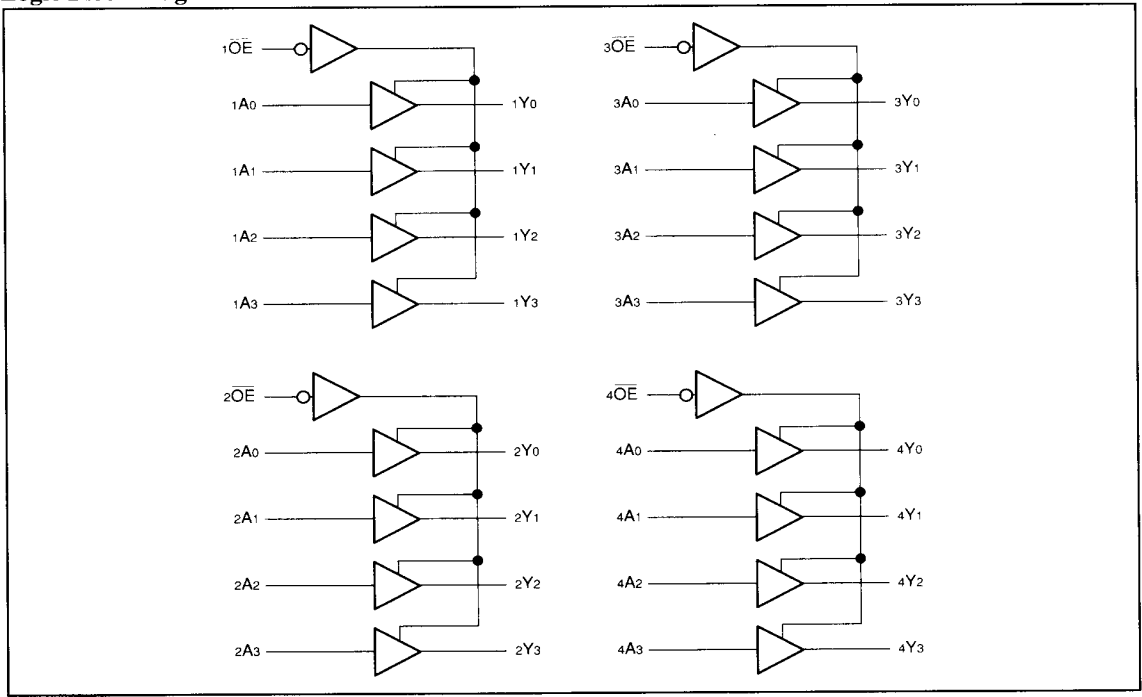
Product Description:

Pericom Semiconductor's PI74FCT series of logic circuits are produced in the Company's advanced 0.8 micron CMOS technology, achieving industry leading speed grades.

The PI74FCT163244 is a 16-bit buffer/line driver designed for applications driving high capacitance loads and low impedance backplanes. This high-speed, low power device offers bus/backplane interface capability and a flow-through organization for ease of board layout. This device is designed with three-state controls to operate in a Quad-Nibble, Dual-Byte, or a single 16-bit word mode.

The PI74FCT163244 can be driven from either 3.3 V or 5.0 V devices allowing this device to be used as a translator in a mixed 3.3/5.0 V system.

All products are available in 48-pin 240 mil wide plastic TSSOP and 300 mil wide plastic SSOP packages.

Logic Block Diagram

Product Pin Description

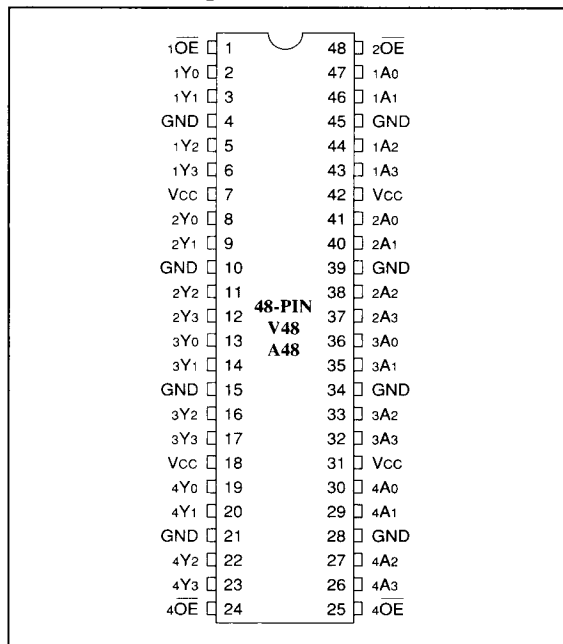
Pin Name	Description
xOE	3-State Output Enable Inputs (Active LOW)
xAx	Inputs
xYx	3-State Outputs
GND	Ground
Vcc	Power

Truth Table

Inputs ⁽¹⁾		Outputs ⁽¹⁾
xOE	xAx	xYx
L	L	L
L	H	H
H	X	Z

Note:

1. H = High Voltage Level, X = Don't Care,
L = Low Voltage Level, Z = High Impedance

Product Pin Configuration

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Capacitance ($T_A = 25^\circ\text{C}$, $f = 1\text{ MHz}$)

Parameters ⁽¹⁾	Description	Test Conditions	Typ	Max.	Units
C _{IN}	Input Capacitance	V _{IN} = 0 V	4.5	6	pF
C _{OUT}	Output Capacitance	V _{OUT} = 0 V	5.5	8	pF

Note:

1. This parameter is determined by device characterization but is not production tested.

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-55°C to +125°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5V to Vcc
DC Input Voltage	-0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	1.0W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, TA = -40°C to +85°C, VCC = 3.3V ± 0.3V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage (Input pins)	Guaranteed Logic HIGH Level	2.2		5.5	V
	Input HIGH Voltage (I/O pins)		2.0		5.5	V
V _{IL}	Input LOW Voltage (Input and I/O pins)	Guaranteed Logic LOW Level	-0.5		0.8	V
I _{IH}	Input HIGH Current (Input pins)	VCC = Max. VIN = 5.5V			±5	µA
	Input HIGH Current (I/O pins)	VCC = Max. VIN = VCC			15	µA
I _{IL}	Input LOW Current (Input pins)	VCC = Max. VIN = GND			±5	µA
	Input LOW Current (I/O pins)	VCC = Max. VIN = GND			15	µA
I _{OZH}	High Impedance Output Current	VCC = Max. VOUT = VCC			10	µA
I _{OZL}	(3-State Output pins)	VCC = Max. VOUT = GND			10	µA
V _{IK}	Clamp Diode Voltage	VCC = Min., I _{IN} = -18 mA			-1.2	V
I _{ODH}	Output HIGH Current	VCC = 3.3V, VIN = V _{IH} or V _{IL} , V _O = 1.5V ⁽³⁾	-36		-110	mA
I _{ODL}	Output LOW Current	VCC = 3.3V, VIN = V _{IH} or V _{IL} , V _O = 1.5V ⁽³⁾	50		200	mA
V _{OH}	Output HIGH Voltage	VCC = Min. I _{OH} = -0.1mA VIN = V _{IH} or V _{IL}	VCC-0.2			V
		I _{OH} = -8mA	2.4			V
		I _{OH} = -24mA	2.0			V
V _{OL}	Output LOW Voltage	VCC = Min. I _{OL} = 0.1mA VIN = V _{IH} or V _{IL}			0.2	V
		I _{OL} = 16mA			0.4	V
		I _{OL} = 24mA			0.5	V
I _{OS}	Short Circuit Current ⁽⁴⁾	VCC = Max. ⁽³⁾ , VOUT = GND	-60	-135	-240	mA
I _{OFF}		VCC = 0V, VIN or VOUT = 4.5V			100	µA
V _H	Input Hysteresis			150		mV
I _{CC1}	Quiescent Power Supply Current	VCC = Max., VIN = GND or VCC			1.5	mA
I _{CCH}						
I _{CCZ}						

Notes:

- For conditions show as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at VCC = 3.3, +25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- This parameter is guaranteed but not tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
ΔI_{CC}	Quiescent Power Supply Current TTL Inputs HIGH	$V_{CC} = \text{Max.}$	$V_{IN} = V_{CC} - 0.6^{(3)}$		2.0	30	μA
			$V_{IN} = 2.4 \text{ V}^{(3)}$		70	500	μA
I_{CCD}	Dynamic Power Supply ⁽⁴⁾	$V_{CC} = \text{Max.}$, Outputs Open $\overline{xOE} = \text{GND}$ One Bit Toggling 50% Duty Cycle	$V_{IN} = V_{CC}$ $V_{IN} = \text{GND}$		50	75	$\mu A/\text{MHz}$
I_C	Total Power Supply Current ⁽⁶⁾	$V_{CC} = \text{Max.}$, Outputs Open $f_i = 10 \text{ MHz}$ 50% Duty Cycle $\overline{xOE} = \text{GND}$ One Bit Toggling	$V_{IN} = V_{CC} - 0.6 \text{ V}$ $V_{IN} = \text{GND}$		0.6	2.3	mA
			$V_{IN} = 2.4 \text{ V}$ $V_{IN} = \text{GND}$		0.6	2.5	
		$V_{CC} = \text{Max.}$, Outputs Open $f_i = 2.5 \text{ MHz}$ 50% Duty Cycle $\overline{xOE} = \text{GND}$ 16 Bits Toggling	$V_{IN} = V_{CC} - 0.6 \text{ V}$ $V_{IN} = \text{GND}$		2.1	4.7 ⁽⁵⁾	
			$V_{IN} = 2.4 \text{ V}$ $V_{IN} = \text{GND}$		2.6	8.5 ⁽⁵⁾	

Notes:

- For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at $V_{CC} = 3.3 \text{ V}$, $+25^\circ\text{C}$ ambient.
- Per TTL driven input ($V_{IN} = 3.4 \text{ V}$); all other inputs at V_{CC} or GND .
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I_{CC} formula. These limits are guaranteed but not tested.
- $I_C = I_{\text{QUIESCENT}} + I_{\text{INPUTS}} + I_{\text{DYNAMIC}}$
 $I_C = I_{CC} + \Delta I_{CC} D_{HNT} + I_{CCD} (f_{CP}/2 + f_i N_i)$
 $I_{CC} = \text{Quiescent Current (ICCL, ICCH and ICCZ)}$
 $\Delta I_{CC} = \text{Power Supply Current for a TTL High Input}$
 $D_H = \text{Duty Cycle for TTL Inputs High}$
 $N_T = \text{Number of TTL Inputs at } D_H$
 $I_{CCD} = \text{Dynamic Current Caused by an Input Transition Pair (HLH or LHL)}$
 $f_{CP} = \text{Clock Frequency for Register Devices (Zero for Non-Register Devices)}$
 $N_{CP} = \text{Number of Clock Inputs at } f_{CP}$
 $f_i = \text{Input Frequency}$
 $N_i = \text{Number of Inputs at } f_i$
All currents are in milliamps and all frequencies are in megahertz.

Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	FCT163244T		FCT163244AT		Unit
			Com.		Com.		
			Min	Max	Min	Max	
tPLH	Propagation Delay	CL = 50 pF RL = 500Ω	1.5	6.5	1.5	4.8	ns
tPHL	xAX to xYx						
tpZH	Output Enable Time		1.5	8.0	1.5	6.2	ns
tPZL							
tPHZ	Output Disable Time		1.5	7.0	1.5	5.6	ns
tPLZ							
tSK(o)	Output Skew ⁽³⁾			0.5		0.5	ns

Notes:

1. See test circuit and wave forms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.