# SPECIFICATION FOR LCD MODULE

Model No. TM9632ECIWG

Prepared by: Date: Checked by: Date: Verified by: Date: Approved by: Date:

TIANMA MICROELECTRONICS CO., LED

## **REVISION RECORD**

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

#### 1. General Specifications:

1.1 Display type: FSTN

1.2 Display color\*:

Display color: Blue-Black

Background: White

1.3 Polarizer mode: Transflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/32 Duty 1/6 Bias

1.6 Lcd operating voltage: 7.5V

1.7Controller: S6B0715A11-B0CZ (KS0715UM-H0CC)

1.8 Without Backlight

1.9 Data Transfer: Serial

1.10 Operating Temperature: -20----+70 ℃

Storage Temperature: -30----+80 ℃

1.11 Outline Dimensions: Refer to outline drawing on next page

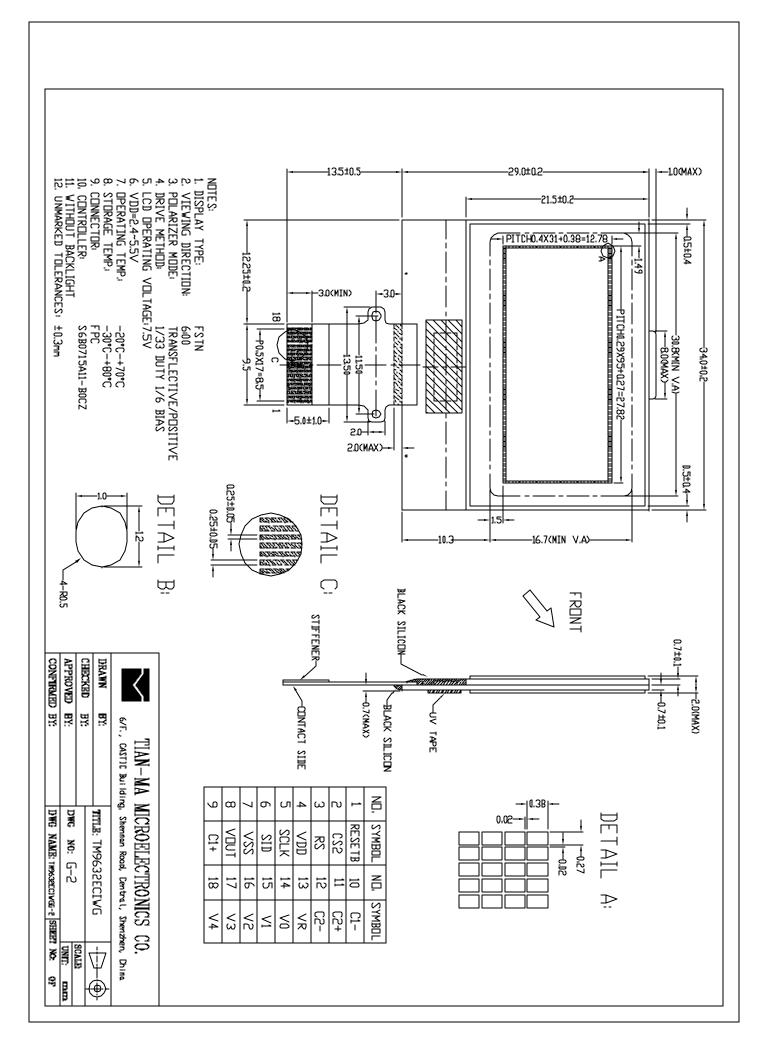
1.12 Dot Matrix: 96X 32 DOTS

1.13 Dot Size: 0.27X0.38(mm)

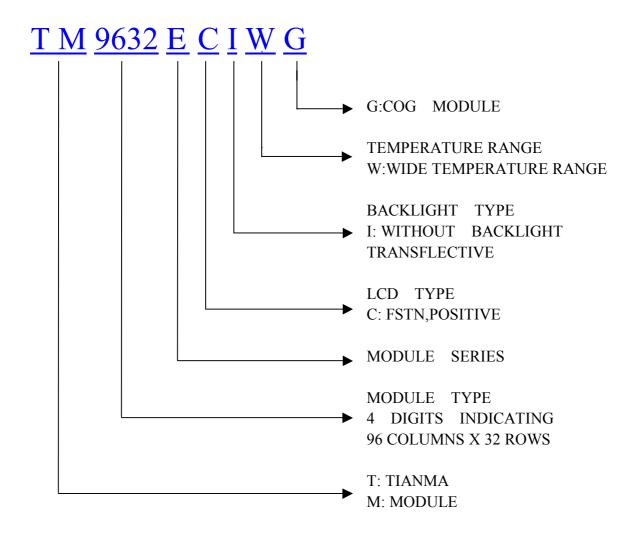
1.14 Dot Pitch: 0.29X0.40 (mm)

1.15 Weight: 15g (Approx.)

<sup>\*</sup> Color tone is slightly changed by temperature and driving voltage.



#### 3. LCD Module Part Numbering System



# **5. Absolute Maximum Ratings**

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	7.0	V	
LCD Driving Voltage	VLCD	-0.3	13.0	v	
Operating Temperature Range	Тор	-20	+70	°C	No
Storage Temperature Range	Тѕт	-30	+80		Condensation

# **6. Electrical Specifications and Instruction Code**

# 6.1 Electrical characteristics

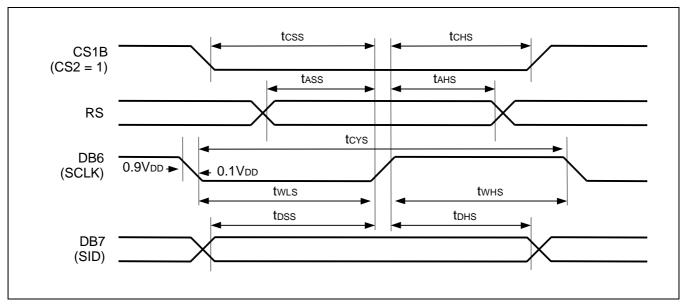
Iter	n	Symbol	mbol Min. Typ. Max.			
Supply Voltage (Logic)		V <sub>DD</sub> -V <sub>SS</sub>	2.4	3.0	3.6	V
Supply V (LCD D	•	Vlcd	1	7.5	-	V
Input	High	V <sub>IH</sub> (V <sub>DD</sub> =3.0)	$0.8\mathrm{V}_\mathrm{DD}$	-	$V_{ m DD}$	V
Signal Voltage Low	Low	$V_{\text{\tiny IL}}$ $(V_{\text{DD}}=3.0)$	0	-	$0.2V_{\mathrm{DD}}$	V
Supply c		$I_{DD}$ $(VDD-Vss = 3.0)$	-	70	100	uA

# 6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	RESETB	H/L	Reset signal input
2	CS2	H/L	Chip select input
3	RS	H/L	Selects registers input
4	VDD	3.0V	Power supply voltage for logic
5	SCLK	H/L	Serial Input clock
6	SID	H/L	Serial input data
7	VSS	0V	Ground
8	VOUT	-	Voltage convert I/O port
9	<b>C</b> 1+	-	Capacitor pin for voltage converter
10	C1-	-	Capacitor pin for voltage converter
11	C2+	-	Capacitor pin for voltage converter
12	C2-	-	Capacitor pin for voltage converter
13	VR	H/L	V0 voltage adjustment pin
14	V0	7.5v	Power supply voltage for LCD
15	V1	5.9v	Power supply voltage for LCD
16	V2	4.7v	Power supply voltage for LCD
17	V3	2.3v	Power supply voltage for LCD
18	V4	1.2v	Power supply voltage for LCD

# 6.3 Interface Timing Chart

#### **Serial Interface Characteristics**



**Figure 33. Serial Interface Characteristics** 

 $(VDD = 2.4 \text{ to } 3.6V, Ta = -40 \text{ to } +85^{\circ}C)$ 

Item	Signal	Symbol	Min	Тур	Max	Unit	Remark
Serial clock cycle SCLK high pulse width SCLK low pulse width	DB6 (SCLK)	tCYS tWHS tWLS	450 180 135	- - -	- - -	ns	
Address setup time Address hold time	RS	tass tahs	90 360	-	-	ns	
Data setup time Data hold time	DB7 (SID)	tdss tdhs	90 90	-	-	ns	
CS1B setup time CS1B hold time	CS1B	tcss tchs	55 180			ns	

## 6.4 Instruction Code

#### x: Don't care

Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description X : Doint care
Read display data	1	1	Read data							Read data from DDRAM	
Write display data	1	0		Write data							Write data into DDRAM
Read status	0	1	BUSY	ADC	ONOFF	RESETB	0	0	0	0	Read the internal status
Display ON / OFF	0	0	1	0	1	0	1	1	1	DON	Turn ON / OFF LCD panel When DON = 0: display OFF When DON = 1: display ON
Initial display line	0	0	0	1	ST5	ST4	ST3	ST2	ST1	ST0	Specify DDRAM line for COM0
Set reference voltage mode	0	0	1	0	0	0	0	0	0	1	Set reference voltage mode
Set reference voltage register	0	0	1	0	0	SV4	SV3	SV2	SV1	SV0	Set reference voltage register
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address
Set column address MSB	0	0	0	0	0	1	0	Y6	Y5	Y4	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB
ADC select	0	0	1	0	1	0	0	0	0	ADC	Select SEG output direction When ADC = 0: normal direction (SEG0→SEG99) When ADC = 1: reverse direction (SEG99→SEG0)
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	Select normal / reverse display When REV = 0: normal display When REV = 1: reverse display
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	Select normal/ entire display ON When EON = 0: normal display. When EON = 1: entire display ON
LCD bias select	0	0	1	0	1	0	0	0	1	BIAS	Select LCD bias
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions
SHL select	0	0	1	1	0	0	SHL	×	×	×	Select COM output direction When SHL = 0: normal direction (COM0→COM31) When SHL = 1: reverse direction (COM31→COM0)
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation
Set static indicator register	0	0	1	0	1	0	1	1	0	SI	Set static indicator register SI = 0 (OFF), SI = 1 (ON)
Power save	-	-	-	-	-	-	-	-	-	-	Compound instruction of display OFF and entire display ON
Test instruction	0	0	1	1	1	1	×	×	×	×	Don't use this instruction.

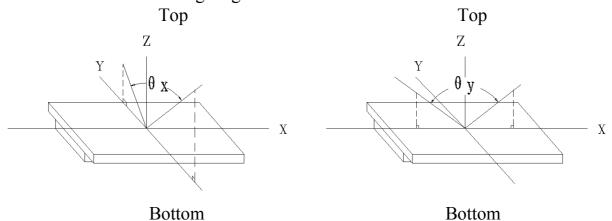
# 7. Optical Characteristics

7.1 Optical Characteristics

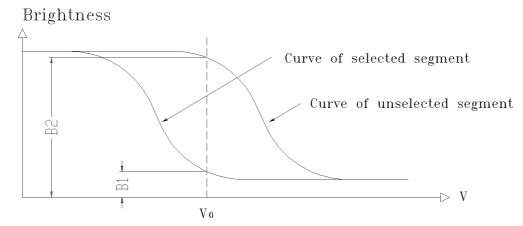
$T_{\alpha}$	-25	$\sim$
ra-	-23	$\cup$

F								
Item		Symbol	Condition		Min.	Тур.	Max.	Unit
Viewing Angle		$\theta_{\!\scriptscriptstyle { m X}}$	C > 2	θ <sub>y</sub> =0°	-35	5 20	Dag	
		θу	Cr≥2	θ <sub>x</sub> =0°	-30	Deg		
Contrast 1	Contrast Ratio $C_r$ $\theta_x = 0$ $\theta_y = 0$		=0°	4	1	-		
Response	Turn on	Ton	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		-	-	250	mg
Time	Turn off	Toff	$\theta_{y}$ =	=0°	-	-	250	ms

- 7.2 Definition of Optical Characteristics
- 7.2.1 Definition of Viewing Angle



#### 7.2.2 Definition of Contrast Ratio



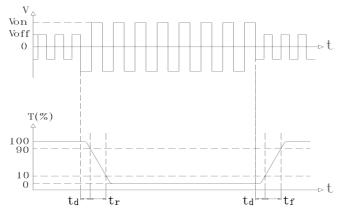
Contrast Ratio =  $B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$ 

Measuring Conditions:

1) Operating Voltage: 25°C;

2) Frame frequency: 70.0Hz

## 7.2.3 Definition of Response time



Turn on time:  $t_{on} = t_d + t_r$ 

Turn off time:  $t_{off} = t_d + t_f$ 

Measuring Condition:

1) Operating Voltage: 7.0V

2) Frame frequency: 70.0Hz

# 8. Reliability

8.1 Content of Reliability Test

Ta=25°C

No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	80°C
	Storage	storage temperature for a long time	240H
2	Low Temperature	Endurance test applying the low	-30°C
	Storage	storage temperature for a long time	240H
	High Temperature	Endurance test applying the electric	
3	Operation	stress (voltage & current) and the	70°C
		thermal stress to the element for a	240H
		long time	
	Low Temperature	Endurance test applying the electric	-20°C
4	Operation	stress under low temperature for a	240H
	High Tanananatan	long time	60°C
_	High Temperature	Endurance test applying the high	60°C
5	/Humidity Storage	temperature and high humidity	95%RH
	T	storage for a long time	240H
	Temperature Cycle	Endurance test applying the low and high temperature cycle	-30°C/80°C
	Cycle	$-30^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C} \longleftrightarrow 80^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C}$	10 cycles
6		30min 5min 30min 5min	- 5 5 5 5 5 5 5
		←───	
		1 cycle	
	Vibration Test	Endurance test applying the	10Hz~500Hz,
7	(package state)	vibration during transportation	$100 \text{m/s}^2$ ,
	G1 1 T		120min
0	Shock Test	Endurance test applying the shock	Half-sinewave,
8	(package state)	during transportation	$300 \text{m/s}^2$ ,
	Atmagnharia	Endurance test anniving the	18ms
9	Atmospheric Pressure Test	Endurance test applying the	25kPa
)	riessuie iest	atmospheric pressure during transportation by air	16H
		transportation by an	

# 8.2 Failure Judgment Criterion

Criterion			Te	est l	Iten	n N	0.			Failure Judgement Criterion	
Item	1	2	3	4	5	6	7	8	9	randre Judgement Criterion	
Basic Specification	√	√	√	<b>√</b>	√	√	√	√	√	Out of the basic Specification	
Electrical specification	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>					Out of the electrical specification	
Mechanical Specification							<b>√</b>	<b>√</b>		Out of the mechanical specification	
Optical Characteristic	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>			<b>√</b>	Out of the optical specification	
Note	For test item refer to 8.1										
Remark	Basic specification = Optical specification + Mechanical specification										

## 9. QUALITY LEVEL

Examination	At T <sub>amb</sub> =25°C	Inspection						
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL		
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See ann	nex A		II	Major 1.0 Minor 2.5		
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See ann	nex B		II	Major 1.0 Minor 2.5		

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

#### 10. Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature:  $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$ 

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

**Appendix A**Inspection items and criteria for appearance defects

Items	Contents		Criteria				
Protective Glue		No clear defects					
Cover Tape		Covering all o	of the c	chip and no clear co	rimple		
Leakage		Not permitted					
Rainbow		According to	the lin	nit specimen			
	Wrong polarizer attachment	Not permitted					
Polarizer	Bubble between	Not counted		Max. 3 defects allowed			
	polarizer and glass	ф<0.3mm		0.3mm≤¢≤0.5r	nm		
	Scratches of polarizer	According to	nit specimen				
Black spot		Not counted	Max	. 3 spots allowed			
(in viewing area)		X<0.20mm	0.201	mm≤X≤0.5mm	Max. 3		
arca)	0	X=(a+b)/2		spots (lines)			
Black line (in viewing		Not counted	Max	. 3 lines allowed	allowed		
area)	0	a<0.02mm	0.021	mm≤a≤0.05mm			
				b≤2.0mm			
Progressive cracks	Not permitted						

Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents	Criteria					
	Cracks on pads	a	b		с	Max. 2	
		≤3mm	≪W	V/5 ≤T/2		Cracks	
	h b ->/-	≤2mm	≪W	1/5	T/2 <c<t< td=""><td>allowed</td><td rowspan="2"></td></c<t<>	allowed	
	Cracks on contact side	a			b		
		≤3mm		≤T/2			
Glass Cracks		≤2m	m	7	Γ/2 <b<t< td=""><td></td><td></td></b<t<>		
		C shall be not reach the seal area			Max. 2 cracks	Max. 5 cracks allowed	
	Cracks on non-contact side	a			b	allowed	
	Q C Q C Q C Q C Q C Q C Q C Q C Q C Q C	≤3m	≤3mm ≤T/2		≤T/2		
		≤2m	m	7	Γ/2 <b<t< td=""><td colspan="2"></td></b<t<>		
		C≤0.5mm					
		d≤SW/3					
	Corner cracks	e<2.0mm <sup>2</sup> f<2.0mm <sup>2</sup>			Max. 3 cracks allowed		
	f-A						

**Appendix B**Inspection items and criteria for display defects

Items		Contents	Criteria			
Open segment or open common			Not permitted			
Short			Not permitted			
Wrong viewing angle			Not permitted			
Contrast radio uneven			According to the limit specimen			
Crosstalk			According to the limit specimen			
	- <del> </del>  -a		Not counted	Max.3 dots allowed		
		X<0.1mm	0.1mm≤X≤0.2mm			
Pin holes		X=(a+b)/2	Max.3 dots			
and cracks in segment	D	Not counted	Max.2 dots allowed	allowed		
(DOT)		A<0.1mm	0.1mm≤A≤0.2mm			
				D<0.25mm		
Black spot	ot		Not counted	Max.3 spots allowed		
(in viewing area)		X<0.1mm	0.1mm≤X≤0.2mm			
ureu)		X=(a+b)/2	Max.3 spots			
Black line		Not counted	Max.3 lines allowed	(lines) allowed		
(in viewing area)		a<0.02mm	0. 02m≤a≤0.05mm b≤0.5mm			

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Criteria			
	Q + 0	Not counted	Max. 2 defects allowed		
		x<0.1mm	0.1mm≤x≤0.2mm		
		x=(a+b)/2			
	Π-+/+ <u>+</u> -α	Not counted	Max. 1 defects allowed	Max.3 defects	
Transfor- mation of segment		a<0.1mm	0.1mm≤a≤0.2mm D>0	allowed	
		Max.2 defects 0.8W≤a≤1.2 a=measured va W=nominal va			