Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS)

TPC6108

TENTATIVE

Notebook PC Applications Portable Equipment Applications

- Small footprint due to small and thin package
- Low drain-source ON resistance: RDS (ON) = 50 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 7.4 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement-model: $V_{th} = -0.8 \text{ to } -2.0 \text{ V}$

 $(V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	-30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	-30	V	
Gate-source voltage			V _{GSS}	±20	V	
Drain current	DC	(Note 1)	I _D	-4.5	А	
	Pulse	(Note 1)	I _{DP}	-18		
Drain power dissipation(t = 5 s) (Note 2a)			P _D	2.2	W	
Drain power dissipation(t = 5 s) (Note 2b)			P _D	0.7	VV	
Single pulse avalanche energy (Note 4)			E _{AS}	1.3	mJ	
Avalanche current	Avalanche current			-2.25	Α	
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3b, 5)			E _{AR}	0.22	mJ	
Channel temperature			T _{ch}	150	°C	
Storage temperature range			T _{stg}	-55~150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient(t = 5 s) (Note 2a)	R _{th (ch-a)}	56.8	°C/W
Thermal resistance, channel to ambient(t = 5 s) (Note 2b)	R _{th (ch-a)}	178.5	°C/W

Note: For (Note 1), (Note 2), (Note 3), (Note 4) and (Note 5), please refer to the next page.

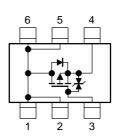
This transistor is an electrostatic sensitive device. Please handle with caution.

1. Drain 2. Drain 3. Gate 0.3 ± 0.1 2.9 ± 0.2 1. Orain 3. Gate 0.3 ± 0.1 2.0 ± 0.25 0.25 ± 0.15 4. Source 5. Drain 6. Drain 6. Drain

Weight: 0.011 g (typ.)

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Circuit Configuration



2-3T1A



Electrical Characteristics (Ta = 25°C)



Cha	Characteristics		Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$		_	±10	μΑ
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
Dialii-source ble	akuowii voitage	V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15 — —		_	
Gate threshold ve	oltage	V_{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8 — -2.0		V	
Drain-source ON	rosistanco	R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -2.2 \text{ A}$	_	75	100	mΩ
Dialii-source ON	resistance	R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -2.2 \text{ A}$	_	50	60	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -2.2 \text{ A}$	3.7	7.4	_	S
Input capacitance	Э	C _{iss}		_	570	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	75	_	pF
Dutput capacitance		Coss		_	85	_	
Switching time	Rise time	t _r	VGS $_{-10}^{0}$ V $_{DD}^{0}$	_	3.5	_	ns
	Turn-on time	t _{on}			12	_	
	Fall time	t _f		_	21	_	
	Turn-off time	t _{off}		_	70	_	
Total gate charge (gate-source plus	otal gate charge gate-source plus gate-drain)		$V_{DD} \simeq -24 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -4.5 \text{ A}$	_	13	_	nC
Gate-source charge1		Q _{gs1}		_	1.8	_	
Gate-drain ("mille	er") charge	Q_{gd}		_	2.5		

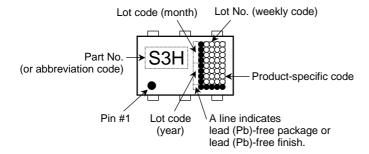
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characterist	ics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_	_	-18	Α
Forward voltage (diode)		V_{DSF}	$I_{DR} = -4.5 \text{ A}, V_{GS} = 0 \text{ V}$			1.2	V



Marking (Note 5)

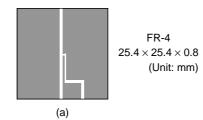


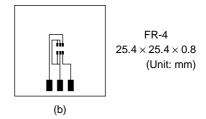


Note 1: Ensure that the channel temperature does not exceed 150 .

Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)

(b) Device mounted on a glass-epoxy board (b) (t = 5 s)





Note 3: $V_{DD} = -24~V$, $T_{ch} = 25^{\circ}C$ (initial), L = 0.2~mH, $R_G = 25~\Omega$, $I_{AR} = -2.25~A$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: on lower left of the marking indicates Pin 1.

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