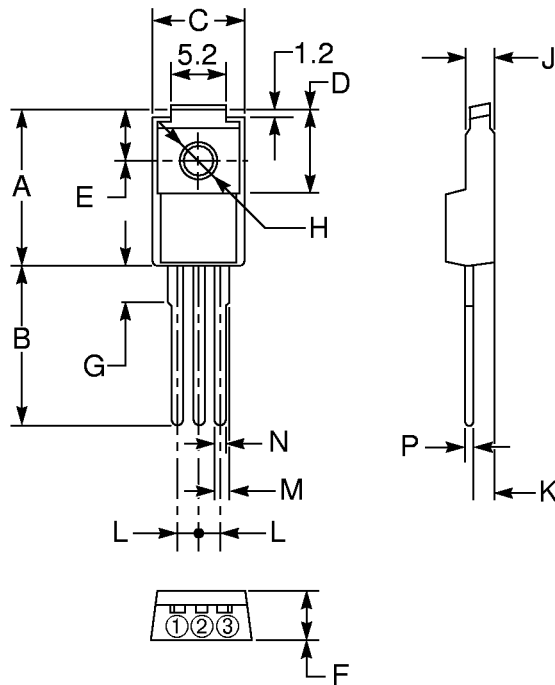


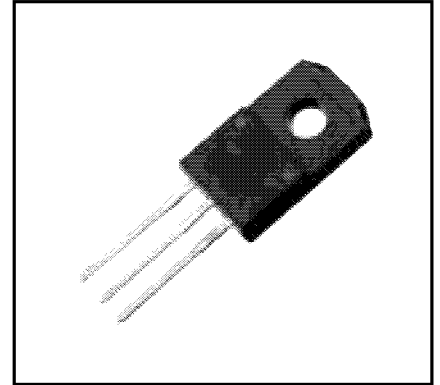
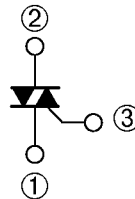
Isolated Triac 10 Amperes/400-600 Volts

OUTLINE DRAWING



CONNECTION DIAGRAM

- ① T1 TERMINAL
- ② T2 TERMINAL
- ③ GATE TERMINAL



Description:

A triac is a solid state silicon AC switch which may be gate triggered from an off-state to an on-state for either polarity of applied voltage.

Features:

- ☐ Full Molded Isolation Package
- ☐ Glass Passivation
- ☐ 1500 V_{RMS} Isolation Voltage
- ☐ UL Recognition
- ☐ Excellent Surge Capability
- ☐ Selected for Inductive Loads

Applications:

- ☐ AC Switch
- ☐ Motor Controls
- ☐ Lighting
- ☐ Switch Mode Power Supply

Ordering Information:

Example: Select the complete eight, nine or ten digit part number you desire from the table - i.e. BCR10PM-8 is a 400 Volt, 10 Ampere Triac.

Type	V _{DRM} Volts	Code	Inductive Load*
BCR10PM	400	-8	L
	600	-12	

*For inductive load, add L.

Outline Drawing (Conforms to TO-220F)

Dimensions	Inches	Millimeters
A	0.67	17.0
B	0.49 Min.	12.5 Min.
C	0.39	10.0
D	0.33	8.5
E	0.20	5.0
F	0.18	4.5
G	0.14	3.6

Dimensions	Inches	Millimeters
H	0.126 ±0.008 Dia.	3.2 ±0.2 Dia.
J	0.11	2.8
K	0.102	2.6
L	0.10	2.5
M	0.039	1.0
N	0.031	0.8
P	0.020	0.5



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BCR10PM

Isolated Triac

10 Amperes/400-600 Volts

Absolute Maximum Ratings, $T_a = 25^\circ\text{C}$ unless otherwise specified

Ratings	Symbol	BCR10PM-8	BCR10PM-12	Units
Repetitive Peak Off-state Voltage	V_{DRM}	400	600	Volts
Non-repetitive Peak Off-state Voltage	V_{DSM}	500	720	Volts
On-state Current, $T_c = 85^\circ\text{C}$	$I_{\text{T(RMS)}}$	10	10	Amperes
Non-repetitive Peak Surge, One Cycle (60 Hz)	I_{TSM}	100	100	Amperes
I^2t for Fusing, $t = 8.3$ msec	I^2t	41.6	41.6	A^2sec
Peak Gate Power Dissipation, 20 μsec	P_{GM}	5	5	Watts
Average Gate Power Dissipation	$P_{\text{G(avg)}}$	0.5	0.5	Watts
Peak Gate Current	I_{GM}	2	2	Amperes
Peak Gate Voltage	V_{GM}	10	10	Volts
Storage Temperature	T_{stg}	-40 to 125	-40 to 125	$^\circ\text{C}$
Operating Junction Temperature	T_j	-40 to 125	-40 to 125	$^\circ\text{C}$
Isolation Voltage	V_{iso}	1500	1500	Volts
Weight	—	2	2	Grams



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BCR10PM

Isolated Triac

10 Amperes/400-600 Volts

Electrical and Thermal Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions (Trigger Mode)				BCR10PM			
		V _D	R _L	R _G	T _J	Min.	Typ.	Max.	Units
Gate Parameters									
DC Gate Trigger Current									
MT2+ Gate+	I _{GT}	6V	6Ω	330Ω	25 °C	—	—	30	mA
MT2+ Gate—		6V	6Ω	330Ω	25 °C	—	—	30	mA
MT2— Gate—		6V	6Ω	330Ω	25 °C	—	—	30	mA
DC Gate Trigger Voltage									
MT2+ Gate+	V _{GT}	6V	6Ω	330Ω	25 °C	—	—	1.5	Volts
MT2+ Gate—		6V	6Ω	330Ω	25 °C	—	—	1.5	Volts
MT2— Gate—		6V	6Ω	330Ω	25 °C	—	—	1.5	Volts
DC Gate Non-trigger Voltage									
All	V _{GD}	1/2 V _{DRM}	—	—	125 °C	0.2	—	—	Volts

BCR10PM

Isolated Triac

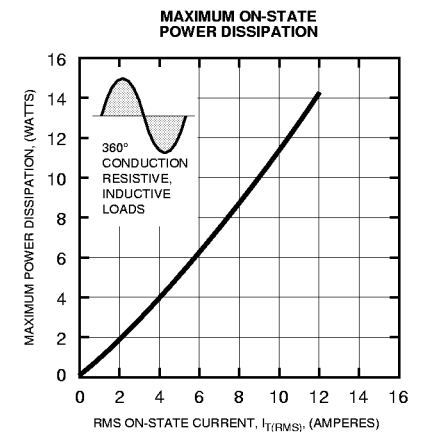
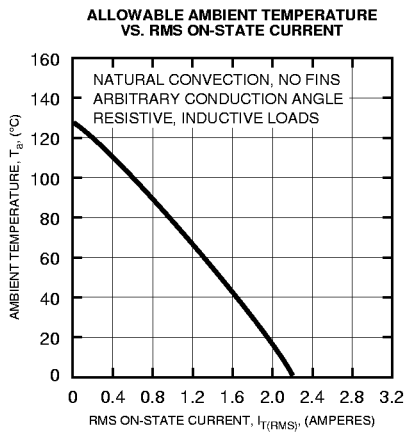
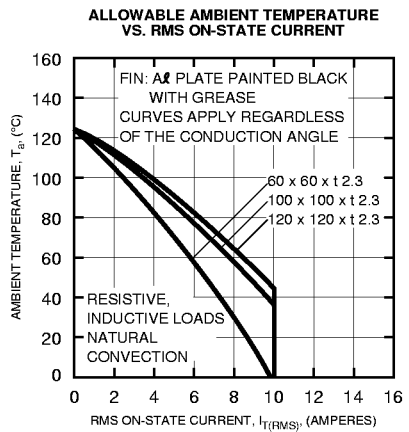
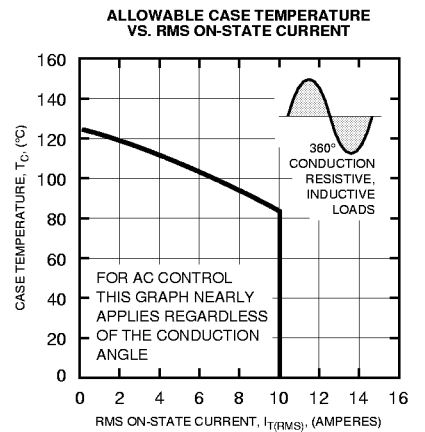
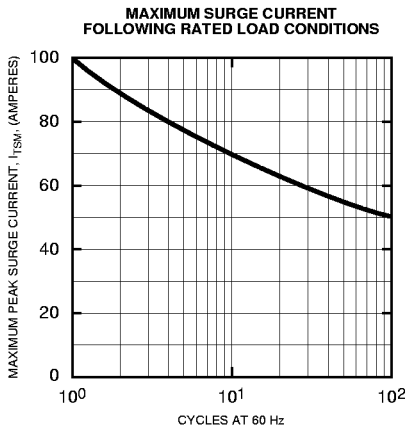
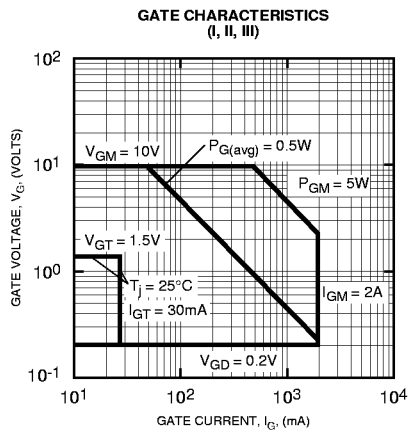
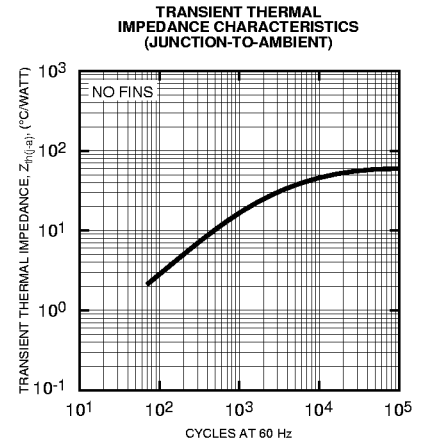
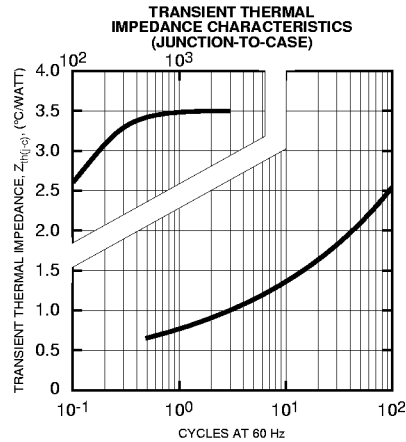
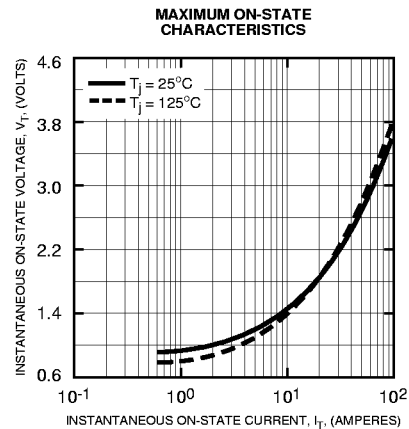
10 Amperes/400-600 Volts

Electrical and Thermal Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction-to-case	$R_{th(j-c)}$	—	—	—	3.5	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-ambient	$R_{th(j-a)}$	—	—	—	60	$^\circ\text{C/W}$
Voltage – Blocking State Repetitive Off-state Current	I_{DRM}	Gate Open Circuited, $V_D = V_{DRM}$, $T_j = 125^\circ\text{C}$	—	—	2.0	mA
Current – Conducting State Peak On-state Voltage	V_{TM}	$T_c = 25^\circ\text{C}$, $I_{TM} = 15\text{A}$	—	—	1.5	Volts
Critical Rate-of-rise of Commutating Off-state Voltage (Commutating dv/dt) ▲ for inductive load (L) (Switching)	$(dv/dt)_c$	—	—	—	—	$\text{V}/\mu\text{s}$

Δ Part Number	Commutating dv/dt , $(dv/dt)_c$ ($\text{V}/\mu\text{sec}$)		Test Condition	Commutating Voltage & Current Waveform (Inductive Load)
	V_{DRM} (Volts)	Minimum		
BCR10PM-8L	400	10	$T_j = 125^\circ\text{C}$,	
BCR10PM-12L	600	10	Rate of Decay On-state Commutating Current $(di/dt)_c = -5\text{A/msec}$; Peak Off-state Voltage $V_D = 400\text{V}$	

BCR10PM
Isolated Triac
 10 Amperes/400-600 Volts

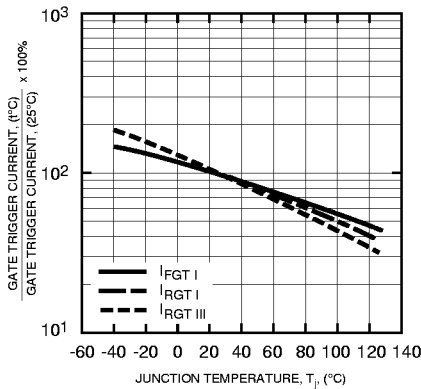


BCR10PM

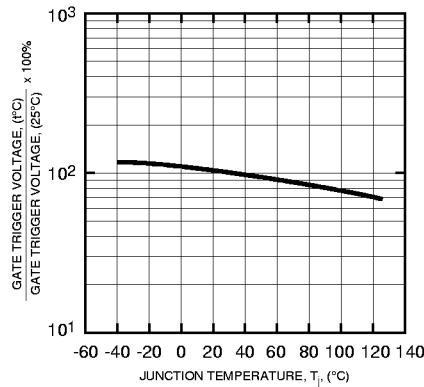
Isolated Triac

10 Amperes/400-600 Volts

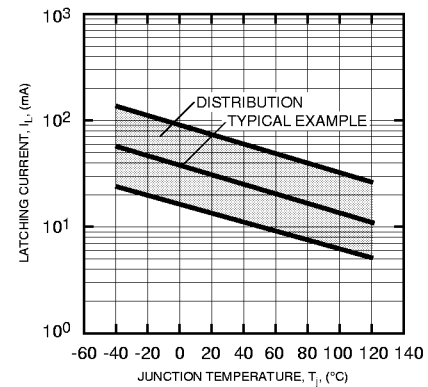
**GATE TRIGGER CURRENT
VS. JUNCTION TEMPERATURE
(TYPICAL)**



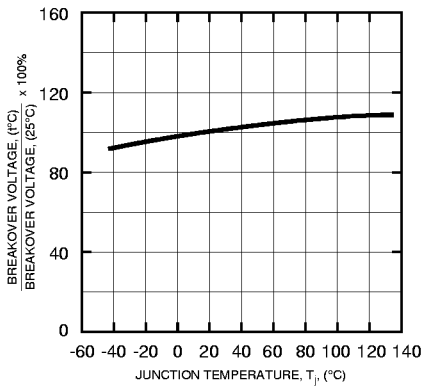
**GATE TRIGGER VOLTAGE
VS. JUNCTION TEMPERATURE
(TYPICAL)**



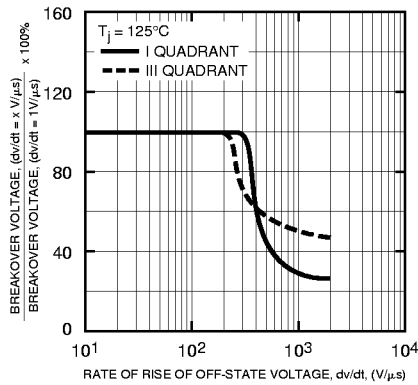
**LATCHING CURRENT
VS. JUNCTION TEMPERATURE
(TYPICAL)**



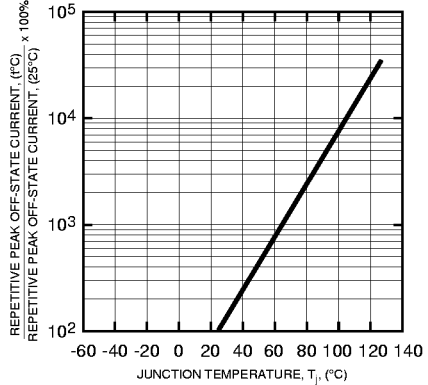
**BREAKOVER VOLTAGE
VS. JUNCTION TEMPERATURE
(TYPICAL)**



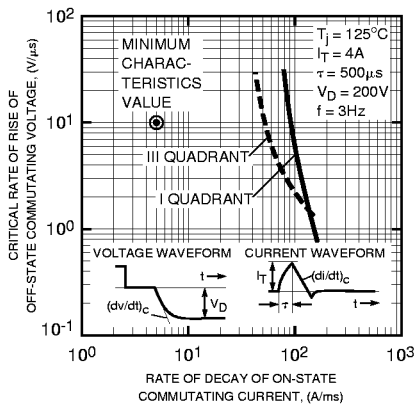
**BREAKOVER VOLTAGE VS.
RATE OF RISE OF OFF-STATE VOLTAGE
(TYPICAL)**



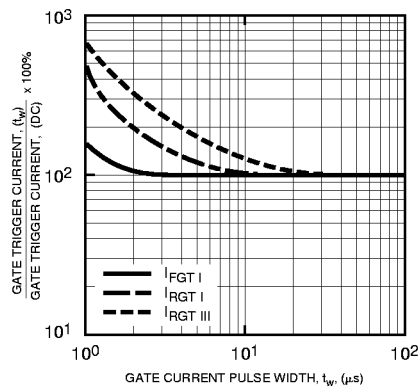
**REPETITIVE PEAK OFF-STATE CURRENT
VS. JUNCTION TEMPERATURE
(TYPICAL)**



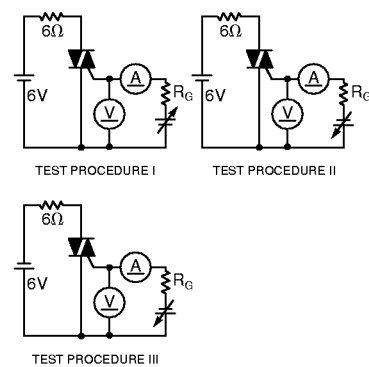
**COMMUTATION CHARACTERISTICS
(TYPICAL)**



**GATE TRIGGER CURRENT
VS. GATE CURRENT PULSE WIDTH
(TYPICAL)**



**GATE TRIGGER CHARACTERISTICS
TEST CIRCUITS**





T-91-01

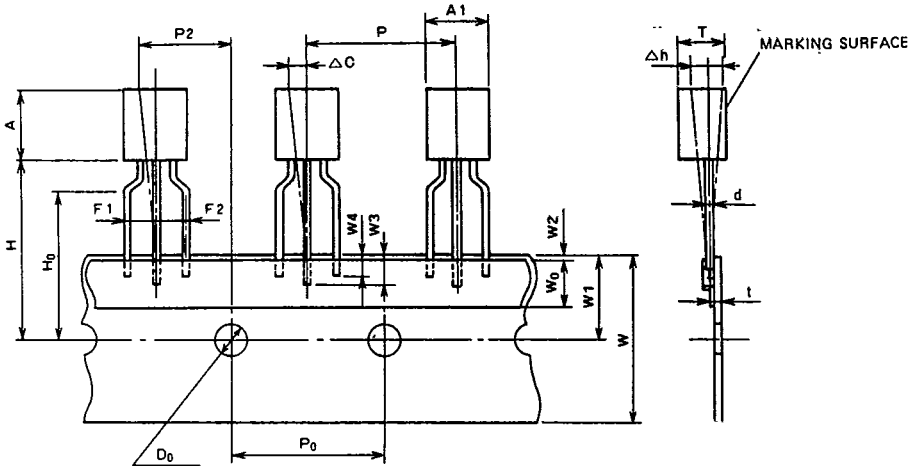
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Taping

STANDARD SPECIFICATIONS
FOR TAPING OF MOLDED
PACKAGE THYRISTORS AND
TRIACS

TO-92 Package

Thyristor
CR02AM, CR03AM, CR04AM
Triac
BCR1AM



Taping dimensions

Description of symbol	Symbol	Dimensions (Unit:mm)	Remark
Product width	A1	5.0 MAX	
Product height	A	5.0 MAX	
Product thickness	T	3.7 MAX	
Lead wire diameter	d	0.6 MAX	
Sticker lead wire length (1)	W3	2.5 MIN	
Sticker lead wire length (2)	W4	2.0 MIN	
Pitch between products	P	12.7 ± 1.0	
Feed hole pitch	P0	12.7 ± 0.3	The cumulative pitch error is ± 1mm per 20 pitches.
Feed hole deviation (1)	P2	6.35 ± 1.3	
Distance between lead wires	F1, F2	2.5 ± 0.4	
Defective product (1)	Δh	0 ± 2.0	
Tape width	W	18.0 ± ^{1.0} _{0.5}	
Sticker tape width	W0	6.0 ± 0.5	
Feed hole deviation (2)	W1	9.0 ± 0.5	
Sticker tape deviation	W2	0.5 MAX	
Position of product bottom surface	H	17.5 MIN	
Lynch height of lead wire	H0	16.0 ± 0.5	
Feed hole diameter	D0	4.0 ± 0.2	
Tape thickness	t	0.7 ± 0.2	
Defective product (2)	ΔC	0 ± 1.0	

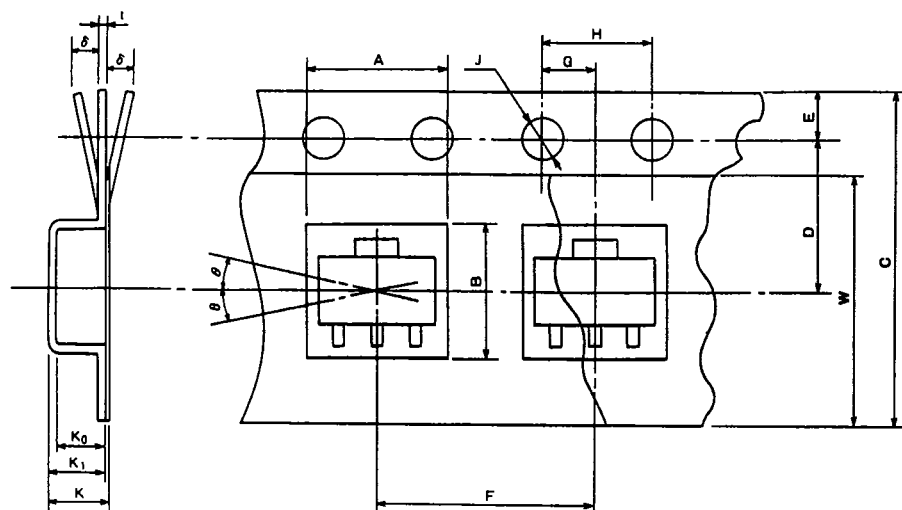


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Powerex Semiconductor Data Book

Taping



SOT-89 Package

Thyristor
CR08AS

Taping dimensions

Description of symbol		Symbol	Dimensions/angles Unit:mm	Remark
Parts Insertion	Height	A	5.0 ± 0.1	Cross-section of the surface 0.5mm above the inner bottom
	Width	B	4.6 ± 0.1	Cross-section of the surface 0.5mm above the inner bottom
Concave square hole	Depth	K ₀	1.8 ± 0.1	Inner space
	Pitch	F	8.0 ± 0.1	Cumulative error +0.1/-0.3 MAX/10 pitches
Round feed hole	Diameter	J	$\phi 1.5 \pm 0.05$	
	Pitch	H	4.0 ± 0.1	Cumulative error +0.1/-0.3 MAX/10 pitches
	Position	E	1.5 ± 0.1	Distance between the tape edge and the hole center
Distance between center lines	Vertical	G	2.0 ± 0.5	Center line of concave square hole and round feed hole
	Horizontal	D	5.65 ± 0.05	Center line of concave square hole and round feed hole
Cover tape	Width	W	$9.5 + 0.3/-0$	Thickness: 0.1 MAX
Carrier tape	Width	C	12 ± 0.2	Warp ± 0.3 MAX
	Thickness	t	0.3 ± 0.05	
	Package hole depth	K ₁	2.1 ± 0.1	
Device	Package dimensions	—	—	As shown in (e)
	Inclination	θ	30° MAX.	
Total Thickness		K	2.3 ± 0.1	Total thickness including cover and carrier tapes