SHARP PC4H510NIP

PC4H510NIP

■ Features

- High collector-emitter voltage
 Collector-emitter voltage:350V
- 2. Compact and thin package Half pitch, mini flat package
- 3. Recognized by UL, file No. E64380

■ Absolute Maximum Patings

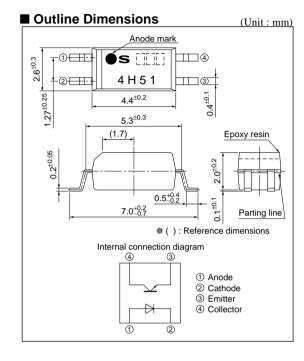
■ Applications

1. Modems

Absolute waximum Ratings (Ta=25°C)								
	Parameter	Symbol	Rating	Unit				
Input	*1 Forward current	IF	50	mA				
	*2 Peak forward current	Iғм	1	A				
	Reverse voltage	V_R	6	V				
	*1 Power dissipation	P	70	mW				
Output	Collector-emitter voltage	Vceo	350	V				
	Emitter-collector voltage	VECO	6	V				
	Collector current	Ic	50	mA				
	*1 Collector power dissipation	Pc	150	50 mW				
	*1 Total power dissipation P _{tot}		170	mW				
Operating temperature		Topr	-25 to +100	°C				
Storage temperature *3 Isolation voltage *4 Soldering temperature		Tstg	-55 to +125	°C				
		Viso (rms)	2.5	kV				
		T1	260	°C				

 $[\]ast 1$ The derating factors of absolute maximum ratings due to ambient temperature are shown in Fig.2 to 5

Mini Flat Half-pitch Type High Collector-Emitter Voltage Photocoupler



^{*2} Pulse width≤100µs, Duty ratio=0.001(shown in Fig.6)

^{*3 40} to 60% RH, AC for 1 min, f=60Hz

^{*4} For 10 s

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Ta=25°										
Parameter			Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Input	Forward voltage		V _F	I _F =20mA	-	1.2	1.4	V		
	Reverse current		IR	V _R =4V	-	_	10	μΑ		
	Terminal capacitance		Ct	V=0, f=1kHz	-	30	250	pF		
Output	Collector dark current		Iceo	Vce=200V, I _F =0	-	_	1	μΑ		
	Collector-emitter breakdown voltage		BVCEO	Ic=0.1mA, I _F =0	350	_	_	V		
	Emitter-collector breakdown voltage		BVECO	Iε=10μA, Iε=0	6	-	_	V		
Transfer characteristics	Collector current		Ic	I _F =5mA, V _{CE} =5V	2.0	4.0	12.0	mA		
	Collector-emitter saturation voltage		V _{CE(sat)}	I _F =20mA, I _C =1mA	_	0.1	0.3	V		
	Isolation resistance		Riso	DC500V, 40 to 60%RH	5×10 ¹⁰	1011	_	Ω		
	Floating capacitance		Cf	V=0, f=1MHz	_	0.6	1.0	pF		
	Cut-off frequency		fc	Vce=5V, Ic=2mA, Rl= 100Ω , $-3dB$	_	50	_	kHz		
	Response time	Rise time	tr	V _{CE} =2V I _C =2mA	-	4	10	μs		
		Fall time	t f	$R_{L}=100\Omega$	_	5	12	μs		

Fig.1 Forward Current vs. Ambient Temperature

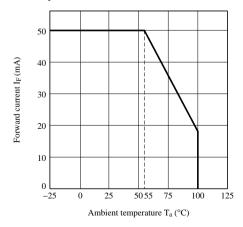
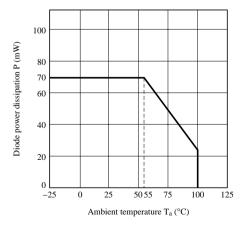


Fig.2 Diode Power Dissipation vs. Ambient Temperature



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Fig.3 Collector Power Dissipation vs. Ambient Temperature

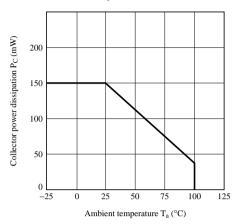


Fig.5 Peak Forward Current vs. Duty Ratio

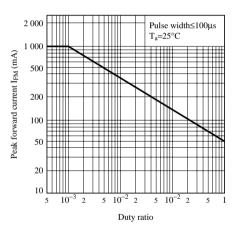


Fig.7 Current Transfer Ratio vs. Forward Current

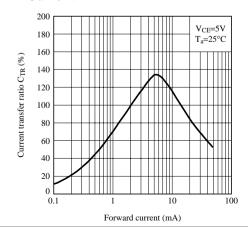


Fig.4 Total Power Dissipation vs. Ambient Temperature

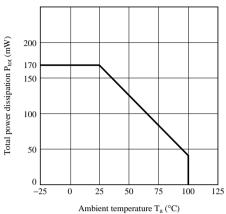


Fig.6 Forward Current vs. Forward Voltage

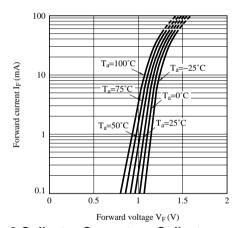
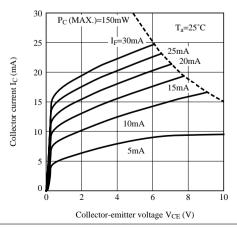


Fig.8 Collector Current vs. Collector-emitter Voltage



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Fig.9 Relative Current Transfer Ratio vs. Ambient Temperature

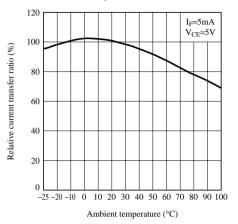


Fig.11 Collector Dark Current vs. Ambient Temperature

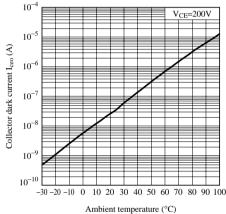


Fig.13 Test Circuit for Response Time

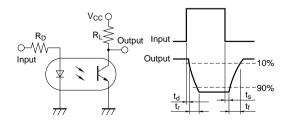


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

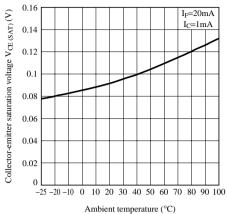


Fig.12 Response Time vs. Load Resistance

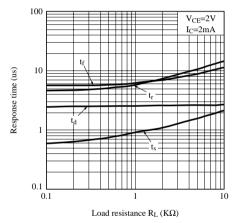


Fig.14 Collector-emitter Saturation Voltage vs. Forward Current

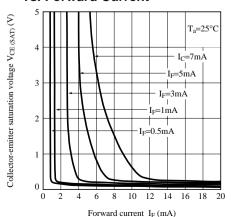
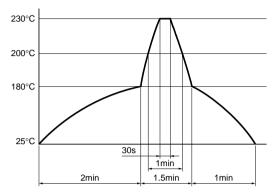


Fig.15 Reflow Soldering

Only one time soldering is recommended within the temperature profile shown below.



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