



**Electrical Characteristics**

**Electrical Characteristics of Logic Block at, Ta = -15 to +75°C V<sub>DD</sub> = 3.0 to 3.6 V**

Parameter	Symbol	Conditions	Ratings			Unit	Applicable pins
			min	typ	max		
High-level input voltage	V <sub>IH</sub>	CMOS levels	0.7 V <sub>DD</sub>			V	(1)
Low-level input voltage	V <sub>IL</sub>				0.2 V <sub>DD</sub>	V	
High-level input voltage	V <sub>IH</sub>	CMOS levels	0.75 V <sub>DD</sub>			V	(2) (5)
Low-level input voltage	V <sub>IL</sub>	Schmitt			0.15 V <sub>DD</sub>	V	
High-level output voltage	V <sub>OH</sub>	I <sub>OH</sub> = 2 mA	V <sub>DD</sub> - 0.8			V	(3) (4)
Low-level output voltage	V <sub>OL</sub>	I <sub>OL</sub> = 2 mA			0.4	V	
Low-level output voltage	V <sub>OL</sub>	I <sub>OL</sub> = 4 mA			0.4	V	(5)
Input leak current	I <sub>L</sub>	V <sub>I</sub> = V <sub>DD</sub> , V <sub>SS</sub>	-10		+10	µA	(1) (2)
Output leak current	I <sub>OZ</sub>	During high-impedance output	-10		+10	µA	(3) (5)

The applicable pin sets are as follows.

**INPUT**

- (1) SELMT1, SELMT2, WBMD1, WBMD2, CSET1, CSET2, OMOD1, OMOD2, RGST3 to 12, SMERSW
- (2) LCIN0 to 7, WHCLK, FTRT1, FTRT2, OEB, REGRES, RGST1 to 2, RGST13 to 20

**OUTPUT**

- (3) Y [7:0], U [7:0], V [7:0]
- (4) STRG, VD, HD, CBLNK, CKOUT

**INPUT/OUTPUT**

- (5) RGST2 (Open Drain)

\* AGCDC is not included in DC characteristics.

**Electrical Characteristics of Analog Block**

**(Electrical Characteristics of DAC for Gain Control) at Ta = 25°C, V<sub>DD</sub> = 3.3 V, V<sub>SS</sub> = 0 V**

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Resolution			—	—	7	Bits
Linearity Error			—	—	0.5	LSB
Differential Linearity Error			—	—	0.5	LSB
Reference Resistance			—	1.4	—	kΩ

Block Diagram

