Low Power Peak EMI Reducing Solution

Features

- Generates an EMI optimized clock signal at the
- Integrated loop filter components.
- Operates with a 3.3V / 2.5V Supply.
- Operating current less than 4mA.
- Low power CMOS design.
- Input frequency range: 6MHz to 12MHz for 2.5V. : 6MHz to 13MHz for 3.3V.
- Generates a 1X low EMI spread spectrum clock of the input frequency.
- Frequency deviation: ±0.65% @ 8MHz
- Available in 6 pin TSOT-23, 8 pin SOIC and
- 8 pin TSSOP Packages.

Product Description

The ASM3P2760A is a versatile spread spectrum frequency modulator designed specifically for a wide range of clock frequencies. The ASM3P2760A reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. The ASM3P2760A allows significant system cost savings by reducing the number of circuit board layers ferrite beads, shielding that are traditionally required to pass EMI regulations.

The ASM3P2760A uses the most efficient and optimized modulation profile approved by the FCC and is implemented by using a proprietary all digital method.

The ASM3P2760A modulates the output of a single PLL in order to "spread" the bandwidth of a synthesized clock, and more importantly, decreases the peak amplitudes of its harmonics. This results in significantly lower system EMI compared to the typical narrow band signal produced by oscillators and most frequency generators. Lowering EMI by increasing a signal's bandwidth is called 'spread spectrum clock generation'.

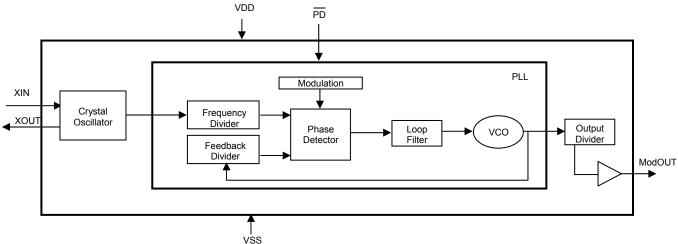
Applications

The ASM3P2760A is targeted towards all portable devices with very low power requirements like MP3 players and digital still cameras.

Key Specifications

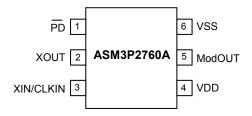
Description	Specification
Supply voltages	VDD = 3.3V / 2.5V
Cycle-to-Cycle Jitter	200pS (Max)
Output Duty Cycle	45/55%
Modulation Rate Equation	F _{IN} /256
Frequency Deviation	±0.65% @ 8MHz

Block Diagram





Pin Configuration (6-pin TSOT-23 Package)

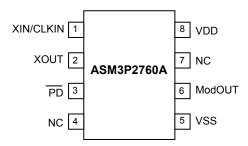


Pin Description

Pin#	Pin Name	Туре	Description				
1	— PD	I	Power-down control pin. Pull low to enable power-down mode. Connect to VDD if not used.				
2	XOUT	0	Crystal connection. If using an external reference, this pin must be left unconnected.				
3	XIN/CLKIN	I	Crystal connection or external reference frequency input. This pin has dual functions. It can be connected either to an external crystal or an external reference clock.				
4	VDD	Р	Power supply for the entire chip				
5	ModOUT	0	Spread spectrum clock output.				
6	VSS	Р	Ground connection.				



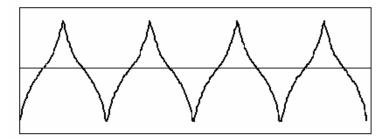
Pin Configuration (8-pin SOIC and TSSOP Packages)



Pin Description

Pin#	Pin Name	Туре	Description
1	XIN/CLKIN	I	Crystal connection or external reference frequency input. This pin has dual functions. It can be connected either to an external crystal or an external reference clock.
2	XOUT	0	Crystal connection. If using an external reference, this pin must be left unconnected.
3	PD	I	Power-down control pin. Pull low to enable power-down mode. Connect to VDD if not used.
4	NC	-	No connect.
5	VSS	Р	Ground connection.
6	ModOUT	0	Spread spectrum clock output.
7	NC	-	No connect.
8	VDD	Р	Power supply for the entire chip

Modulation Profile



Specification

Description		Specification
Frequency Range	For 2.5V Supply	6MHz < CLKIN < 12MHz
Frequency Range	For 3.3V Supply	6MHz < CLKIN < 13MHz
Modulation Equation		F _{IN} /256
Frequency Deviation		±0.65% @ 8MHz



Absolute Maximum Ratings

Parameter	Rating	Unit
Voltage on any pin with respect to Ground	-0.5 to +4.6	V
Storage temperature	-65 to +125	°C
Operating temperature	-40 t0 +85	°C
Max. Soldering Temperature (10 sec)	260	°C
Junction Temperature	150	°C
Static Discharge Voltage (As per JEDEC STD22- A114-B)	2	KV
	Voltage on any pin with respect to Ground Storage temperature Operating temperature Max. Soldering Temperature (10 sec) Junction Temperature Static Discharge Voltage	Voltage on any pin with respect to Ground -0.5 to +4.6 Storage temperature -65 to +125 Operating temperature -40 t0 +85 Max. Soldering Temperature (10 sec) Junction Temperature Static Discharge Voltage 2

DC Electrical Characteristics for 2.5V Supply (Test condition: All parameters are measured at room temperature (+25°C) unless otherwise stated)

Symbol	Parameter	Min	Тур	Max	Unit
V_{IL}	Input low voltage	VSS-0.3	-	0.8	V
V _{IH}	Input high voltage	2.0	-	VDD+0.3	V
I _{IL}	Input low current	-	-	-35	μΑ
I _{IH}	Input high current	-	-	35	μA
I _{XOL}	XOUT output low current (@0.5V, VDD=2.5V)	-	3	-	mA
I _{XOH}	XOUT output high current (@1.8V, VDD=2.5V)	-	3	-	mA
V _{OL}	Output low voltage (VDD = 2.5V, I _{OL} = 8mA)	-	-	0.6	V
V _{OH}	Output high voltage (VDD = 2.5V, I _{OH} = 8mA)	1.8	-	-	V
I _{DD}	Static supply current*	-	_	10	μΑ
Icc	Dynamic supply current (2.5V, 8MHz and no load)	-	2.5	-	mA
VDD	Operating Voltage	2.375	2.5	2.625	V
t _{ON}	Power-up time (first locked cycle after power-up)**	-	-	5	mS
Z _{OUT}	Output impedance	-	50	-	Ω
	pin and <u>PD</u> pin are pulled <u>low</u> N/CLKIN input are stable, PD pin is made high from low.				

AC Electrical Characteristics for 2 5V Supply

Symbol	Parameter			Тур	Max	Unit
CLKIN	Input frequency	Input frequency		-	12	MHz
ModOUT	Output frequency		6	-	12	MHz
f.	Frequency Deviation	Input Frequency = 6MHz	-	±1.0	-	- %
f _d	Frequency Deviation	Input Frequency = 12MHz	-	±0.45	-	
t _{LH} *	Output rise time (measured from 0.7V to 1.7V)		0.4	1.2	1.4	nS
t _{HL} *	Output fall time (measured from 1.7V to 0.7V)			0.9	1.1	nS
t _{JC}	Jitter (cycle to cycle)	-	-	200	pS	
t _D	Output duty cycle	45	50	55	%	
* t _{LH} and t _{HL} are	e measured into a capacitive load of 15pF	:	•	•		

January 2007



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DC Electrical Characteristics for 3.3V Supply (Test condition: All parameters are measured at room temperature (+25°C) unless otherwise stated.)

Symbol	Parameter	Min	Тур	Max	Unit
V _{IL}	Input low voltage	VSS - 0.3	-	0.8	V
V _{IH}	Input high voltage	2.0	-	VDD + 0.3	V
I _{IL}	Input low current	-	-	-35	μA
I _{IH}	Input high current	-	-	35	μA
I _{XOL}	XOUT output low current (@0.4V, VDD=3.3V)	-	3	-	mA
I _{XOH}	XOUT output high current (@2.5V, VDD=3.3V)	-	3	-	mA
V _{OL}	Output low voltage (VDD = 3.3 V, I _{OL} = 8mA)	-	-	0.4	V
V _{OH}	Output high voltage (VDD = 3.3 V, I _{OH} = 8mA)	2.5	-	-	V
I _{DD}	Static supply current*	-	-	10	μΑ
Icc	Dynamic supply current (3.3V, 8MHz and no load)	-	3.0	-	mA
VDD	Operating Voltage	2.7	3.3	3.6	V
ton	Power-up time(first locked cycle after power up)**	-	-	5	mS
Z _{OUT}	Output impedance	-	45	-	Ω
Z _{OUT}		-		- 45	-

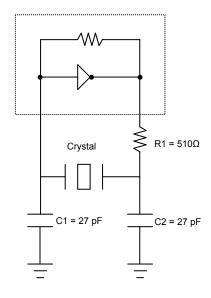
^{**}V_{DD} and XIN/CLKIN input are stable; PD pin is made high from low.

AC Electrical Characteristics for 3.3V Supply

Symbol	Para	Parameter			Max	Unit	
CLKIN	Input frequency		6	-	13	MHz	
ModOUT	Output frequency		6	-	13	MHz	
f_d	Fraguency Deviation	Input Frequency = 6MHz	-	±1.0	-	%	
ld .	Frequency Deviation	Input Frequency = 13MHz	-	±0.4	-	70	
t∟ _H *	Output rise time (measured a	Output rise time (measured at 0.8V to 2.0V)		1.3	1.5	nS	
t _H ∟*	Output fall time (measured a	Output fall time (measured at 2.0V to 0.8V)		1.0	1.2	nS	
t _{JC}	Jitter (cycle to cycle)	Jitter (cycle to cycle)			200	pS	
t _D	Output duty cycle			50	55	%	
*t _{LH} and t _{HL} are measured	*t _{LH} and t _{HL} are measured into a capacitive load of 15pF						



Typical Crystal Oscillator Circuit



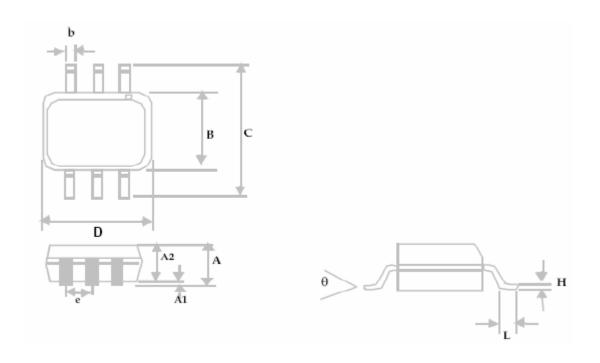
Typical Crystal Specifications

Fundamental AT cut parallel resonant crystal				
Nominal frequency	8.000MHz			
Frequency tolerance	± 50ppm or better at 25°C			
Operating temperature range	-25°C to +85°C			
Storage temperature	-40°C to +85°C			
Load capacitance	18pF			
Shunt capacitance	7pF maximum			
ESR	25Ω			



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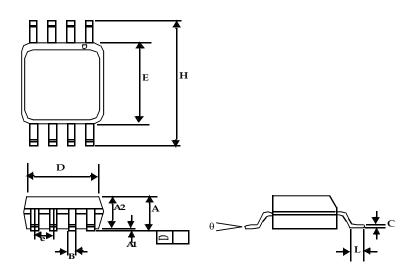
6-pin TSOT-23 Package



	Dimensions				
Symbol	Inches		Millim	neters	
	Min	Max	Min	Max	
Α		0.04		1.00	
A1	0.00	0.004	0.00	0.10	
A2	0.033	0.036	0.84	0.90	
b	0.012	0.02	0.30	0.50	
Н	0.005	BSC	0.127 BSC		
D	0.114	BSC	2.90 BSC		
В	0.06	BSC	1.60 BSC		
е	0.0374 BSC		0.950	BSC	
С	0.11 BSC		2.80	BSC	
L	0.0118	0.02	0.30	0.50	
θ	0°	4°	0°	4°	



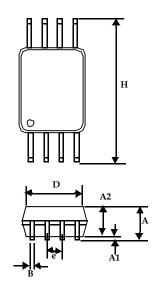
8-Pin SOIC Package

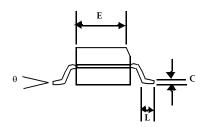


	Dimensions				
Symbol	Inc	hes	Millimeters		
	Min	Max	Min	Max	
A1	0.004	0.010	0.10	0.25	
Α	0.053	0.069	1.35	1.75	
A2	0.049	0.059	1.25	1.50	
В	0.012	0.020	0.31	0.51	
С	0.007	0.010	0.18	0.25	
D	0.193	BSC	4.90 BSC		
Е	0.154 BSC		3.91	BSC	
е	0.050 BSC		1.27	BSC	
Н	0.236 BSC		6.00	BSC	
L	0.016	0.050	0.41	1.27	
θ	0°	8°	0°	8°	

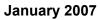


8-Pin TSSOP Package





	Dimensions				
Symbol	Inches		Millimeters		
	Min	Max	Min	Max	
А		0.043		1.10	
A1	0.002	0.006	0.05	0.15	
A2	0.033	0.037	0.85	0.95	
В	0.008	0.012	0.19	0.30	
С	0.004	0.008	0.09	0.20	
D	0.114	0.122	2.90	3.10	
E	0.169	0.177	4.30	4.50	
е	0.026 BSC		0.65 BSC		
Н	0.252 BSC		6.40 BSC		
L	0.020	0.028	0.50	0.70	
θ	0°	8°	0°	8°	



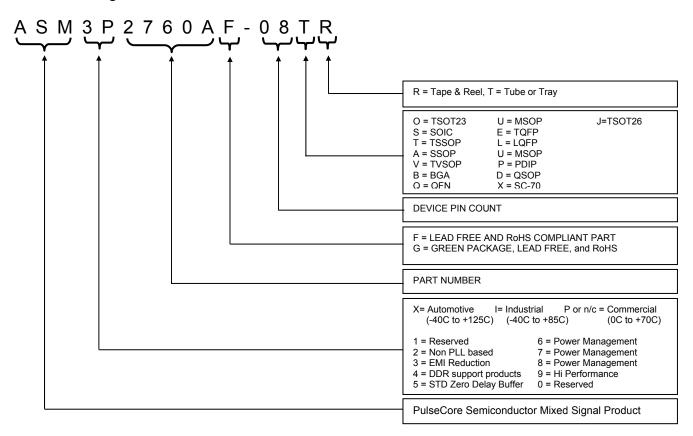


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Ordering Information

Part Number	Marking	Package Type	Temperature
ASM3P2760AF-06OR	E4LL	6-Pin TSOT-23, TAPE & REEL, Pb Free	Commercial
ASM3P2760AF-08TT	3P2760AF	8-Pin TSSOP, TUBE, Pb Free	Commercial
ASM3P2760AF-08TR	3P2760AF	8-Pin TSSOP, TAPE & REEL, Pb Free	Commercial
ASM3P2760AF-08ST	3P2760AF	8-Pin SOIC, TUBE, Pb Free	Commercial
ASM3P2760AF-08SR	3P2760AF	8-Pin SOIC, TAPE & REEL, Pb Free	Commercial
ASM3P2760AG-06OR	E3LL	6-Pin TSOT-23, TAPE & REEL, Green	Commercial
ASM3P2760AG-08TT	3P2760AG	8-Pin TSSOP, TUBE, Green	Commercial
ASM3P2760AG-08TR	3P2760AG	8-Pin TSSOP, TAPE & REEL, Green	Commercial
ASM3P2760AG-08ST	3P2760AG	8-Pin SOIC, TUBE, Green	Commercial
ASM3P2760AG-08SR	3P2760AG	8-Pin SOIC, TAPE & REEL, Green	Commercial
ASM3I2760AF-06OR	E5LL	6-Pin TSOT-23, TAPE & REEL, Pb Free	Industrial
ASM3I2760AF-08TT	3I2760AF	8-Pin TSSOP, TUBE, Pb Free	Industrial
ASM3I2760AF-08TR	3I2760AF	8-Pin TSSOP, TAPE & REEL, Pb Free	Industrial
ASM3I2760AF-08ST	3I2760AF	8-Pin SOIC, TUBE, Pb Free	Industrial
ASM3I2760AF-08SR	3I2760AF	8-Pin SOIC, TAPE & REEL, Pb Free	Industrial
ASM3I2760AG-06OR	E6LL	6-Pin TSOT-23, TAPE & REEL, Green	Industrial
ASM3I2760AG-08TT	3I2760AG	8-Pin TSSOP, TUBE, Green	Industrial
ASM3I2760AG-08TR	3I2760AG	8-Pin TSSOP, TAPE & REEL, Green	Industrial
ASM3I2760AG-08ST	3I2760AG	8-Pin SOIC, TUBE, Green	Industrial
ASM3I2760AG-08SR	3I2760AG	8-Pin SOIC, TAPE & REEL, Green	Industrial



Device Ordering Information



Licensed under U.S Patent Nos 5,488,627 and 5,631,921





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Note: This product utilizes US Patent #6,646,463 Impedance Emulator Patent issued to PulseCore Semiconductor, dated 11-11-2003

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