## **BTA225-600BT**

# Three quadrant triacs high commutation Rev. 2 — 9 November 2011

Product data sheet

#### 1. **Product profile**

### 1.1 General description

Passivated high commutation triac in a SOT78 (TO-220AB) plastic package. Intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. These devices will commutate the full rated RMS current at the maximum rated junction temperature, without the aid of a snubber.

#### 1.2 Features and benefits

- High maximum junction temperature
- High commutation capability

### 1.3 Applications

- Motor control
- Industrial and domestic heating

### 1.4 Quick reference data

- V<sub>DRM</sub> ≤ 600 V
- $I_{TSM} \le 200 \text{ A}$

- $I_{T(RMS)} \le 25 A$
- $I_{GT} \le 50 \text{ mA} (T2+ G+; T2+ G-; T2- G-)$

#### **Pinning information** 2.

Table 1 Pinning

Pin	Description	Simplified outline	Symbol
1	main terminal 1 (T1)		<b>.</b> .
2	main terminal 2 (T2)	mp -	T2—T1
3	gate (G)		sym051
mb	mounting base, connected to main terminal 2 (T2)	1 2 3 03ab54	
		SOT78 (TO-220AB)	



### 3. Ordering information

### Table 2. Ordering information

Type number	Package					
	Name	Description	Version			
BTA225-600BT	SC-46	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78			

### 4. Limiting values

### Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		<u>[1]</u> -	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 116 °C; see <u>Figure 4</u> and <u>5</u>	-	25	Α
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; $T_j = 25$ °C prior to surge; see Figure 2 and 3			
		t <sub>p</sub> = 20 ms	-	200	Α
		t <sub>p</sub> = 16.7 ms	-	220	Α
I <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 10 ms	-	200	A <sup>2</sup> s
dI <sub>T</sub> /dt	repetitive rate of rise of on-state current after triggering	$I_{TM} = 30 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu\text{s}$	-	100	A/μs
I <sub>GM</sub>	peak gate current		-	2	Α
$V_{GM}$	peak gate voltage		-	5	V
$P_{GM}$	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	+150	°C
Tj	junction temperature		-	150	°C

<sup>[1]</sup> Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/µs.

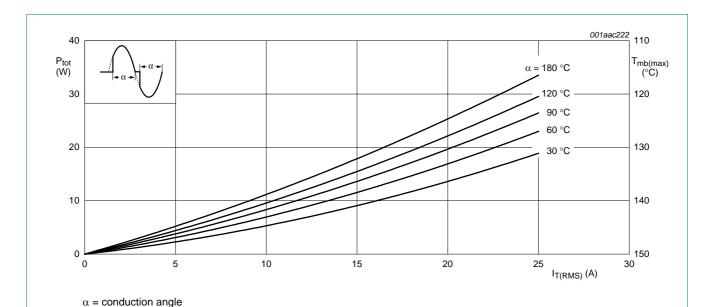


Fig 1. Total power dissipation as a function of RMS on-state current; maximum values

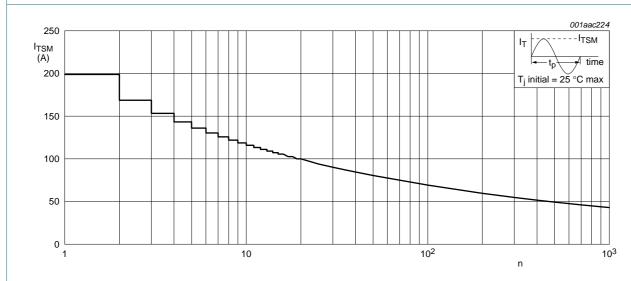


Fig 2. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

f = 50 Hz

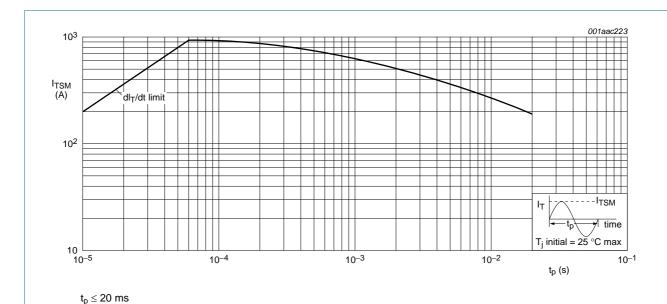


Fig 3. Non-repetitive peak on-state current as a function of pulse width (tp) for sinusoidal currents; maximum values

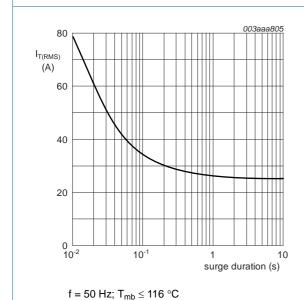


Fig 4. RMS on-state current as a function of surge duration for sinusoidal currents

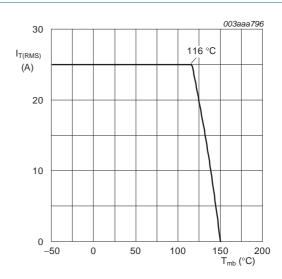
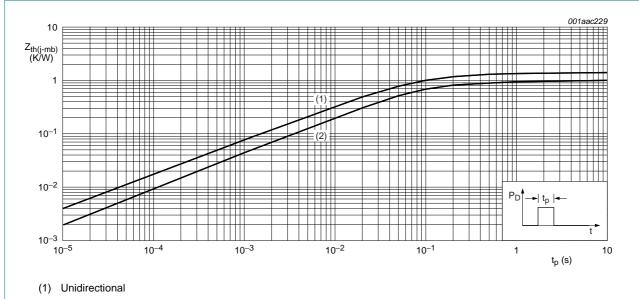


Fig 5. RMS on-state current as a function of mounting base temperature; maximum values

### 5. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
ui(J-iiib)	thermal resistance from	full cycle	-	-	1.0	K/W
	junction to mounting base	half cycle	-	-	1.4	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	60	-	K/W



(2) Bidirectional

Fig 6. Transient thermal impedance as a function of pulse width

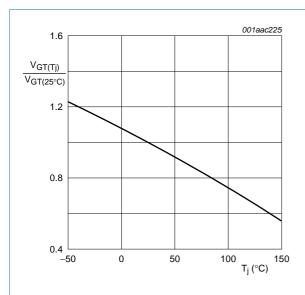
### 6. Characteristics

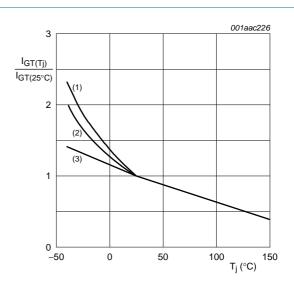
Table 5. Characteristics

 $T_i = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A;}$ see <u>Figure 8</u>	[1]			
		T2+ G+	2	18	50	mA
		T2+ G-	2	21	50	mA
		T2- G-	2	34	50	mA
I <sub>L</sub> latching current		$V_D = 12 \text{ V; } I_{GT} = 0.1 \text{ A;}$ see <u>Figure 10</u>				
		T2+ G+	-	31	60	mA
		T2+ G-	-	34	90	mA
		T2- G-	-	30	60	mA
Ін	holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A};$ see <u>Figure 11</u>	-	31	60	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 30 A; see <u>Figure 9</u>	-	1.3	1.55	V
$V_{GT}$	gate trigger voltage	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A;}$ see Figure 7	-	0.7	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A};$ $T_j = 150 \text{ °C}$	0.25	0.4	-	V
I <sub>D</sub>	off-state leakage current	$V_D = V_{DRM(max)}$ ; $T_j = 150  ^{\circ}C$	-	1	5	mA
Dynamic o	haracteristics					
$ \frac{\text{dV}_{\text{D}}/\text{dt}}{\text{dV}_{\text{D}}/\text{dt}} \qquad \frac{\text{critical rate of rise of}}{\text{off-state voltage}} \qquad \frac{\text{V}_{\text{D}}}{\text{T}_{\text{j}}} $		$V_{DM} = 67 \% V_{DRM(max)};$ $T_j = 150 ^{\circ}C;$ exponential waveform; gate open circuit	1000	4000	-	V/μs
dl <sub>com</sub> /dt	critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 150 \text{ °C};$ $I_{T(RMS)} = 25 \text{ A};$ without snubber; gate open circuit; see Figure 12	9	20	-	A/ms
t <sub>gt</sub>	gate controlled turn-on time	$I_{TM} = 30 \text{ A}; V_D = V_{DRM(max)};$ $I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	2	-	μS

<sup>[1]</sup> Device does not trigger in the T2-, G+ quadrant.

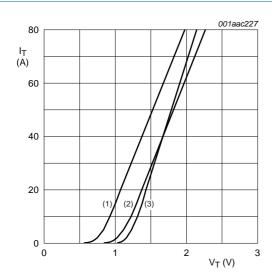




- (1) T2- G-
- (2) T2+ G-
- (3) T2+ G+

Fig 7. Normalized gate trigger voltage as a function of junction temperature





 $V_0 = 1.073 \text{ V}$ 

 $R_S = 0.015 \Omega$ 

- (1)  $T_i = 150 \,^{\circ}\text{C}$ ; typical values
- (2)  $T_i = 150 \,^{\circ}\text{C}$ ; maximum values
- (3)  $T_i = 25 \, ^{\circ}C$ ; maximum values

Fig 9. On-state current characteristics

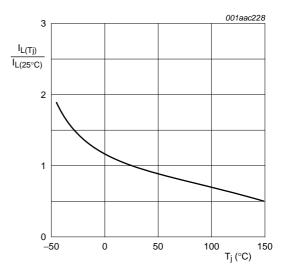


Fig 10. Normalized latching current as a function of junction temperature

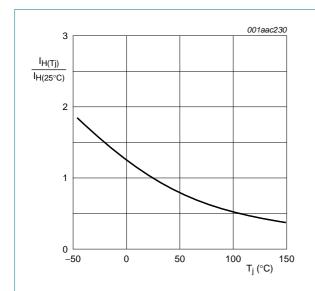


Fig 11. Normalized holding current as a function of junction temperature

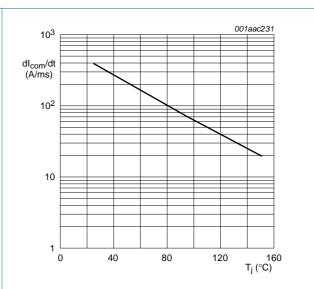


Fig 12. Critical rate of change of commutating current as a function of junction temperature; typical values

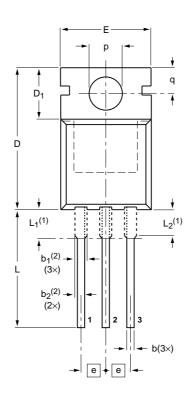
### 7. Package information

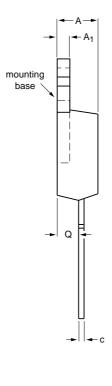
Refer to mounting instructions for SOT78 (TO-220AB) package.

Epoxy meets requirements of UL94 V-0 at ½ inch.

### Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB **SOT78** 





0 5 10 mm scale

#### **DIMENSIONS** (mm are the original dimensions)

UNIT	Α	A <sub>1</sub>	b	b <sub>1</sub> (2)	b <sub>2</sub> (2)	C	D	D <sub>1</sub>	E	е	L	L <sub>1</sub> (1)	L <sub>2</sub> <sup>(1)</sup> max.	р	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

#### Notes

- Lead shoulder designs may vary.
   Dimension includes excess dambar.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	1330E DATE
SOT78		3-lead TO-220AB	SC-46			<del>08-04-23</del> 08-06-13

Fig 13. Package outline SOT78 (TO-220AB)

BTA225-600BT

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### 9. Revision history

### Table 6. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
BTA225-600BT v.2	20111109	Product data sheet	-	BTA225-600BT v.1			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> </ul>						
	<ul> <li>Legal texts</li> </ul>	have been adapted to the	new company name whe	ere appropriate.			
BTA225-600BT v.1	20050303	Product data sheet	-	-			

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#### 10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design
- [2] The term 'short data sheet' is explained in section "Definitions"
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### BTA225-600BT

#### Three quadrant triacs high commutation

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