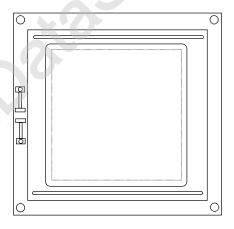


www.DatasheetAl PRODUCT SPECIFICATION HDM128GS12 -1

128 x 128 GRAPHICS (small size) LCD DISPLAY MODULE



HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014 Q.A.: JK REV.: 2.2

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1. MECHANICAL DATA

(1) Product No.	HDM128GS121
(2) Module Size	72.4 (W)mm x 69.9 (H)mm x MAX13.5 (D)mm
	(LED B.L.)
	72.4 (W)mm x 69.9 (H)mm x MAX9.5 (D)mm
	(W/O,EL B.L.)
(3) Dot Size	0.32 (W)mm x 0.32 (H)mm
(4) Dot Pitch	0.35 (W)mm x $0.35 (H)$ mm
(5) Number of Dots	128 (W) x 128 (H)Dots
(6) Duty	1/128
(7) LCD Display Mode	STN: Gray Mode Green Yellow Mode Green Blue Mode
	FS†N:□ Black and White(Normal White/Positive Image)
	□ Black and White(Normal Black/Negative Image)
	Rear Polarizer: 🗆 Reflective 🗆 Transflective 🗀 Transmissive
	□ Transflective(High Transmissive)
(8) Viewing Direction	□ 6 O'clock □ 12 O'clock □O'clock
(9) Backlight	□ W/O □ EL □ LED □ CCFT
(10) LCD Controller	BUILT-IN T6963C (TOSHIBA)
(11) Weight	W/O B/L: obout 51.4 g
	EL B/L: obout 54.3 g
	LED B/L: obout 63 g

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2. ABSOLUTE MAXIMUM RATINGS

(1) ELECTRICAL ABSOLUTE RATINGS

Vss=0V

ITEM	SYMBOL	MIN	MAX	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	7.0	٧	
Power Supply for LC Drive	VDD-VEE	0	25.0	V	
Input Voltage	VI	-0.3	VDD	٧	
Static Electricity	_		_	_	Note 1

Note 1 LCM should be grounded during handling LCM.

(2) ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

	NORMAL TEMP.				WIDE TEMP,			
ITEM	OPERATING		STORAGE		OPERATING		STORAGE	
	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.
Ambient Temperature	0	50	-20	70	-20	70	-30	80
Humidity (Without Condensation)	Note 1,3		Note 2,3		Note 3,4		. Note	3,5

Note 1 Ta ≤ 50°C : 85%RH max

 $Ta > 50^{\circ}C$: Absolute humidity must be lower

than the humidity of 85%RH at 50°C

Note 2 Ta at -20° C will be < 48hrs, at 70°C will be < 120hrs

Note 3 Background color changes slightly depending on ombient temperature.

This phenomenon is reversible.

Note 4 Ta ≤ 70°C : 75%RH max

Ta > 70°C : Absolute humidity must be lower

than the humidity of 75%RH at 70°C

Note 5 To at -30° C will be < 48hrs, at 80°C will be < 120hrs

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3. ELECTRICAL CHARACTERISTICS

($VDD = 5V \pm 10\%$)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Logic Circuit Power Supply	VDD-VSS	_	4.75	5.0	5. 25	>
Input Voltage	VIH	H level	0.8VDD	_	VDD	V
input voitage	VIL	L level	0		0.2VDD	V
Recommended LC Driving Voltage		0°C	_	19.0	20.0	
(Normal Temp. LCM)	VDD-VEE	25℃	16.8	17.6	18.2	٧
	1/12 Bias	50℃	15.7	16,1	_	
		-20t	_	16.9	17.4	
	VDD-VEE 1/12 Bias	-10°C	15.2	16.1	16.7	
Recommended LC Driving Voltage		0°C	15.2	16.1	16.5	
(Wide Temp. LCM)		25ზ	15.2	16.1	16.4	٧
		50℃	15.2	15.9	16.4	
		70°C	14.3	14.9	_	
Suralu Current (LCD)	IDD	VDD = 5.0V	_	_	9	mΑ
Supply Current (LCD) (Normal Temp. LCM)	IEE	VEE = 12.6V	_	-	4	mΑ
LED Power Supply Current	1 LED	VBL = 5VDC	_	240	400	mΑ
LED Averoge Brightness	B(LED)	$(R_{BL} = 3.3\Omega)$	_	32.1	_	cd/m²
EL Power Supply Current		VEL = 110 VAC	_	_	5	mA
EL Average Brightness	B(EL)	400Hz	_	20.18	- .	cd/m²

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4-1.OPTICAL CHARACTERISTICS

(For Normal Temperature Mode LCM)

AT Vo⊭

	ITEM	TEM Cr(Contrast Ratio)		θ(Viev	ving Angle)	ø(Viewing Angle)		
		25	τ	25	J.C.	25°C		
MODE		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	
	Α	3 .	4	40	60	25	30	
R	С	4	6 .	40	60	25	35	
	J	4	6	40	60	25	35	
	А	3	4	40	60	20	30	
S	С	4	6	40	60	25	35	
	J	3.5	6	40	55	20	30	
Ŧ	E	3	4	35	65	20	40	
1	G	6	15	45	90	30	. 50	
no	te	NOTE6			NOTE5			

AT $\phi = 0$, $\theta = 0$.

1TEM .	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Response Time (rise)		90	- 450 90		900		_
	Tr	25°C	_	110	220	ms	NOTE 2
		50℃		65	130		
•	Tf	σO	-	650	1100	-	NOTE 2
Response Time (fall)		25℃		135	250	ms	
		50ზ		80	150		

note:

R: REFLECTIVE

S: TRANSFLECTIVE T: TRANSMISSIVE A: GRAY

C: YELLOW E: BLUE

E: BLUE G: NORMALLY BLACK J: NORMALLY WHITE

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4-2.OPTICAL CHARACTERISTICS

(For Wide Temperature Mode LCM)

AT VOP

	1TEM	Cr(Contrast Ratio)		<i>⊕</i> (Viev	wing Angle)	¢(View	ing Angle)
		25	orc or	25	J.C.	2.	5°C
MODE		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.
	А	3.0	4.0	40	60	28	35
R	С	— :	_	_	_		_
•	J	4.0	6.5	35	52	25	33
	Α	3.0	3.8	35	50	20	25
s	С	-		-	-	-	_
	J	_	_	-	-	_	
_	Α	-	2.5	20	40	15	20
T	G	5	10	50	86	35	50
no	te	NOTE6			NOTE 5	5	

AT Ø=0' θ=0'

1TEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
		-20°	_	2200	4400		
		−10℃	-	940	1880		
D	Tr	30	_	440	880		
Response Time (rise)	i ir	25℃	_	120	240	ms	NOTE 2
		50℃		60	120		
	3	70°C	_	50	100		
		-20°	_	3800	6000		
		−10℃	_	1260	2400		
Response Time (fall)	Tf	Οt	_	620	1200	ms	NOTE 2
(1617)		25℃		180	350	1115	NOIL Z
		50℃	_	80	150		!
		70ზ	_	70	130		

note:

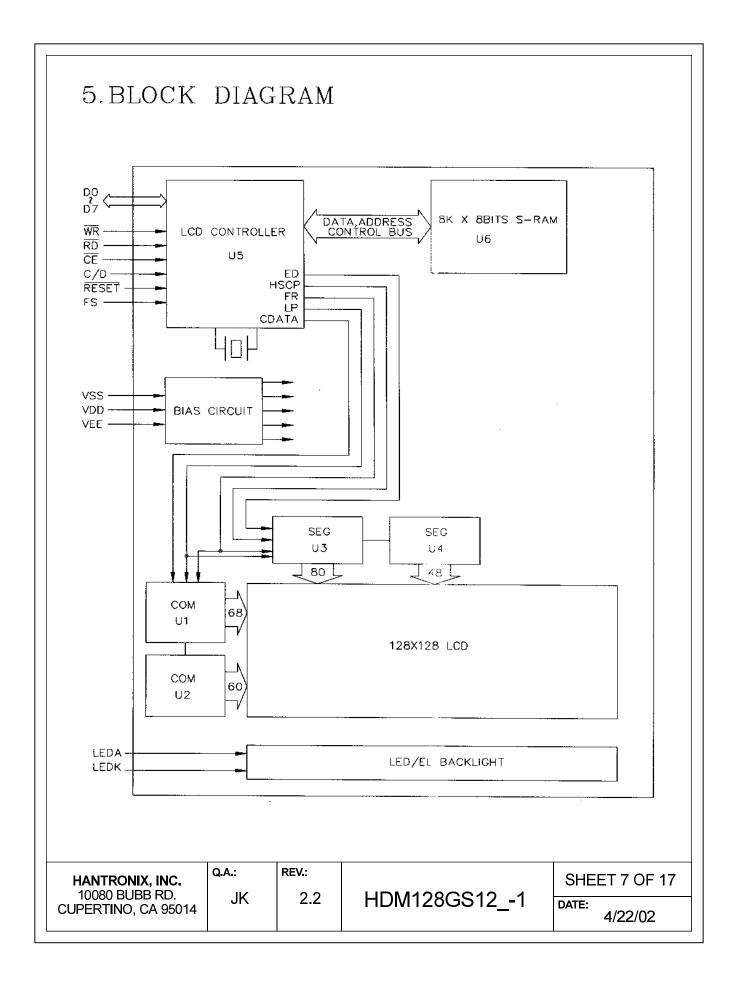
R: REFLECTIVE

S: TRANSFLECTIVE T: TRANSMISSIVE A: GRAY

C: YELLOW E: BLUE

G: NORMALLY BLACK J: NORMALLY WHITE

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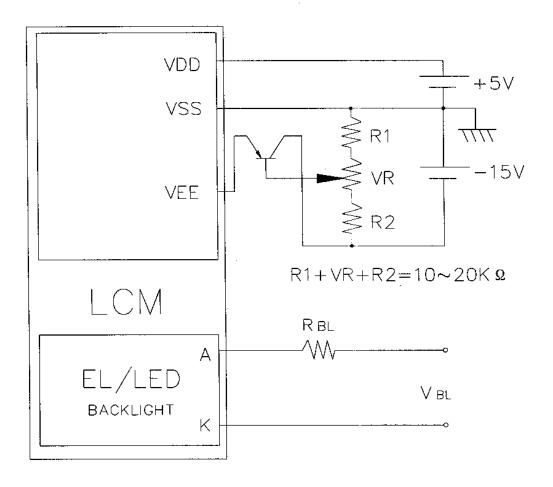


6. INTERNAL PIN CONNECTION

PIN NO.	SYMBOL	FUNCTION						
1	VSS	GROUND						
2	VDD	POWER SUPPLY FOR LOGIC CIRCUIT						
3	VEE	POWER SUPPLY FOR LCD CIRCUIT						
4	WR	DATA WRITE						
5	RD	DATA READ						
6	CE	CHIP ENABLE						
7	C/D	\overline{WR} ="L",C/D="H" : COMMAND WRITE \overline{WR} ="L",C/D="L" : DATA WRITE \overline{RD} ="L",C/D="H" : STATUS READ \overline{RD} ="L".C/D="L" : DATA READ						
8	RESET	CONTROLLER RESET						
9	DO	DATA INPUT/OUTPUT						
10	D1	DATA INPUT/OUTPUT						
11	D2	DATA INPUT/OUTPUT						
12	D3	DATA INPUT/OUTPUT						
13	D4	DATA INPUT/OUTPUT						
14	D5	DATA INPUT/OUTPUT						
15	D6	DATA INPUT/OUTPUT						
16	D7	DATA INPUT/OUTPUT						
17	FS	FONT SELECT CONNECT TO VDD : 6X8 PIXELS/CHARACTER CONNECT TO VSS : 8X8 PIXELS/CHARACTER						
18	NC	NO CONNECTION						
19	LEDK	LED or EL BACKLIGHT						
20	LEDA	LED or EL BACKLIGHT						

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7. POWER SUPPLY



Recommended Value for RBL and VBL

Back	R BL		V BL		
Light Interface	LED	EL	LED	EL	
A,K PIN	3.3 ♀	OΩ	5Vpc	110 Vac 400Hz	

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8. TIMING CHARACTERISTICS

8-1.INTERFACE TIMING

ITEM	ITEM	CONDITION	MIN.	мах.	UNIT	
C/D SET UP TIME	tcds	Fig.	100	-	ns	
C/D HOLD TIME	^t CDH Fig. 10			ùs		
CE,RD,WR CLOCK WIDTH	^t CP, ^t RP, ^t WP	Fig.	80	_	ns	
DATA SET UP TIME	t _{DS}	Fig,	80	-	- ns	
DATA HOLD TIME	t _{DH} Fig. 40		40	_	ns	
ACCESS TIME	^t ACC	Fig.		150	ns	
DATA OUTPUT HOLD TIME	^t OH	Fig.	10	50	ns	

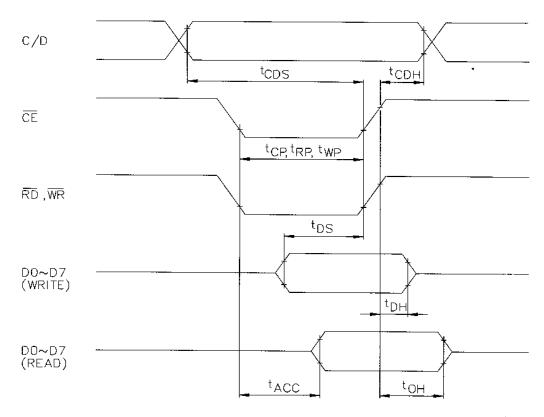
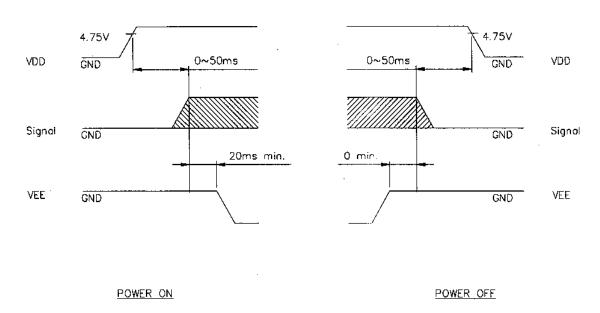


Fig. INTERFACE TIMING CHART

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8-2. POWER ON/OFF TIMING



The missing pixels may occur when the LCM is driven beyond above power interface timing sequence.

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9. DISPLAY PATTERN D7 D6 D5 D4 D3 D2 D1 D0 D7 D6 D5 D4 D3 D2 D1 D0 128 X 128 Dots Matrix 128 Dots 128 Dots Startting dot for the startting address of display RAM. D0~D7 are 8 bits transmitted data where D0 is LSB and D7 is MSB. Q.A.: REV.: HANTRONIX, INC. **SHEET 12 OF 17** 10080 BUBB RD. HDM128GS12_-1 JK 2.2

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10. RELIABILITY TEST

NO	ITEM	CONDITION		STANDARD	NOTE	
1	High Temp. Leaving	70°C	120HR		Appearance without defect	
2	Low Temp. Leaving	−20°C	120HR	·	Appearance without defect	
3	High Temp. & High Humi. Leaving	40°C 90%RH	120HR		Appearance without defect	
4	Thermal Shock	-20°C,3 60°C (1cycle)	,30min-	25°C,5min -25°C,5min	Appearance without defect	5 cycles

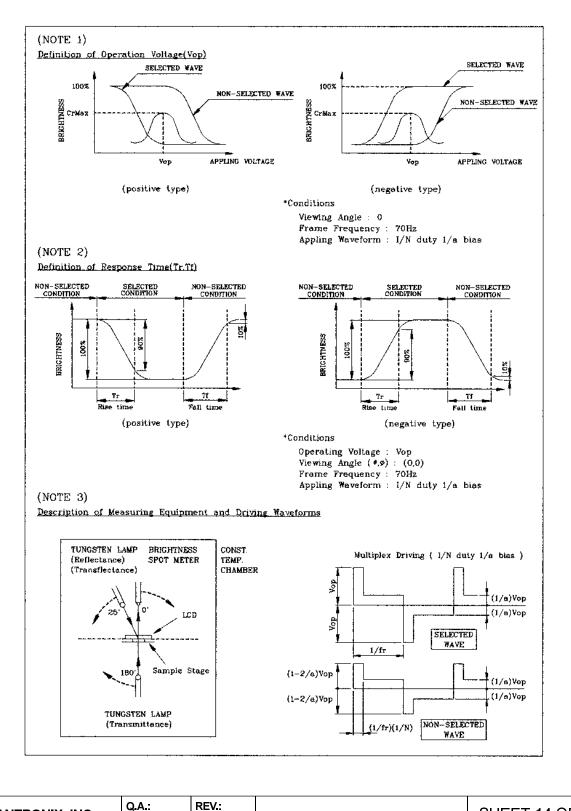
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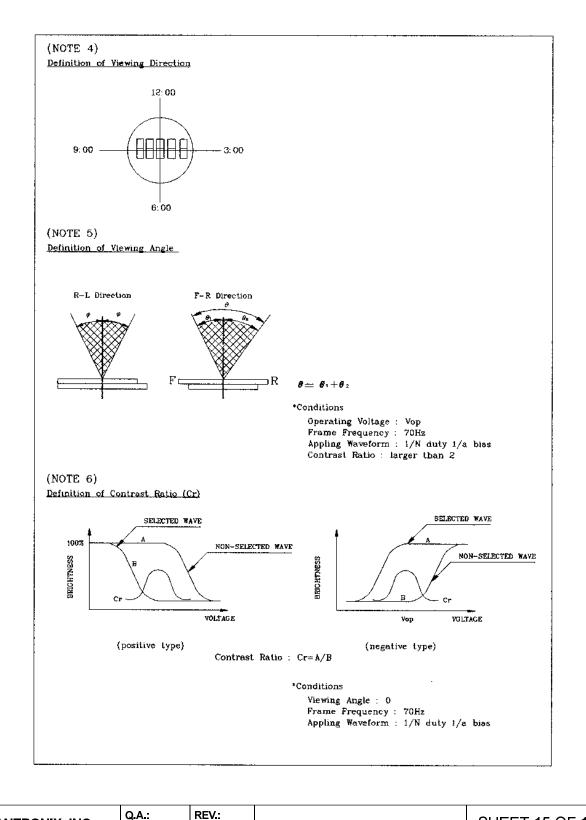
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(2) NOTE:

SAFETY

- 1.If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 2.If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

HANDLING

- 1. Avoid static electricity which can damage the CMOS LSI.
- 2.Do not remove the panel or frame from the module.
- 3. The polarizing plate of the display is very fragile. So, please handle it very carefully.
- 4.Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.

STORAGE

- 1.Store the panel or module in a dark place where the temperature is $25^{\circ}C\pm5^{\circ}C$ and the humidity is below 65% RH.
- 2.Do not place the module near organics solvents or corrosive goses.
- 3.Do not crush, shake, or jolt the module.

· TERMS OF WARRANT

1. Acceptance inspection period

The period is within one month after the arrival of contracted commodity at the buyer's factory site.

2. Applicable warrant period

The period is within twelve months since the date of shipping out under normal using and storage conditions.

. THE OPERATING LIFE TIME OF BACK LIGHT

LED : 50,000HR EL : 5,000HR CCFT : 10,000HR

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