

Ultra Low Capacitance ESD Protection Diode

RSAC16CS

● **Applications**

ESD Protection

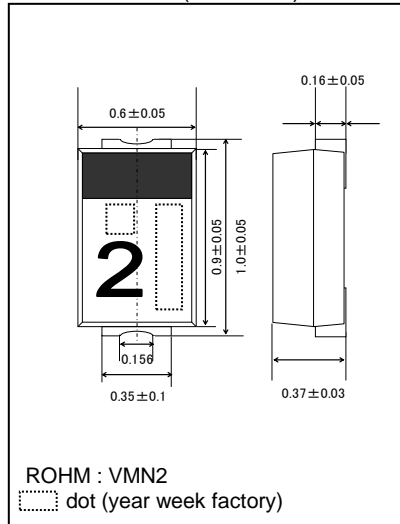
● **Features**

- 1) Ultra small mold type.(VMN2)
- 2) Ultra low capacitance.
- 3) High reliability.
- 4) Bi chip-mounter, automatic mounting is possible.

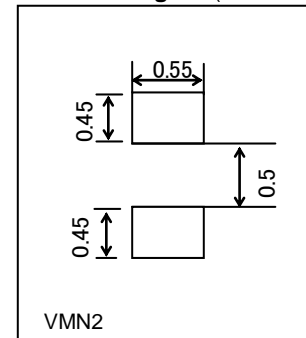
● **Construction**

Silicon epitaxial planer

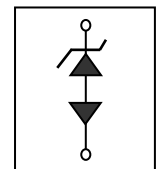
● **Dimensions (Unit : mm)**



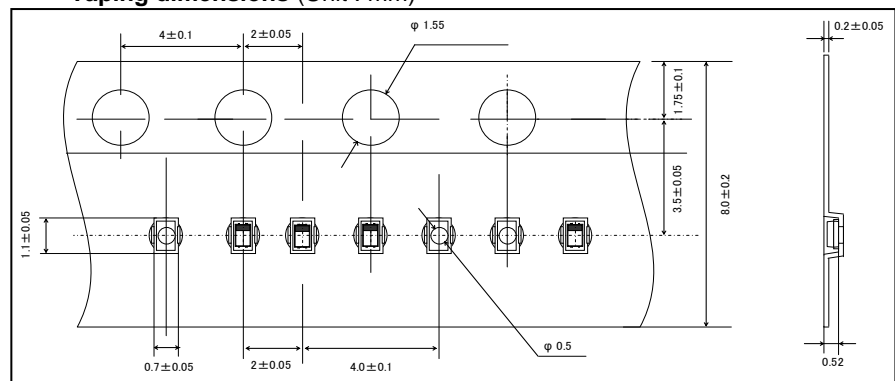
● **Land size figure (Unit : mm)**



● **Structure**



● **Taping dimensions (Unit : mm)**



● **Absolute maximum ratings (Ta=25°C)**

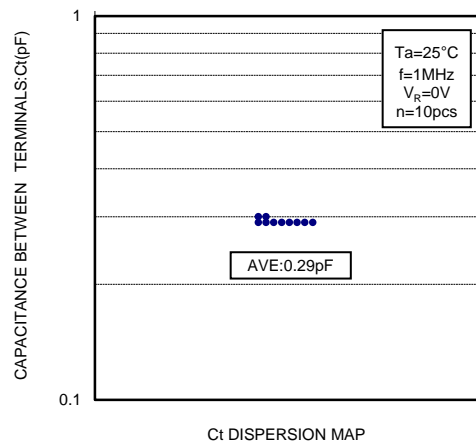
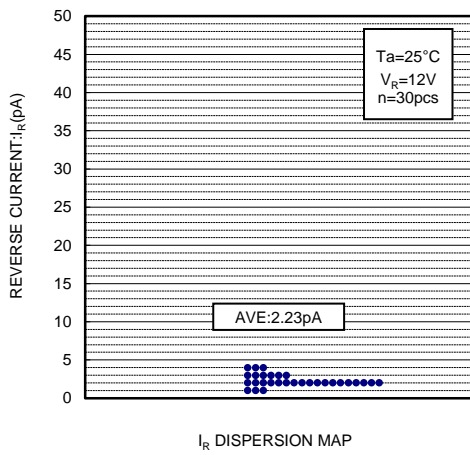
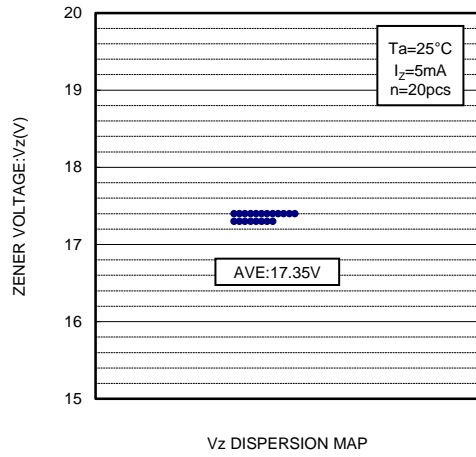
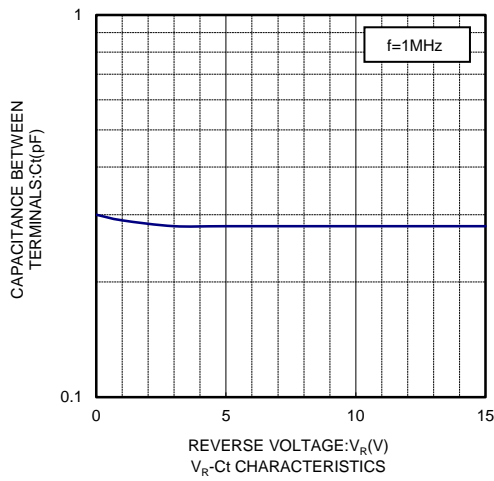
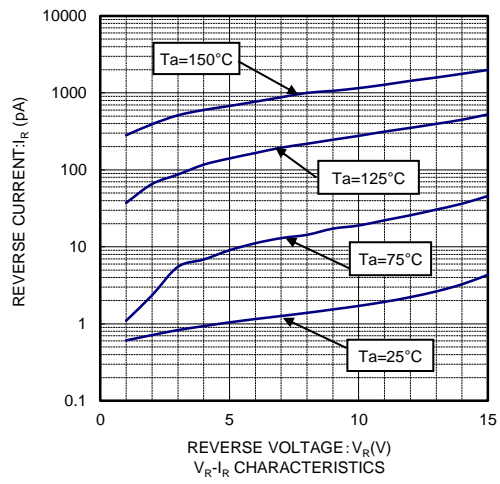
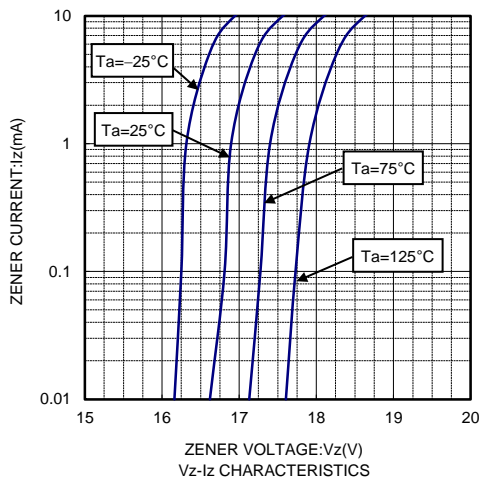
Parameter	Symbol	Limits	Unit
Power dissipation	P	100	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

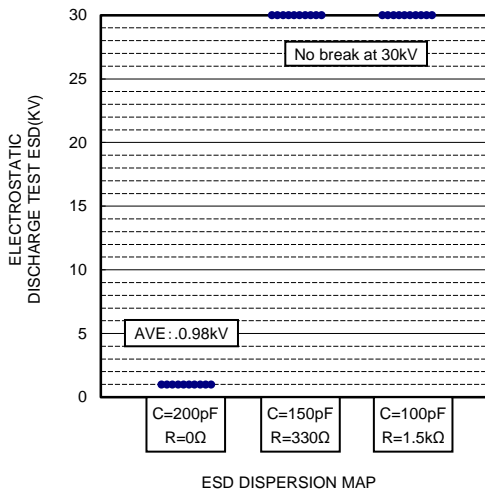
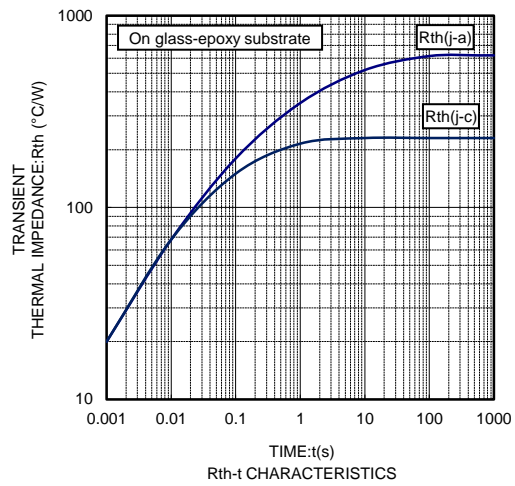
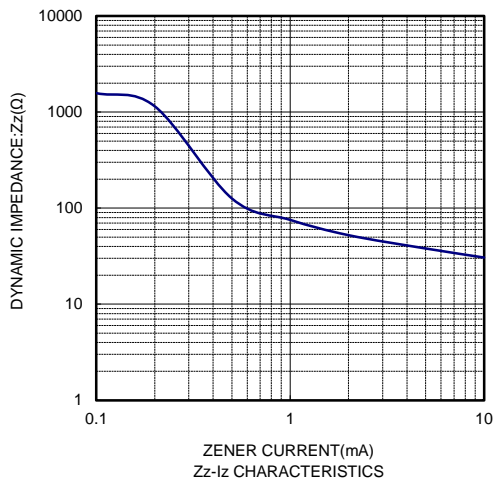
● **Electrical characteristics (Ta=25°C)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Zener voltage	V _z	16.49	-	17.51	V	I _z =5mA
Reverse current	I _R	-	-	0.1	μA	V _R =12V
Capacitance between terminals	C _t	-	0.3	-	pF	V _R =0V, f=1MHz

*1) Zener voltage (V_z) shall be measured at 40ms after loading current.

*2) Electrical characteristic assurance is just only zener direction.





Notes

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