

# **STD123S**

**NPN Silicon Transistor** 

#### **Features**

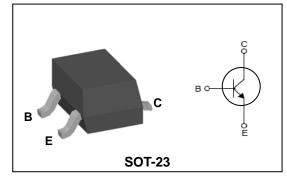
- Low saturation medium current application
- Extremely low collector saturation voltage
- Suitable for low voltage large current drivers
- High DC current gain and large current capability
- Low on resistance :  $R_{ON}=0.6\Omega(Max.)$  ( $I_B=1mA$ )

## **Ordering Information**

Type No.	Marking	Package Code
STD123S	<u>123</u> <u></u> ① ②	SOT-23

①Device Code ② Year&Week Code

## **PIN Connection**



**Absolute maximum ratings** 

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	20	V
Collector-Emitter voltage	$V_{CEO}$	15	V
Emitter-Base voltage	$V_{EBO}$	6.5	V
Collector current	I <sub>C</sub>	1	А
Collector dissipation	P <sub>C</sub> *	350	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

<sup>\* :</sup> Package mounted on 99.5% alumina 10×8×0.1mm

#### **Electrical Characteristics**

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = 50 \mu A, I_E = 0$	20	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=1$ mA, $I_B=0$	15	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = 50 \mu A, I_C = 0$	6.5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 20V, I_{E} = 0$	-	-	0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=6V$ , $I_C=0$	-	-	0.1	μΑ
DC current gain	h <sub>FE</sub>	$V_{CE} = 1V, I_{C} = 100 \text{mA}$	150	-	-	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA	-	0.1	0.3	V
Transistor frequency	$f_{T}$	$V_{CE}$ =5V, $I_{C}$ =50mA	-	260	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}=10V$ , $I_{E}=0$ , $f=1MHz$	-	5	-	рF
On resistance	R <sub>ON</sub>	$f=1KHz$ , $I_B=1mA$ , $V_{IN}=0.3V$	-	0.6	-	Ω

## **Electrical Characteristic Curves**

Fig. 1  $P_C$  -  $T_a$ 

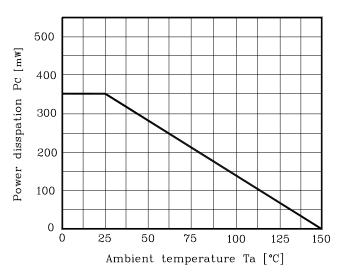


Fig. 2  $C_{Ob}$ - $V_{CB}$ 

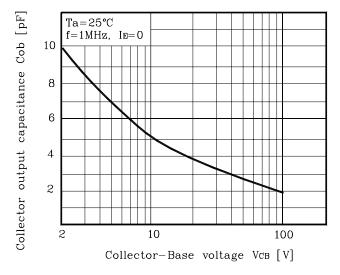


Fig. 5 R<sub>ON</sub>-I<sub>B</sub>

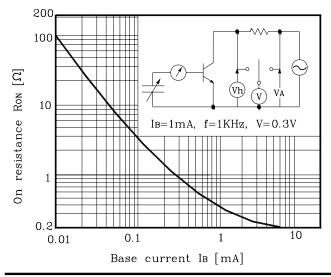


Fig. 2  $V_{CE(sat)}$   $I_C$ 

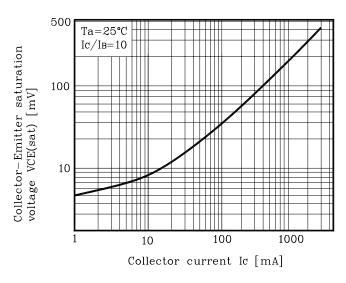
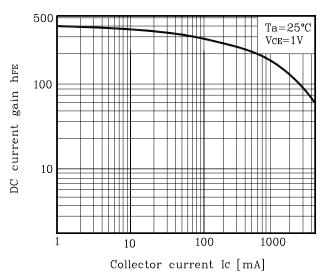
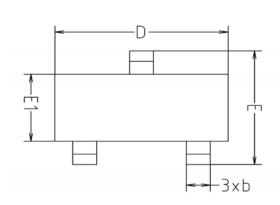


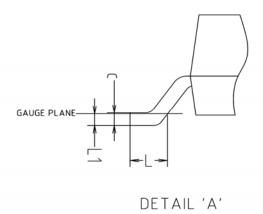
Fig.  $4h_{FE}I_{C}$ 

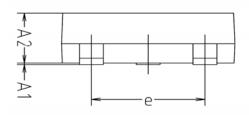


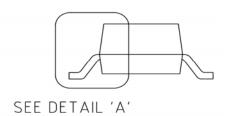
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## **Outline Dimension**



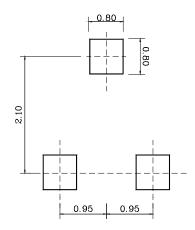






SYMBOL	MILLIMETERS		NOTE	
STITIBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
Е	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1		0.12BSC		

## \*Recommend PCB solder land [Unit: mm]



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