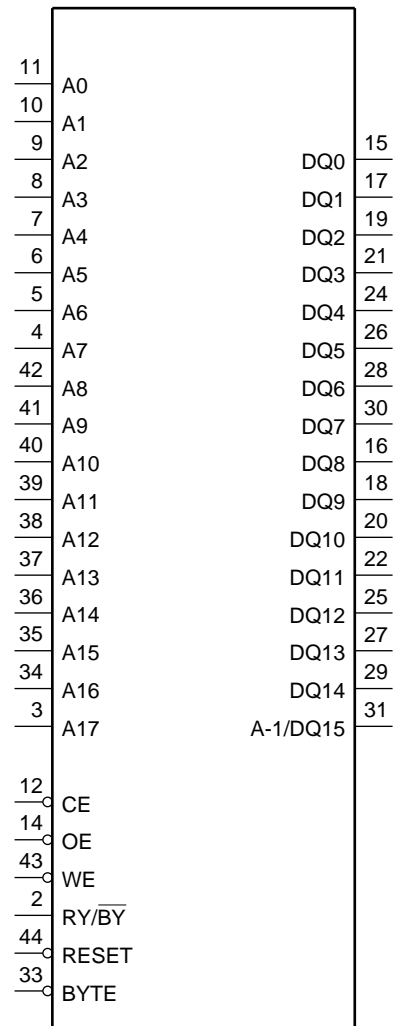
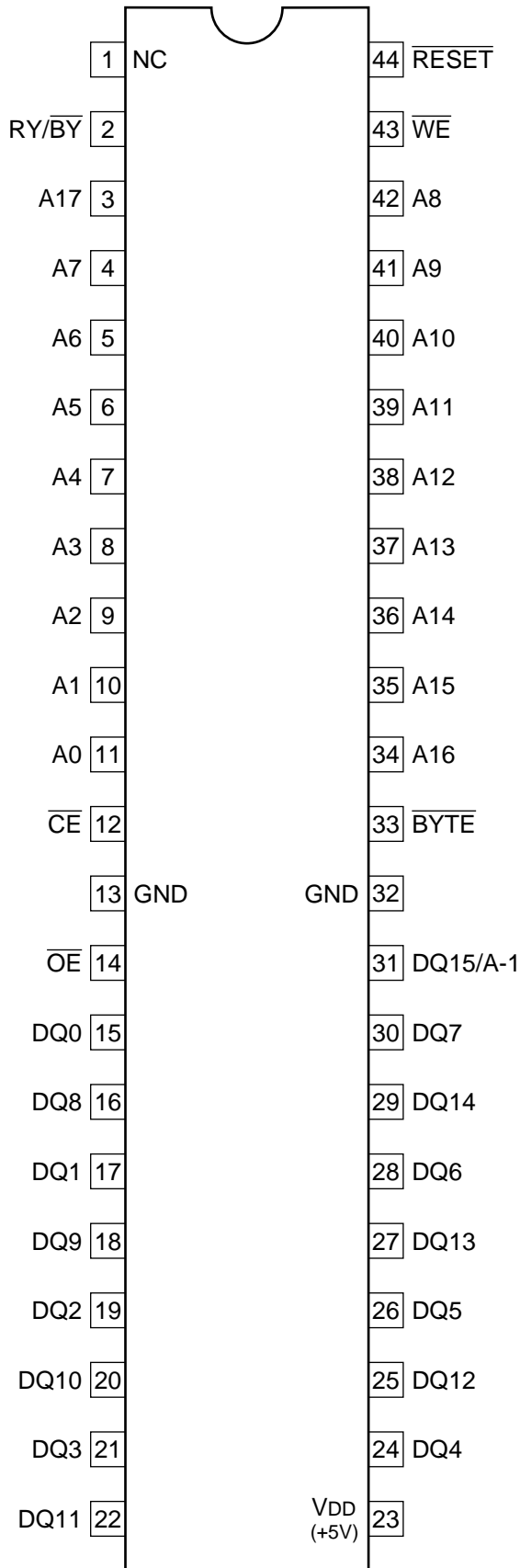


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**C-MOS 4M(524,288X8)-BIT SECTOR ERASE FLASH MEMORY**  
 -TOP VIEW-



**INPUT**  
 A0-A17 ; ADDRESS  
 CE ; CHIP ENABLE  
 OE ; OUTPUT ENABLE  
 WE ; WRITE ENABLE  
 RY/BY ; READY-BUSY  
 RESET ; RESET  
 BYTE ; SELECTS 8-BIT OR 16-BIT MODE

**INPUT/OUTPUT**  
 DQ0-DQ15 ; DATA

