**Power LDMOS transistor** 

Rev. 01 — 27 May 2010

**Objective data sheet** 

## 1. Product profile

### 1.1 General description

140 W LDMOS power transistor for base station applications at frequencies from 2500 MHz to 2700 MHz.

### Table 1. Typical performance

Typical RF performance at  $T_{case} = 25 \ ^{\circ}C$  in a common source class-AB production test circuit.

Mode of operation	f	I <sub>Dq</sub>	$V_{\text{DS}}$	P <sub>L(AV)</sub>	Gp	$\eta_{\bm{D}}$	ACPR <sub>885k</sub>
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)
IS-95	2500 to 2700	1300	28	20	17.0	22	-45 <mark>[1]</mark>

 Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

### **1.2 Features and benefits**

- Excellent ruggedness
- High efficiency
- Low R<sub>th</sub> providing excellent thermal stability
- Designed for low memory effects providing excellent digital pre-distortion capability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### **1.3 Applications**

 RF power amplifiers for W-CDMA base stations and multi carrier applications in the 2500 MHz to 2700 MHz frequency range



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## 2. Pinning information

Pin	Description		Simplified outline	Graphic symbol	
BLF7G2	7L-140 (SOT502A)				
1	drain1				
2	drain2			1 لــــا	
3	gate1				
4	gate2			3	
5	source	<u>[1]</u>		sym112	
BLF7G2	7LS-140 (SOT502B)				
1	drain1				
2	drain2			1 لــــا	
3	gate1				
4	gate2				
5	source	<u>[1]</u>		sym112	

[1] Connected to flange.

## 3. Ordering information

Table 3. Ordering information						
Type number	Packag	ge				
	Name	Description	Version			
BLF7G27L-140	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A			
BLF7G27LS-140	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B			

## 4. Limiting values

### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage		-	65	V
V <sub>GS</sub>	gate-source voltage		-0.5	+13	V
I <sub>D</sub>	drain current		-	<tbd></tbd>	А
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C
-					

## 5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-c)</sub>	thermal resistance from junction to case	$T_{case}$ = 80 °C; $P_L$ = 140 W	<tbd></tbd>	K/W

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### 6. Characteristics

<b>Table 6.</b> $T_j = 25 \ ^{\circ}C$	Characteristics Cunless otherwise specified.					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS}$ = 0 V; $I_D$ = 1 mA	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	$V_{DS}$ = 10 V; $I_{D}$ = 216 mA	1.5	1.8	2.3	V
I <sub>DSS</sub>	drain leakage current	$V_{GS}$ = 0 V; $V_{DS}$ = 28 V	-	-	5	μA
I <sub>DSX</sub>	drain cut-off current	$\label{eq:VGS} \begin{array}{l} V_{GS} = V_{GS(th)} + 3.75 \; V; \\ V_{DS} = 10 \; V \end{array}$	-	<tbd></tbd>	-	A
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 11 V; $V_{DS}$ = 0 V	-	-	500	nA
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_{D}$ = 7.56 A	-	<tbd></tbd>	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 7.56 A$	-	<tbd></tbd>	-	Ω

## 7. Test information

### Table 7. Functional test information

Mode of operation: 1-carrier N-CDMA, single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz;  $f_1 = 2600$  MHz;  $f_2 = 2700$  MHz; RF performance at  $V_{DS} = 28$  V;  $I_{Dq} = 1300$  mA;  $T_{case} = 25$  °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P <sub>L(AV)</sub>	average output power		-	20	-	W
G <sub>p</sub>	power gain		-	17.0	-	dB
RL <sub>in</sub>	input return loss		-	-10	-	dB
$\eta_D$	drain efficiency		-	22	-	%
ACPR <sub>885k</sub>	adjacent channel power ratio (885 kHz)		-	-45	-	dBc

### 7.1 Ruggedness in class-AB operation

The BLF7G27L-140 and BLF7G27LS-140 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS} = 28 \text{ V}; I_{Dq} = 1300 \text{ mA}; P_L = 140 \text{ W} (CW); f = 2500 \text{ MHz}.$ 

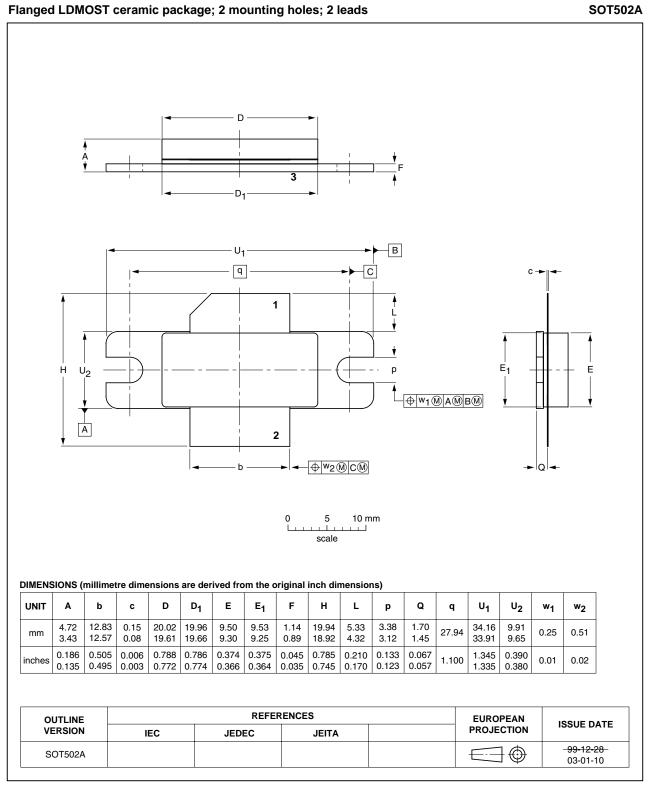
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#### **Package outline** 8.



Package outline SOT502A Fig 1.

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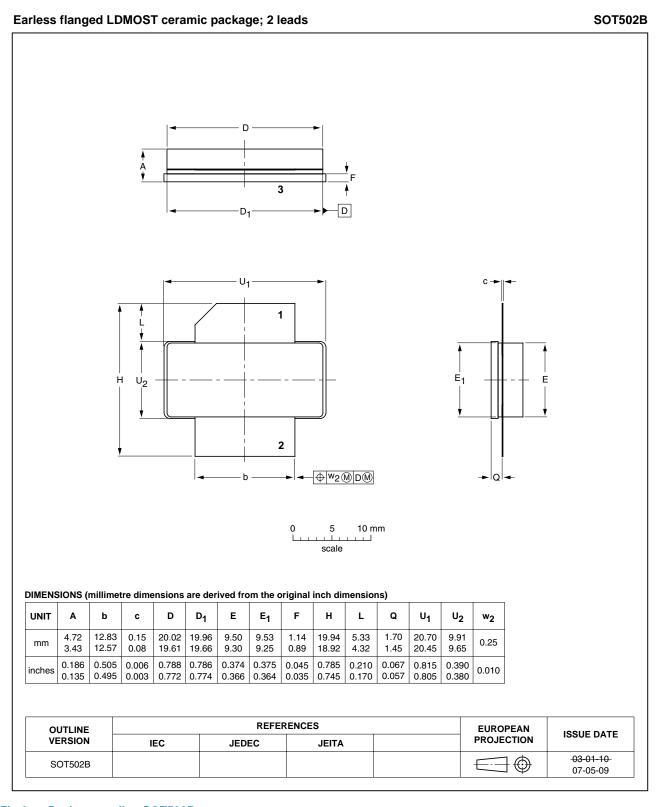


Fig 2. Package outline SOT502B

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## 9. Abbreviations

Table 8.	Abbreviations
Acronym	Description
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
IS-95	Interim Standard 95
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
N-CDMA	Narrowband Code Division Multiple Access
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio

## 10. Revision history

Table 9.         Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF7G27L-140_7G27LS-140_1	20100527	Objective data sheet	-	-

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### 11.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Date of release: 27 May 2010 Document identifier: BLF7G27L-140\_7G27LS-140\_1