# **NCX2202**

# Low voltage comparator; open-drain output

Rev. 1 — 20 July 2011

**Product data sheet** 

### 1. General description

The NCX2202 is a single low voltage low power comparator with open drain output.

The NCX2202 has a very low supply current of 6  $\mu$ A and is guaranteed to operate at a low voltage of 1.3 V and is fully operational up to 5.5 V which makes this device convenient for use in both 3.0 V and 5.0 V systems.

#### 2. Features and benefits

- Wide supply voltage range from 1.3 V to 5.5 V (functional operating range)
- Rail-to-rail input/output performance
- Very low supply current of 6 μA (typical)
- Very low-power consumption
- No phase inversion with overdriven input signals
- Internal hysteresis
- Propagation delay of 0.8 μs (typical)
- ESD protection:
  - ♦ HBM JESD22-A114F Class 3A exceeds 1500 V
  - ◆ CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from –40 °C to +85 °C

## 3. Applications

- Cellular telephones
- Alarm and security systems
- Personal Digital assistants



Low voltage comparator; open-drain output

## 4. Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range	Name	Description	Version			
NCX2202GW	–40 °C to +85 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
NCX2202GM	–40 °C to +85 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1.45 $\times$ 0.5 mm	SOT886			

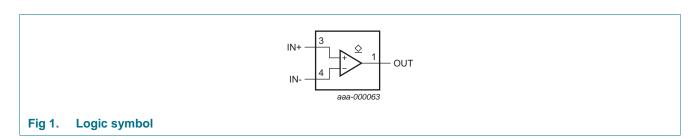
## 5. Marking

#### Table 2. Marking codes

Type number	Marking <sup>[1]</sup>
NCX2202GW	qa
NCX2202GM	qa

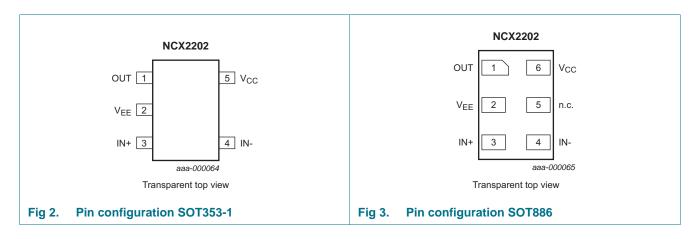
<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 6. Functional diagram



## 7. Pinning information

#### 7.1 Pinning



NCX2202

#### Low voltage comparator; open-drain output

### 7.2 Pin description

Table 3. Pin description

Symbol	Pin		Description
	SOT353-1	SOT886	
OUT	1	1	comparator output (open-drain)
$V_{EE}$	2	2	supply voltage
IN+	3	3	comparator input (positive)
IN-	4	4	comparator input (negative)
n.c.	-	5	not connected
$V_{CC}$	5	6	supply voltage

## 8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to V<sub>EE</sub>.

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		-	7.0	V
$V_{I}$	input voltage	IN-, IN+ inputs	-0.2	$V_{CC} + 0.2$	V
Vo	output voltage		V <sub>EE</sub> - 0.5	7.0	V
t <sub>sc(o)</sub>	output short-circuit time		<u>[1]</u> _	indefinite	S
T <sub>j(max)</sub>	maximum junction temperature		-	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}$	-	250	mW

<sup>[1]</sup> The maximum total power dissipation must not be exceeded.

## 9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CC}$	supply voltage	$V_{CC}$ to $V_{EE}$				
		full spec operating range	1.6	-	5.5	V
		functional operating range	1.3	-	5.5	V
VI	input voltage		$V_{EE}$	-	$V_{CC}$	V
Vo	output voltage		$V_{EE}$	-	5.5	V
T <sub>amb</sub>	ambient temperature		-40	-	+85	°C

Low voltage comparator; open-drain output

### 10. Static characteristics

Table 6. Static characteristics

At recommended operating conditions.  $V_{CC} = 1.6 \text{ V}$  to 5.5 V,  $V_{EE} = 0 \text{ V}$ ;  $V_{CM} = 0.5 V_{CC}$  unless otherwise specified.

Symbol	Parameter	Conditions			25 °C		-40 °C t	o +85 °C	mV mV mV V V V v nA V mA
				Min	Тур	Max	Min	Max	
$V_{H}$	hysteresis voltage			6	9	13	-	-	mV
		V <sub>CC</sub> = 1.3 V		-	20	-	-	-	mV
V <sub>I(offset)</sub>	offset input voltage		<u>[1]</u>	-30	0.5	+30	-30	+30	mV
		V <sub>CC</sub> = 1.3 V	[1]	-	3	-	-	-	mV
V <sub>OL</sub>	LOW-level output voltage	$I_{O} = 0.5 \text{ mA}; V_{CC} = 1.3 \text{ V}$		-	0.05	-	-	-	V
		$I_O = 0.5 \text{ mA}; V_{CC} = 1.6 \text{ V}$	mA; $V_{CC} = 3.0 \text{ V}$ - 0.14	-	-	0.25	V		
		$I_O = 3 \text{ mA}; V_{CC} = 3.0 \text{ V}$		-	0.14	-	-	0.3	V
		$I_{O} = 5 \text{ mA}; V_{CC} = 5.5 \text{ V}$		-	0.20	-	-	0.3	V
l <sub>OZ</sub>	OFF-state output current	$IN- = V_{EE}; IN+ = V_{CC};$ $V_O = 5.5 V$		-	3	-	-	-	nA
$V_{CM}$	common-mode voltage	$V_{CC} = 1.3 \text{ V to } 5.5 \text{ V}$		-	$V_{\text{EE}}$ to $V_{\text{CC}}$	-	-	-	V
I <sub>OS</sub>	output short-circuit current	$V_{CC} = 5.5 \text{ V}; V_O = V_{CC}$		-	68	-	-	-	mA
CMRR	common-mode rejection ratio	$\Delta V_{CM} = V_{CC}$		-	70	-	-	-	dB
PSRR	power supply rejection ratio	$\Delta V_{CC}$ = 1.95 V		45	80	-	-	-	dB
I <sub>IB</sub>	input bias current			-	1.0	-	-	-	pΑ
I <sub>CC</sub>	supply current			-	6.0	-	-	9.0	μΑ
icc	эарріу банені				0.0			3.0	μ

<sup>[1]</sup> Differential input switching level is guaranteed at the minimum or maximum offset voltage, minus or plus half the maximum hysteresis voltage.

#### Low voltage comparator; open-drain output

## 11. Dynamic characteristics

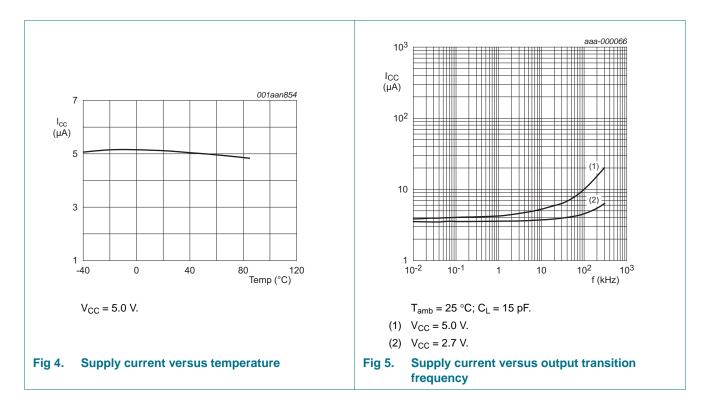
Table 7. Dynamic characteristics

Voltages are referenced to V<sub>EE</sub> (V<sub>EE</sub> = 0 V); V<sub>CC</sub> = 1.6 V to 5.5 V; V<sub>CM</sub> = 0.5 V<sub>CC</sub> unless otherwise specified.

Symbol	Parameter	Conditions		25 °C		Unit
			Min	Тур	Max	
t <sub>pd</sub>	propagation delay	20 mV overdrive; C <sub>L</sub> = 15 pF [1]	-	8.0	-	μS
t <sub>t</sub>	transition time	HIGH to LOW; $V_{CC} = 5.5 \text{ V}$ ; $C_L = 50 \text{ pF}$	-	10	-	ns

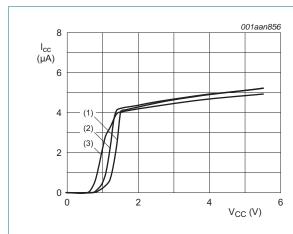
<sup>[1]</sup>  $t_{pd}$  is the same as  $t_{PLZ}$  and  $t_{PZL}$ ;  $t_{PLZ}$  is the time that the output gets actually disabled.

## 12. Graphs



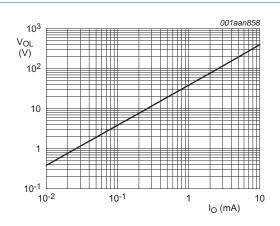
<sup>[2]</sup> Input signal: 1 kHz, squarewave signal with 10 ns edge rate.

#### Low voltage comparator; open-drain output



- (1)  $T_{amb} = -40 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = 85 \, ^{\circ}C$ .

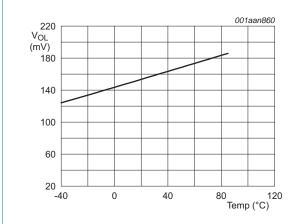
Fig 6. Supply current versus supply voltage



$$T_{amb}$$
 = 25 °C.

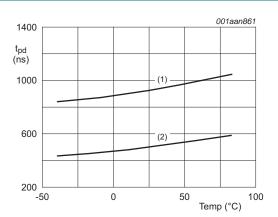
$$V_{CC} = 5.0 \text{ V}.$$

Fig 7. LOW-level output voltage versus output current



 $I_{O} = 4.0 \text{ mA}.$  $V_{CC} = 5.0 \text{ V}.$ 

Fig 8. LOW-level output voltage versus temperature

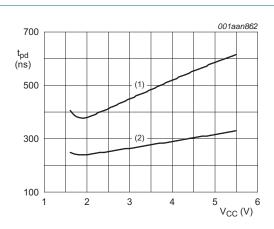


 $V_{CC} = 5.0 \text{ V}$ ; input overdrive = 50 mV.

- (1) t<sub>PLZ</sub>.
- (2) t<sub>PZL</sub>.

Fig 9. Propagation delay versus temperature

#### Low voltage comparator; open-drain output



T<sub>amb</sub> = 25 °C; input overdrive = 100 mV.

- (1) t<sub>PLZ</sub>.
- (2) t<sub>PZL</sub>.

Fig 10. Propagation delay versus supply voltage.

Low voltage comparator; open-drain output

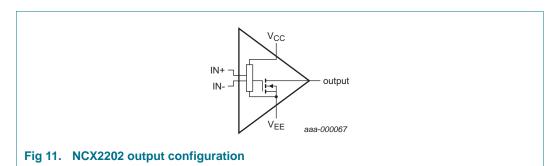
## 13. Application information

#### 13.1 Operating description

The NCX2202 is a single low voltage low power comparator with open drain output. This device is designed for use with a pull-up resistor to define the output switching levels. This device consumes only 6  $\mu A$  of supply current while achieving a typical propagation delay of 0.8  $\mu s$  at a 20 mV input overdrive. Figure 9 and Figure 10 show propagation delay with various input overdrives. This comparator is guaranteed to operate at a low voltage of 1.3 V up to 5.5 V. The common-mode input voltage range extends 0.1 V beyond the upper and lower rail without phase inversion or other adverse effects. This device has a typical internal hysteresis of 9.0 mV. This allows for greater noise immunity and clean output switching.

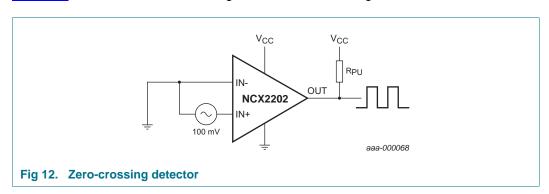
#### 13.2 Output stage

The NCX2202 has an N-channel output stage that has capability of sinking the output to  $V_{\text{FF}}$  with a load ranging up to 5.0 mA. See Figure 11



#### 13.3 Zero-crossing detector

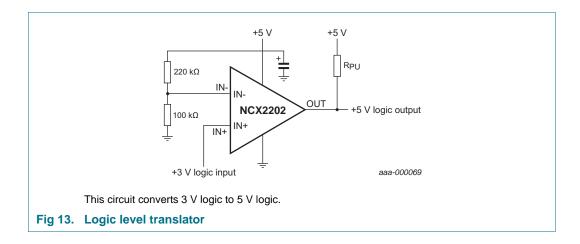
Figure 12 shows the NCX2202 configured as a zero-crossing detector.



#### 13.4 Logic level translator

Figure 13 shows the NCX2202 configured as a logic level translator.

#### Low voltage comparator; open-drain output

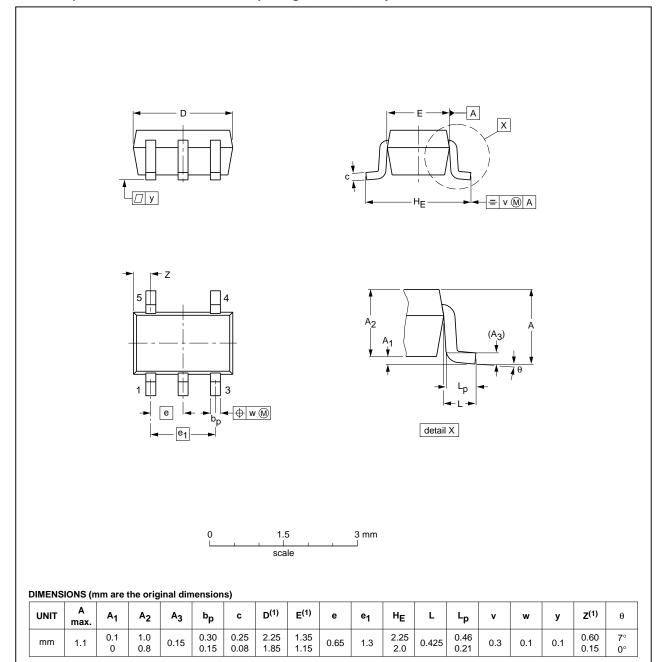


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## 14. Package outline

#### TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



#### Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT353-1		MO-203	SC-88A		<del>-00-09-01-</del> 03-02-19	

Fig 14. Package outline SOT353-1 (TSSOP5)

NCX220

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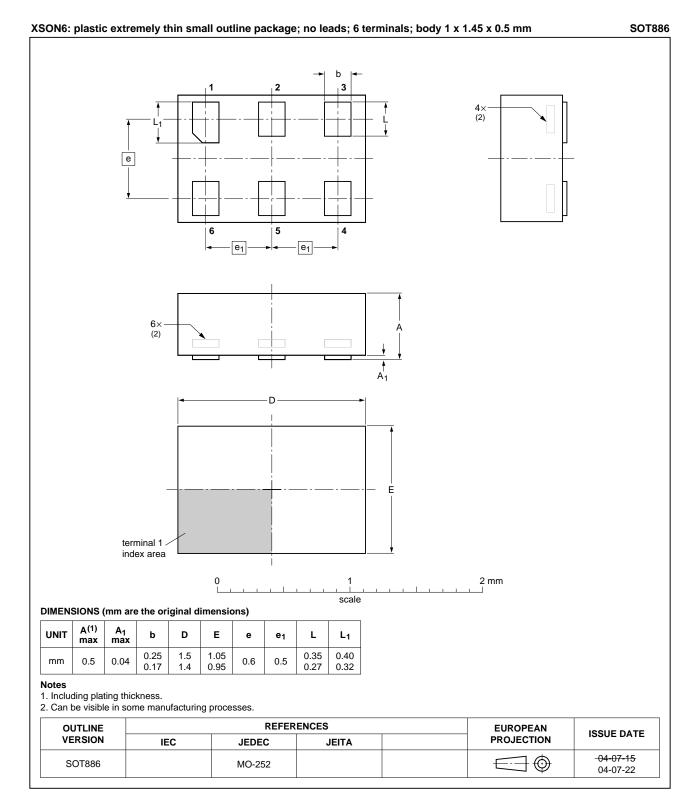


Fig 15. Package outline SOT886 (XSON6)

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## 15. Abbreviations

#### Table 8. Abbreviations

Acronym	Description
CDM	Charged Device Model
ESD	ElectroStatic Discharge
HBM	Human Body Model

## 16. Revision history

#### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
NCX2202 v.1	20110720	Product data sheet	-	-

#### Low voltage comparator; open-drain output

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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#### Low voltage comparator; open-drain output

### 19. Contents

1	General description
2	Features and benefits
3	Applications
4	Ordering information
5	Marking 2
6	Functional diagram 2
7	Pinning information 2
7.1	Pinning
7.2	Pin description
8	Limiting values 3
9	Recommended operating conditions 3
10	Static characteristics 4
11	Dynamic characteristics 5
12	Graphs 5
13	Application information 8
13.1	Operating description 8
13.2	Output stage
13.3	Zero-crossing detector 8
13.4	Logic level translator 8
14	Package outline
15	Abbreviations 12
16	Revision history 12
17	Legal information
17.1	Data sheet status
17.2	Definitions
17.3	Disclaimers
17.4	Trademarks14
18	Contact information 14
19	Contents 15

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