

ULTRA LOW MAGNETIC MOMENT PIN <u>DIODE FOR MRI APPLICATIONS</u>

Rohs Compliant



DESCRIPTION

The UMX5101 PIN diode series was designed to provide ultra low magnetic PIN diodes for in bore surface coil applications associated with higher field strength (3T and greater) MR scanners. These PIN diodes produce the minimum artifacts (magnetic field distortions) available in the industry, today. The diodes have been tested in magnetic fields of ± 7 Tesla.

The UMX5101 PIN diodes have a magnetic moment at 7 T of 2E-8 (J/T).

The diodes are offered in a surface mount package. The SM package utilizes a square end cap to mark the cathode. The anode is round. The fully SOGO passivated PIN diode chip is full face metallurgically bonded to high conductive pins for lower thermal and electrical resistances. The PIN diodes feature low forward bias resistance and high zero bias impedance. The UMX5101 PIN diodes are characterized at 64, 128, and 300 MHz. The UMX5101 meets RoHS requirements per EU Directive 2002/95/EC.

ABSOLUTE MAXIMUM RATINGS AT 25° C (UNLESS OTHERWISE SPECIFIED) Value **Symbol** Unit Rating Peak Repetitive Reverse Voltage 100 V_{RRM} Working Peak Reverse Voltage 100 ٧ V_{RWM} DC Blocking Voltage V_{R} 100 75 ٧ **RMS** Reverse Voltage V_R(RMS) Storage Temperature T stg -65 to +175 ٥С Operating Temperature Non-Repetitive Peak T op -65 to +150 ٥С

THERMAL CHARACTERISTICS AT 25° C					
(UNLESS OTHERWISE SPECIFIED)					
Thermal Resistance	-		_		
UMX5101SM	θ	20	°C/Watt		

IMPORTANT: For the most current data, consult our website: www.MICROSEMI.com



KEY FEATURES

- Ultra low magnetic construction
- SOGO passivated chip
- Thermally matched configuration
- RoHS compliant ¹
- Low capacitance at 0 V bias
- Low conductance at 0 V bias
- Metallurgical bond
- Fused-in-glass construction
- Non cavity design
- Available in surface mount package.
- Compatible with automatic insertion equipment
- 1- These devices are supplied with Silver terminations. Other terminal finishes may be available on request. Consult factory for details.

APPLICATIONS/BENEFITS

- High B Field (3T+) in bore APPLICATIONS:
- Active or semi-active (not passive)
- MR blocking circuits
- MR detuning circuits
- MR disable circuits
- MR receiver protector circuits



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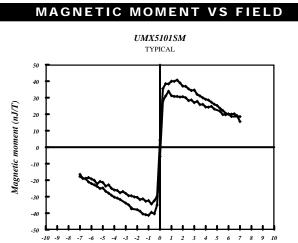
ELECTRICAL PARAMETERS @ 25°C (UNLESS OTHERWISE SPECIFIED)									
Parameter	Symbol	Conditions	Min	Тур.	Max	Units			
Forward Voltage (Note 1)	V _F	I _F = 100 mA		0.82	1.0	V			
Reverse Break Down Voltage	V_{BR}	I _R = 10 uA	100			V			
Reverse Current	I _R	V _R = 100 V			10	uA			
Inductance	Ls			800		рН			
Magnetic moment		@ 7T		2E-8		J/T			
	m	@ 1T		5E-8					
Mass Susceptibility	χ ρ	@ 7T		-2.7E-11		m³/kg			
		@ 1T		1.2E-9					
Volume Susceptibility	χ	>1T to 7T		-2.4E-7		SI			
		<1 T		1.0E-5		31			
Capacitance	Ст	V _R = 0 V, F = 1 MHz		2.5	3.0	pF			
		V _R = 100 V, F = 1 MHz		1.0	1.2				
Parallel Resistance	R₽	V _R = 0 V, F = 64 MHz	10	30		kΩ			
		V _R = 30 V, F = 64 MHz	800	1000					
Series Resistance	Rs	If = 100 mA F = 64 MHz		0.8	1.0	Ω			
Lifetime	τ	If = 10 mA	2	2.5		us			

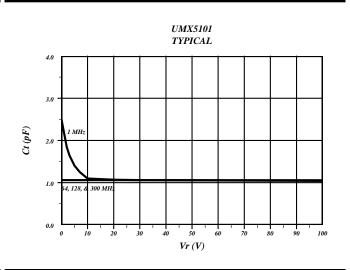


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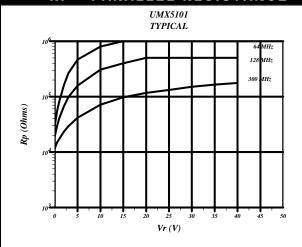




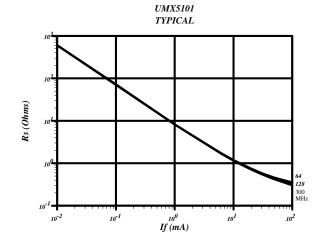
C-V CURVES

RP - PARALLEL RESISTANCE

Field (T)



RS VS IF





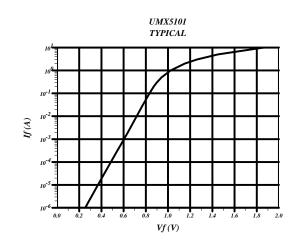
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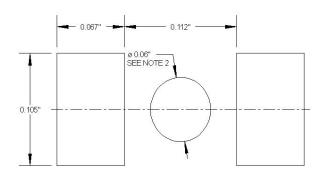
RoHS COMPLIANT



VF VS IF

SM STYLE SOLDER FOOTPRINT





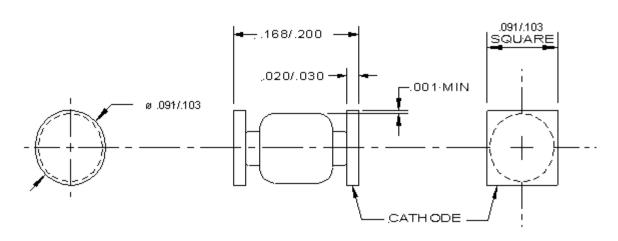
A SIZE (STANDARD SMALL SQUARE END CAP OUTLINE)

NOTES:

- These dimensions will match the terminals and provide for additional solder fillets at the outboard ends at least as wide as the terminals themselves, assuming accuracy of device placement within .005 inches.
- If the mounting method chosen requires use of an adhesive separate from the solder compound, a round (or square) spot of cement as shown should be centrally located.

Dimensions shown are in inches

"SM" STYLE PACKAGE OUTLINE



DIMENSIONS SHOWN ARE IN INCHES