



Vishay General Semiconductor

Dual Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.32 \text{ V}$ at $I_F = 5.0 \text{ A}$

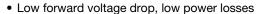




PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 15 A			
V _{RRM}	60 V			
I _{FSM}	200 A			
V _F at I _F = 15 A	0.45 V			
T _J max.	150 °C			

FEATURES

• Trench MOS Schottky technology



• High efficiency operation

Solder dip 275 °C max. 10 s, per JESD 22-B106
 RoHS

 Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VFT30L60C	UNIT	
Maximum repetitive peak reverse voltage		V_{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	30	А	
	per diode		15		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	200	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Isolation voltage from termal to heatsink t = 1 min		V _{AC}	1500	V	
Operating junction and storage temperature range		T _J , T _{STG}	- 40 to + 150	°C	

VFT30L60C

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5.0 A	T _A = 25 °C	V _F ⁽¹⁾	0.43	-	V	
	I _F = 7.5 A			0.46	-		
	I _F = 15 A			0.51	0.60		
	I _F = 5.0 A	T _A = 125 °C		0.32	-		
	I _F = 7.5 A			0.36	-		
	I _F = 15 A			0.45	0.57		
Reverse current per diode	V _R = 60 V	$T_A = 25 ^{\circ}\text{C}$ $T_A = 125 ^{\circ}\text{C}$	I _R ⁽²⁾	-	4.0	- mA	
	v _R = 00 v			27	110		

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VFT30L60C	UNIT	
Typical thermal resistance	per diode	$R_{ heta JC}$	5.0	°C/W	
	per device		3.5	C/VV	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT30L60C-E3/4W	1.75	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

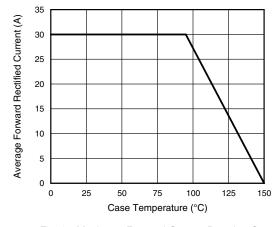


Fig. 1 - Maximum Forward Current Derating Curve

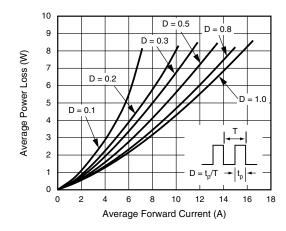


Fig. 2 - Forward Power Dissipation Characteristics Per Diode



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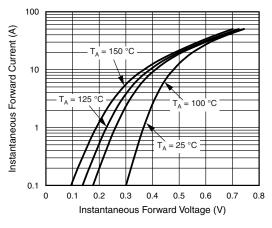


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

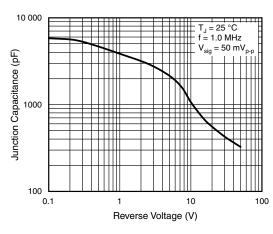


Fig. 5 - Typical Transient Thermal Impedance Per Diode

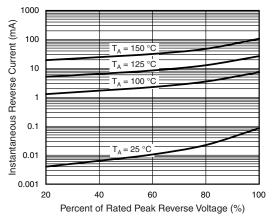


Fig. 4 - Typical Reverse Characteristics Per Diode

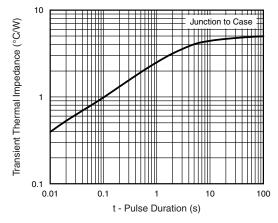
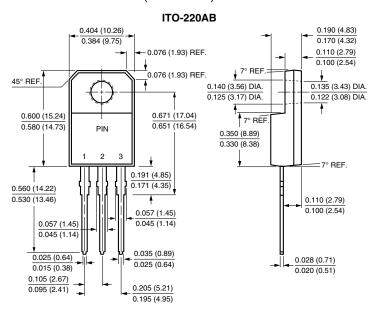


Fig. 6 - Typical Junction Capacitance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)







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