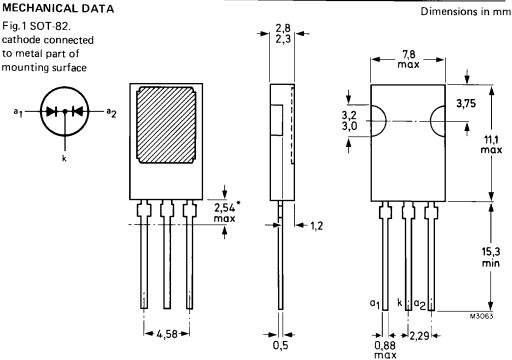
ULTRA FAST-RECOVERY DOUBLE RECTIFIER DIODES

Glass-passivated, high-efficiency double rectifier diodes in plastic envelopes, featuring low forward voltage drop, ultra fast reverse recovery times and soft recovery characteristic. They are intended for use in switched-mode power supplies and high-frequency circuits in general, where both low conduction losses and low switching losses are essential. Their single chip (monolithic) construction ensures excellent matching of the forward and switching characteristics of the two halves, allowing parallel operation without the need for derating. The series consists of common cathode types.

QUICK REFERENCE DATA

Per diode, unless otherwise stated	ss otherwise stated		7–50	100	150	200	
Repetitive peak reverse voltage	V_{RRM}	max.	50	100	150	200	٧
Output current (both diodes conducting)	10	max.	10				А
Forward voltage	٧ _F	<	0.85				V
Reverse recovery time	t _{rr}	<	20				ns



*Within this region the cross-section of the leads is uncontrolled.

Net mass: 2 q

Accessories supplied on request: see data sheets Mounting instructions and accessories for SOT-82 envelopes.

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134).

Voltages (per diode)		BYQ	27–50	100	150	200	
Repetitive peak reverse voltage	V_{RRM}	max.	50	100	150	200	٧
Crest working reverse voltage	V_{RWM}	max.	50	100	150	200	V
Continuous reverse voltage	V_{R}	max.	50	100	150	200	V
Currents (both diodes conducting; note 1)					~		
Output current; switching losses negligible up to 500 kHz; square wave; δ = 0.5; up to T_{mb} = 120 °C	10	max.		1:	0		А
sinusoidal; up to $T_{mb} = 118 {}^{\circ}C$	10	max.		1			Α
R.M.S. forward current	IF(RMS)	max.		1-	4		Α
Repetitive peak forward current $t_p = 20 \mu s$, $\delta = 0.02$ (per diode)	IFRM	max.		81	0		Α
Non-repetitive peak forward current (per diode) half sine-wave; T _j = 150 °C prior to surge; with reapplied V _{RWM} max							
t = 10 ms t = 8.3 ms	FSM	max.		50			A
1 ² t for fusing (t = 10 ms, per diode)	IFSM I ² t	max.		60			A A ² s
	1-1	max.		12.	0		A-s
Temperatures							
Storage temperature	T_{stg}		-40 to	+150	0		οС
Junction temperature	Tj	max.		150	0		oC
THERMAL RESISTANCE							
From junction to mounting base (both diodes conducting)	R _{th j-mb}	=		3.	0		K/W
From junction to mounting base (per diode)	R _{th j-mb} =		4.5		5		K/W
Influence of mounting method							
1. Heatsink-mounted with clip (see mounting instructions)							
Thermal resistance from mounting base to heatsink							
a. with heatsink compound	R _{th mb-h}	=		0.3	3		K/W
b. with heatsink compound and 0.06 mm maximum mica insulator (56354)	R _{th} mb-h	_ =		1.4	4		K/W
c. without heatsink compound	R _{th mb-h} =		1.4			K/W	
2. Free air operation	d e e e e e						

The quoted value of R $_{\mbox{th}}$ j-a should be used only when no leads of other dissipating components run to the same tie point.

Thermal resistance from junction to ambient in free air:

mounted on a printed circuit board at any device lead

length and with copper laminate on the board

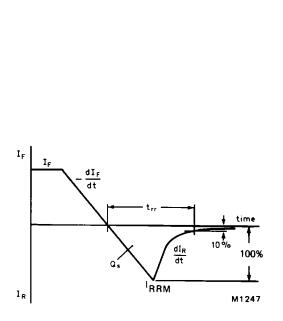
$$R_{th j-a} = 100 K/W$$

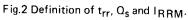
Notes:

1. The limits for both diodes apply whether both diodes conduct simultaneously or on alternate half cycles.

CHARACTERISTICS (per diode)

Forward voltage				
$I_F = 5 \text{ A}; T_j = 150 ^{\circ}\text{C}$	٧ _F	<	0.85	V*
$I_F = 10 \text{ A}; T_j = 25 ^{\circ}\text{C}$	٧ _F	<	1.25	V* -
Reverse current				
$V_R = V_{RWM \text{ max}}; T_j = 100 ^{\circ}\text{C}$	I _R	<	0.2	mA
$V_R = V_{RWM max}$; $T_j = 25 {}^{\circ}\text{C}$	l _R	<	10	μΑ
Reverse recovery when switched from				
$I_F = 1 \text{ A to V}_R \ge 30 \text{ V with } -dI_F/dt = 100 \text{ A/}\mu\text{s}; T_i = 25 ^{\circ}\text{C}$				
recovery time	t _{rr}	<	20	ns
$I_F = 2 \text{ A to V}_R \ge 30 \text{ V with } -dI_F/dt = 20 \text{ A}/\mu\text{s}; T_i = 25 ^{\circ}\text{C}$				
recovered charge	$Q_{\mathbf{s}}$	<	5.5	nC
IF = 5 A to $V_R \ge 30 V$ with $-dI_F/dt = 50 A/\mu s$; $T_i = 25 {}^{\circ}C$	•			
peak recovery current	¹ RRM	<	0.7	Α
Forward recovery when switched to IF = 1 A				
with dI _F /dt = 10 A/ μ s; T _i = 25 °C				
recovery voltage	V_{fr}	typ.	1.0	V





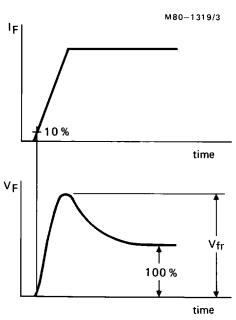


Fig.3 Definition of V_{fr} .

^{*}Measured under pulse conditions to avoid excessive dissipation.

MOUNTING INSTRUCTIONS

- The device may be soldered directly into the circuit, but the maximum permissible temperature of the soldering iron or bath is 275 °C; the heat source must not be in contact with the joint for more than 5 seconds. Soldered joints must be at least 4.7 mm from the seal.
- 2. The leads should not be bent less than 2.4 mm from the seal, and should be supported during bending. The bend radius must be no less than 1.0 mm.
- 3. Mounting by means of a spring clip is recommended.
- 4. For good thermal contact heatsink compound should be used between mounting base and heatsink. Values of R_{th mb-h} given for mounting with heatsink compound refer to the use of a metallic-oxide loaded compound. Ordinary silicone grease is not recommended.
- 5. Body mounting.

A SOT-82 envelope can be adhesive-mounted or soldered into a hybrid circuit. For soldering, a copper plate or an anodized aluminium plate with copper layer is recommended. When adhesive mounting is applied also a ceramic substrate may be used.

OPERATING NOTES

Dissipation and heatsink calculations.

The various components of junction temperature rise above ambient are illustrated in Fig.4.

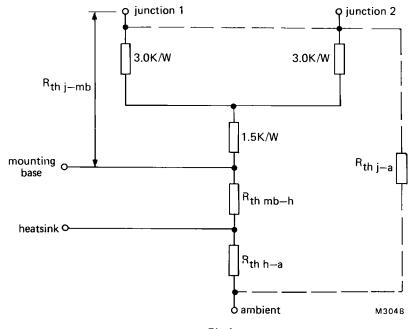


Fig.4.

Any measurement of heatsink temperature should be made immediately adjacent to the device.

SQUARE-WAVE OPERATION (PER DIODE)

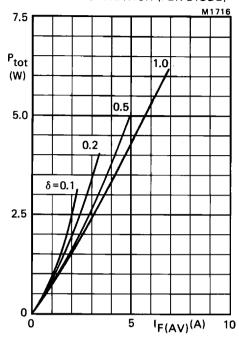
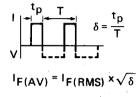


Fig.5 Power rating per diode.
The individual power loss in each diode should first be determined then both added together. The resulting total power loss is then used in conjunction with Fig.6 to determine the heatsink size and corresponding maximum ambient and mounting base temperatures.



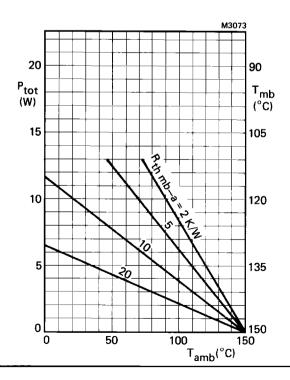


Fig.6.

SINUSOIDAL OPERATION (PER DIODE)

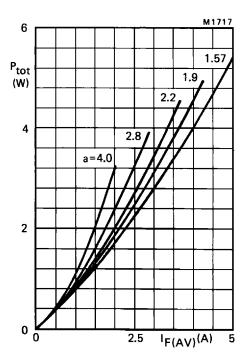


Fig.7 Power rating per diode.
The individual power loss in each diode should first be determined then both added together. The resulting total power loss is then used in conjunction with Fig.6 to determine the heatsink size and corresponding maximum ambient and mounting base temperatures.

 $a = form factor = I_F(RMS)/I_F(AV)$

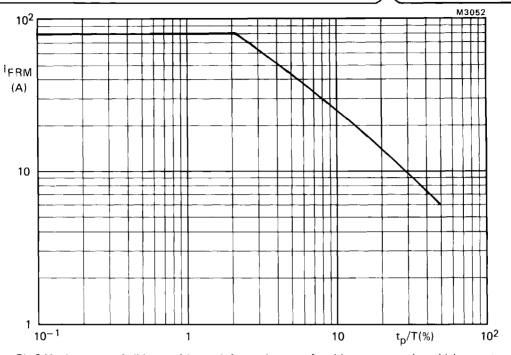
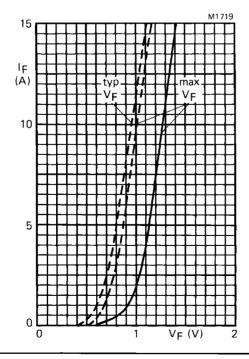
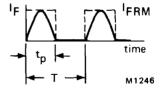


Fig.8 Maximum permissible repetitive peak forward current for either square or sinusoidal currents for 1 μ s < t $_p$ < 1 ms; per diode.





Definition of I_{FRM} and t_p/T

Fig.9
$$\longrightarrow$$
 T_j = 25 °C; $---$ T_j = 150 °C per diode.

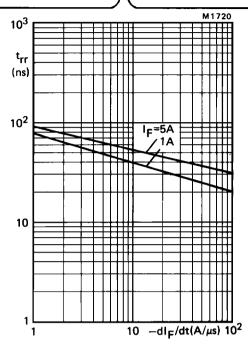


Fig. 10 Maximum t_{rr} at $T_j = 25$ °C.

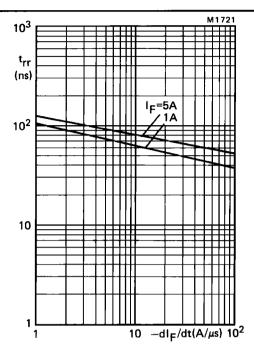


Fig.11 Maximum t_{rr} at $T_j = 100$ °C.

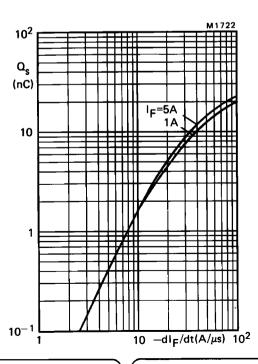


Fig.12 Maximum Q_s at $T_j = 25$ °C.

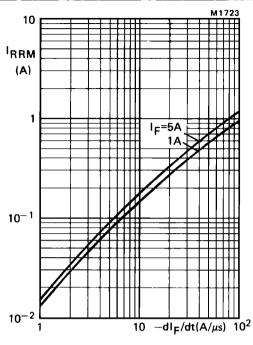


Fig.13 Maximum I $_{RRM}$ at T $_{j}$ = 25 $^{o}\text{C}.$

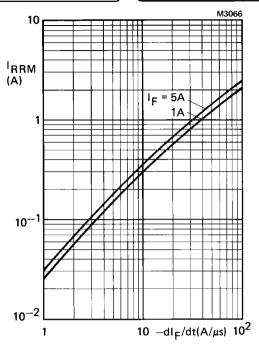


Fig.14 Maximum I_{RRM} at $T_j = 100$ °C.

