

# **DMN2112SN** N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

## **Features**

- Low On-Resistance
- Ideal for Notebook Computer, Portable Phone, PCMCIA Cards, and Battery Powered Circuits
- Lead Free By Design/RoHS Compliant (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability
- **ESD** Protected Gate
- "Green" Device (Note 3)

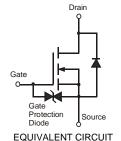
### **Mechanical Data**

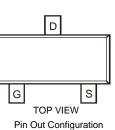
- Case: SC-59
- Case Material Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.008 grams (approximate)

SC-59



TOP VIEW





EQUIVALENT CIRCUIT

### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteri	stic	Symbol	Value	Units	
Drain-Source Voltage		V <sub>DSS</sub>	20	V	
Gate-Source Voltage	Continuous	V <sub>GSS</sub>	± 8	V	
Drain Current	Continuous Pulsed	I <sub>D</sub>	1.2 4.0	A	

## Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation	Pd	500	mW
Thermal Resistance, Junction to Ambient	$R_{ heta}$ JA	250	°C /W
Operating and Storage Temperature Range	Tj, T <sub>STG</sub>	-55 to +150	°C

### Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 1)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20		—	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current @	T <sub>j</sub> = 25°C I <sub>DSS</sub>			10	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Body Leakage	I <sub>GSS</sub>	_		± 10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 1)			•			•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5		1.2	V	$V_{DS} = 10V, I_D = 1.0mA$	
Static Drain-Source On-Resistance	RDS (ON)			0.10 0.14 0.25	Ω	$V_{GS} = 4.5V, I_D = 0.5A \\ V_{GS} = 2.5V, I_D = 0.5A \\ V_{GS} = 1.5V, I_D = 0.1A$	
Forward Transfer Admittance	IY <sub>fs</sub> I		4.2		S	V <sub>DS</sub> = 10V, I <sub>D</sub> =0.5A	
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.1	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS	•					-	
Input Capacitance		_	220		pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance		_	120	—	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>		45	_	pF		
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t <sub>D(ON)</sub>	_	10		ns		
Turn-Off Delay Time		_	75	_	ns	$V_{DD} = 5V, I_D = 0.5A,$	
Turn-On Rise Time	tr	_	15	_	ns	$V_{GS} = 10V, R_{GEN} = 50\Omega$	
Turn-Off Fall Time	tf		65		ns	1	

1. Pulse width  $\leq$  300 $\mu$ s, duty cycle  $\leq$  2%.

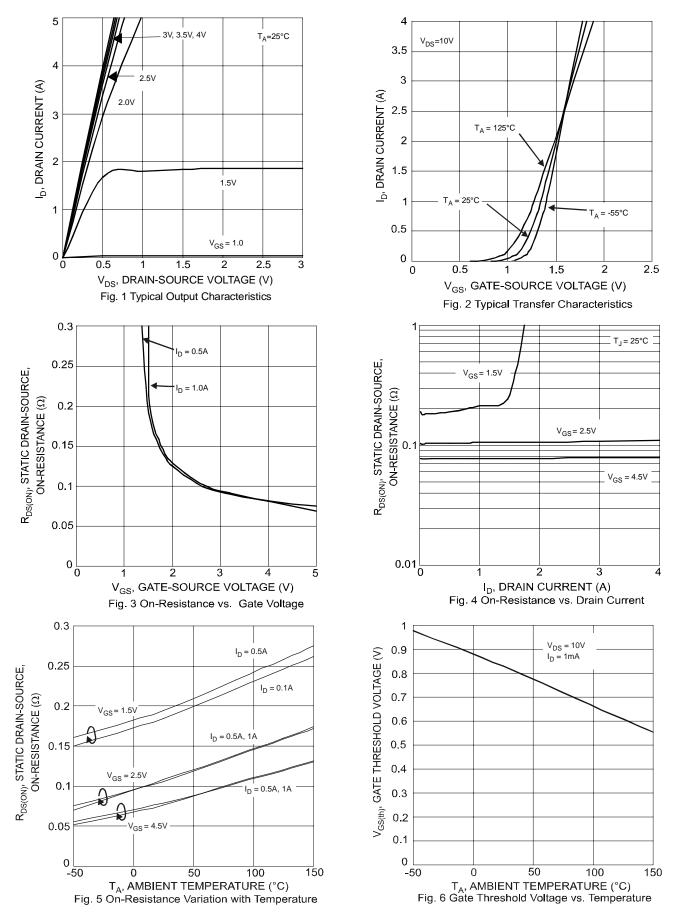
2. No purposefully added lead.

3. Diodes Inc.'s "Green" Policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

Notes:



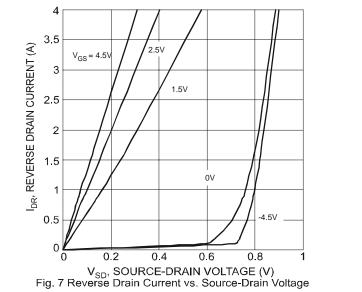
# **DMN2112SN**

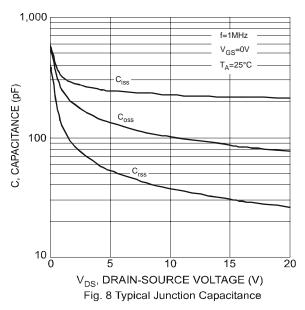


DMN2112SN Document number: DS30830 Rev. 4 - 2



# DMN2112SN



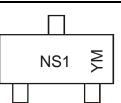


# Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2112SN-7	SC-59	3000/Tape & Reel

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**

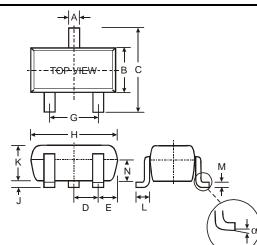


 $\begin{array}{l} \mathsf{NS1} = \mathsf{Product} \ \mathsf{Type} \ \mathsf{Marking} \ \mathsf{Code} \\ \mathsf{YM} = \mathsf{Date} \ \mathsf{Code} \ \mathsf{Marking} \\ \mathsf{Y} = \mathsf{Year} \ \mathsf{ex:} \ \mathsf{T} = 2006 \\ \mathsf{M} = \mathsf{Month} \ \mathsf{ex:} \ 9 = \mathsf{September} \end{array}$ 

#### Date Code Key

Year	20	07	20	08	20	09	20	10	20	11	20	12
Code	ι	J	١	/	V	V	)	<		(	2	<u>Z</u>
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

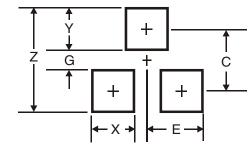
#### **Package Outline Dimensions**



	SC-59					
Dim	Min	Max				
Α	0.35	0.50				
В	1.50	1.70				
С	2.70	3.00				
D	0.95					
ш						
G	1.90					
н	2.90 3.10					
J	0.013	0.10				
K	1.00	1.30				
L	0.35	0.55				
М	0.10	0.20				
N	0.70	0.80				
α	0°	8°				
All Di	All Dimensions in mm					



## Suggested Pad Layout



Dimensions	Value (in mm)
Z	4.0
G	1.2
Х	0.9
Y	1.4
С	2.6
E	0.95

#### IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

#### LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.