

HSS83

Silicon Epitaxial Planar Diode for High Voltage Switching

REJ03G0570-0300
(Previous: ADE-208-177B)
Rev.3.00
Mar 22, 2005

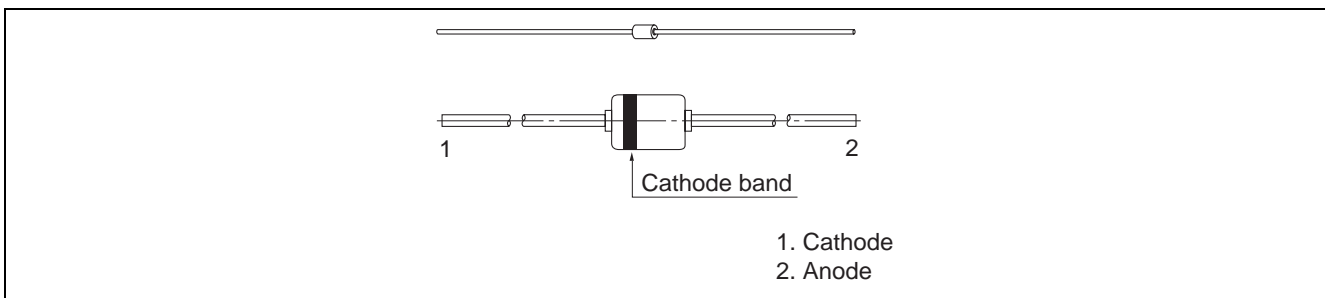
Features

- High reverse voltage. ($V_R = 250$ V)
- Suitable for 5 mm pitch high speed automatically insertion.
- Small glass package (MHD) enables easy mounting and high reliability.

Ordering Information

Type No.	Cathode band	Package Name	Package Code (Previous Code)
HSS83	Black	MHD	GRZZ0002ZC-A (MHD)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}^{*1}	300	V
Reverse voltage	V_R	250	V
Average rectified current	I_O	150	mA
Peak forward current	I_{FM}	625	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	1	A
Power dissipation	P_d	400	mW
Junction temperature	T_j	200	°C
Storage temperature	T_{stg}	-65 to +175	°C

Notes: 1. Reverse voltage in excess of peak reverse voltage may deteriorate electrical characteristic.
2. Within 1s forward surge current.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	200	nA	$V_R = 250\text{ V}$
	I_{R2}	—	—	100	μA	$V_R = 300\text{ V}$
Forward voltage	V_F	—	—	1.0	V	$I_F = 100\text{ mA}$
Capacitance	C	—	1.5	—	pF	$V_R = 0\text{ V}, f = 1\text{ MHz}$
Reverse recovery time	t_{rr}	—	—	100	ns	$I_F = I_R = 30\text{ mA}, I_{rr} = 3\text{ mA}, R_L = 100\ \Omega$

Main Characteristic

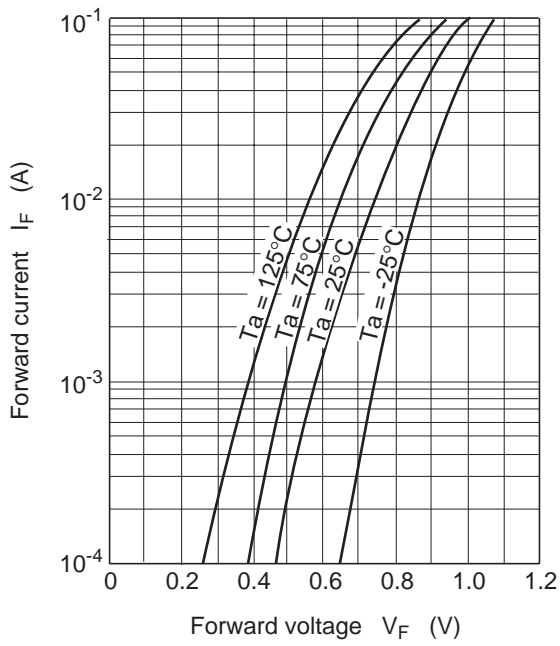


Fig.1 Forward current vs. Forward voltage

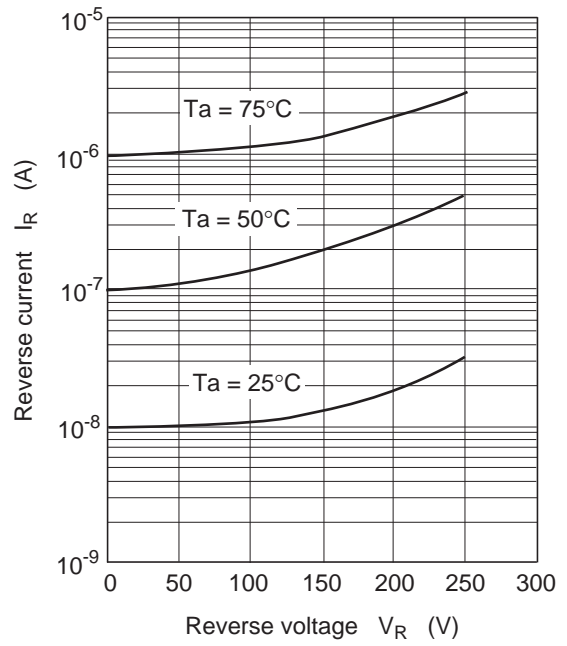


Fig.2 Reverse current vs. Reverse voltage

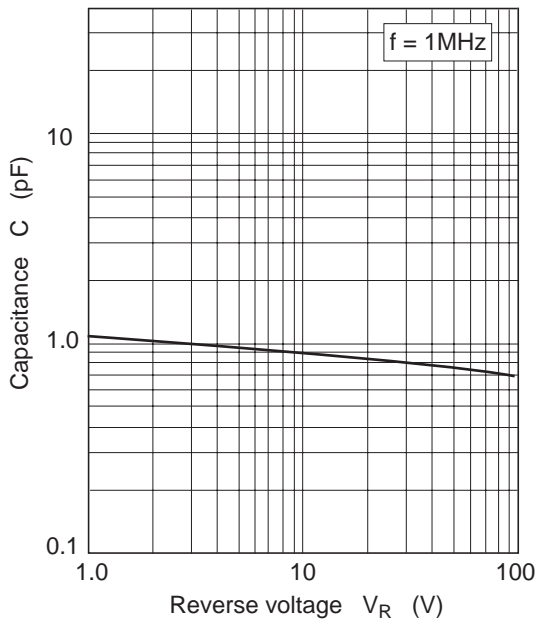
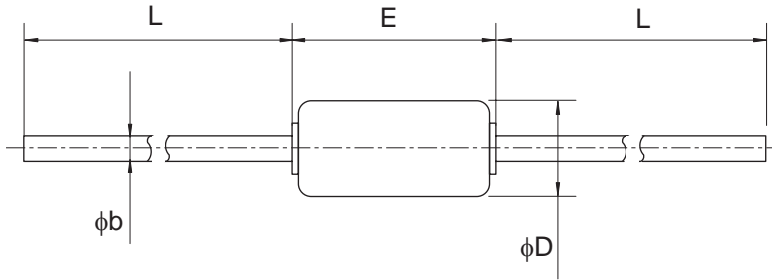


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
—	GRZZ0002ZC-A	MHD / MHDV	0.084g



Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
phi b	-	0.4	-
phi D	-	2.0	-
E	-	-	2.4
L	26.0	-	-

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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001