

# FS16VS-7A

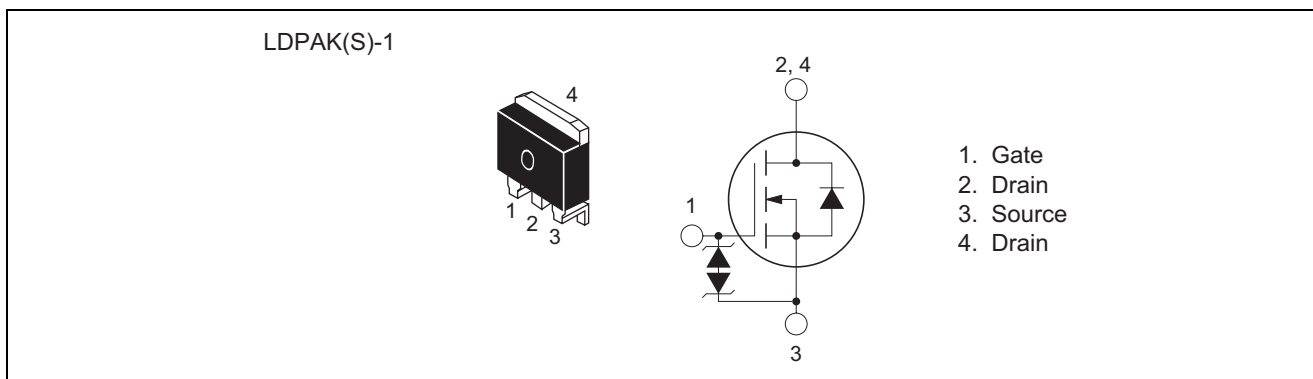
High-Speed Switching Use  
Nch Power MOS FET

REJ03G0276-0100  
Under development  
Rev.1.00  
Aug.20.2004

## Features

- $V_{DSS}$  : 350 V
- $r_{DS(ON)(max)}$  : 0.35  $\Omega$
- $I_D$  : 16 A
- Surface mount package (LDBPAK (S)-1)

## Outline



## Applications

Lamp ballast, switching mode power supply, plasma display, etc.

## Maximum Ratings

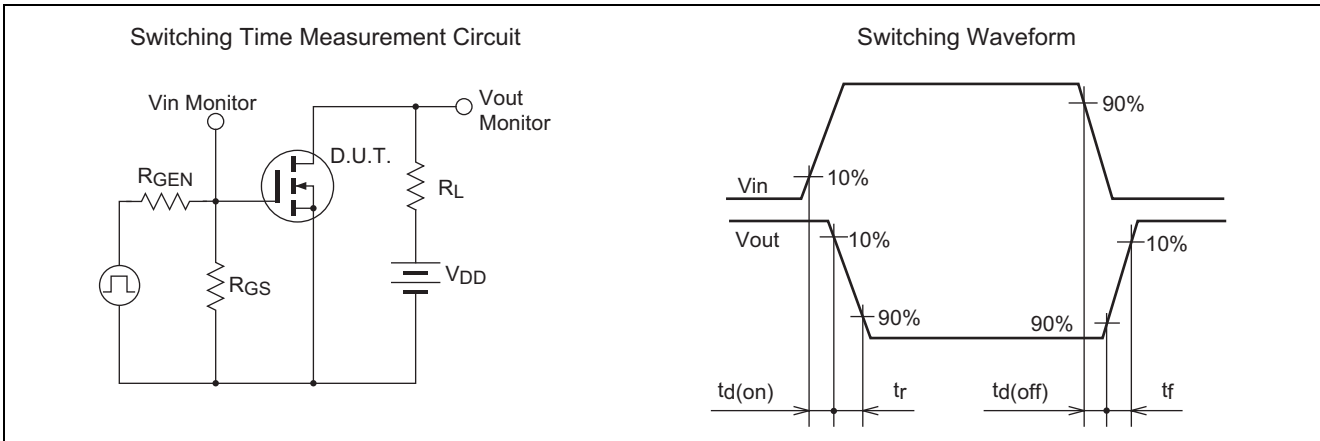
( $T_c = 25^\circ\text{C}$ )

Parameter	Symbol	Ratings	Unit	Conditions
Drain-source voltage	$V_{DSS}$	350	V	$V_{GS} = 0\text{ V}$
Gate-source voltage	$V_{GSS}$	$\pm 30$	V	$V_{DS} = 0\text{ V}$
Drain current	$I_D$	16	A	
Drain current (Pulsed)	$I_{DM}$	48	A	
Avalanche current (Pulsed)	$I_{DA}$	16	A	$L = 200\ \mu\text{H}$
Maximum power dissipation	$P_D$	125	W	
Channel temperature	$T_{ch}$	- 55 to +150	$^\circ\text{C}$	
Storage temperature	$T_{stg}$	- 55 to +150	$^\circ\text{C}$	
Mass	—	1.3	g	Typical value

Electrical Characteristics

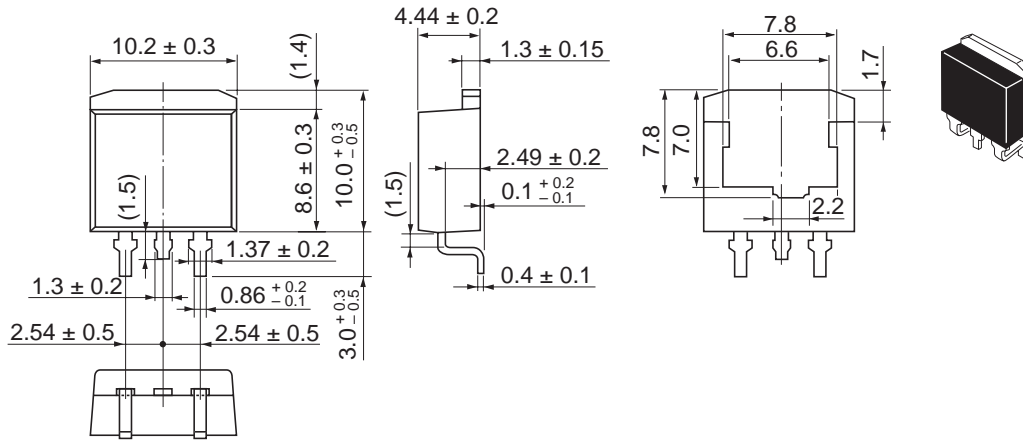
(Tch = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Drain-source breakdown voltage	$V_{(BR)DSS}$	350	—	—	V	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate-source breakdown voltage	$V_{(BR)GSS}$	$\pm 30$	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}, V_{DS} = 0 \text{ V}$
Gate-source leakage current	$I_{GSS}$	—	—	$\pm 10$	$\mu\text{A}$	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$
Drain-source leakage current	$I_{DSS}$	—	—	1	mA	$V_{DS} = 350 \text{ V}, V_{GS} = 0 \text{ V}$
Gate-source threshold voltage	$V_{GS(th)}$	2.5	3.0	3.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Drain-source on-state resistance	$r_{DS(ON)}$	—	0.27	0.35	$\Omega$	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}$
Drain-source on-state voltage	$V_{DS(ON)}$	—	2.16	2.80	V	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}$
Forward transfer admittance	$ y_{fs} $	6.0	13.0	—	S	$I_D = 8 \text{ A}, V_{DS} = 10 \text{ V}$
Input capacitance	$C_{iss}$	—	1500	—	pF	$V_{DS} = 25 \text{ V}, V_{GS} = 10 \text{ V},$ $f = 1 \text{ MHz}$
Output capacitance	$C_{oss}$	—	160	—	pF	
Reverse transfer capacitance	$C_{rss}$	—	40	—	pF	
Turn-on delay time	$t_{d(on)}$	—	25	—	ns	$V_{DD} = 150 \text{ V}, I_D = 8 \text{ A},$ $V_{GS} = 10 \text{ V},$ $R_{GEN} = R_{GS} = 50 \text{ }\Omega$
Rise time	$t_r$	—	45	—	ns	
Turn-off delay time	$t_{d(off)}$	—	200	—	ns	
Fall time	$t_f$	—	55	—	ns	
Source-drain voltage	$V_{SD}$	—	1.5	2.0	V	$I_S = 8 \text{ A}, V_{GS} = 0 \text{ V}$
Thermal resistance	$R_{th(ch-c)}$	—	—	1.0	$^{\circ}\text{C/W}$	Channel to case



Package Dimensions

As of January, 2003  
Unit: mm



Package Code	LDPAK (S)-(1)
JEDEC	—
JEITA	—
Mass (reference value)	1.30 g

Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	1000	Type name – T +Direction (1 or 2) +1	FS16VS-7A-T11

Note : Please confirm the specification about the shipping in detail.

**Renesas Technology Corp.** Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

---

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
  2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
  3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.  
The information described here may contain technical inaccuracies or typographical errors.  
Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.  
Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (<http://www.renesas.com>).
  4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
  5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
  6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
  7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.  
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
  8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.
- 



**RENESAS SALES OFFICES**

<http://www.renesas.com>

**Renesas Technology America, Inc.**

450 Holger Way, San Jose, CA 95134-1368, U.S.A  
Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

**Renesas Technology Europe Limited.**

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom  
Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

**Renesas Technology Europe GmbH**

Dornacher Str. 3, D-85622 Feldkirchen, Germany  
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

**Renesas Technology Hong Kong Ltd.**

7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong  
Tel: <852> 2265-6688, Fax: <852> 2375-6836

**Renesas Technology Taiwan Co., Ltd.**

FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan  
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

**Renesas Technology (Shanghai) Co., Ltd.**

26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China  
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

**Renesas Technology Singapore Pte. Ltd.**

1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632  
Tel: <65> 6213-0200, Fax: <65> 6278-8001