

## 14 pin Dual-in-Line

# 38.0MHz to 640.0MHz

- Frequency range 38MHz to 640MHz
- LVPECL Output
- Supply Voltage 3.3 VDC
- Phase jitter 0.4ps typical
- Pull range from ±30ppm to ±150ppm

#### **DESCRIPTION**

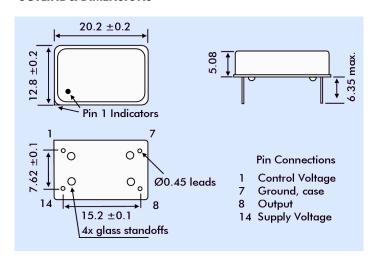
GPF14 VCXOs are packaged in an industry-standard 14 pin dual-inline package. Typical phase jitter for GPF series VCXOs is 0.4 ps. Output is LVPECL. Applications include phase lock loop, SONET/ATM, set-top boxes, MPEG, audio/video modulation, video game consoles and HDTV.

### **SPECIFICATION**

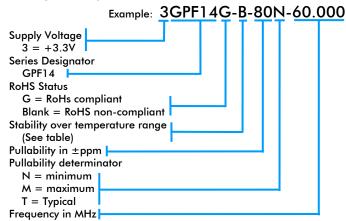
Frequency Range:	38.0MHz to 640.0MHz	
Supply Voltage:	3.3 VDC ±5%	
	LVPECL	
Output Logic: RMS Period Jitter:		
	3.0ps typical	
Peak to Peak Jitter:	20.0ps typical, 30.0ps maximum	
Phase Jitter:	0.4ps typical, 5.0ps maximum	
Initial Frequency Accuracy:	Tune to the nominal frequency with Vc= 1.65 ±0.2VDC	
Output Voltage HIGH (1):	Vdd-1.025V minimum	
	Vdd-0.880V maximum	
Output Voltage LOW (0):	Vdd-1.810V minimum	
	Vdd-1.620V maximum	
	$(RL=50\Omega \text{ to Vdd-2V})$	
Pulling Range:	From ±30ppm to ±150ppm	
Control Voltage Range:	1.65 ±0.35 Volts	
Temperature Stability:	See table	
Output Load:	50Ω into Vdd or Thevenin equiv.	
Rise/Fall Times:	0.5ns typ., 0.7ns max.	
,	20% Vdd to 80% Vdd	
Duty Cycle:	50% +5%	
Doily Cyclo.	(Measured at Vdd-1.3V)	
Start-up Time:	10ms maximum, 5ms typical	
Current Consumption:	75mA maximum at 212.5MHz	
Correin Consomption.	80mA maximum at 622.08MHz	
Static Discharge Protection:	2kV maximum	
Storage Temperature:	-55° to +150°C	
Ageing:	±2ppm per year maximum	
Enable/Disable:	Not implemented - 4 pin package	
RoHS Status:	Fully compliant or non-compliant	
NOTIO GIGIOS.	Tony compliant of non-compliant	



### **OUTLINE & DIMENSIONS**



### **PART NUMBERING**



#### **FREQUENCY STABILITY**

<b>Stability Code</b>	Stability ±ppm	Temp. Range
Α	25	0°∼+70°C
В	50	0°∼+70°C
С	100	0°∼+70°C
D	25	-40°~+85°C
E	50	-40°∼+85°C
F	100	-40°~+85°C

If non-standard frequency stability is required Use '1' followed by stability, i.e. 120 for ±20ppm