

LOW JITTER 5.0VDC PECL FULL-SIZE DIP
CRYSTAL CLOCK OSCILLATORS
ACPE



FEATURES:

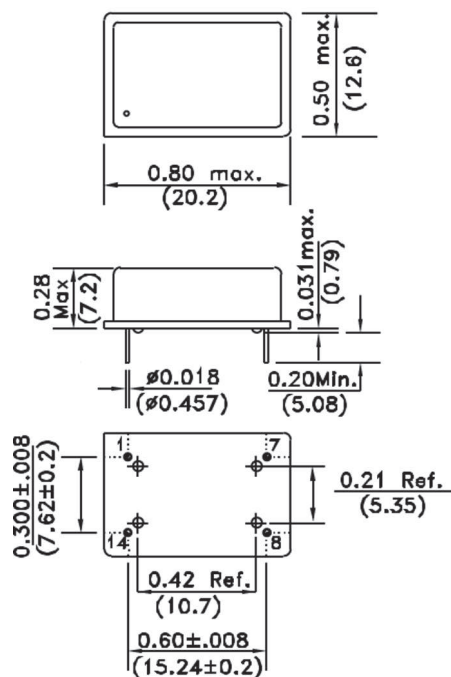
- Low jitter PECL logic.
- Tight overall frequency stability.
- Fast Rise and Fall Times.
- Complementary output.
- Wide frequency range.

APPLICATIONS:

- Gigabit Ethernet.
- Data telecommunications and networking.
- xDSL, Stratum IV.
- SONET/SDH

STANDARD SPECIFICATIONS

PARAMETERS	SPECIFICATIONS
Frequency Range (F ₀)	10.00 MHz - 200 MHz
Operating Temperature (T _{OPR})	0°C to + 70°C (see options)
Storage Temperature (T _{STO})	-40°C to +85°C
Frequency Stability (DF/ F ₀)	± 100 ppm max. (See Options)
Supply Voltage (V _{dd})	+5 Vdc ± 5%
Supply Current (I _{dd})	120mA max.
Duty cycle or Symmetry	40/60% max. measured at 3.67VDC
Rise and Fall times (Tr/Tf)	5ns max. measured at 20% to 80% level
Output	PECL square wave 10KH compatible
Output and E/D options (pin 1)	No connection or Complementary output or E/D
Output load	50W terminated into 3VDC (*)
Output voltage	VOH = 3.98V min., 4.26V max. VOL = 3.05V min., 3.40V max.
Jitter	10ps typical, 25ps RMS max.
Enable/Disable option (pin 1)	VIL= 3.40V max., VIH = 3.87V min. (Output is in "0" state when disabled)
Start-up time	10ms max.



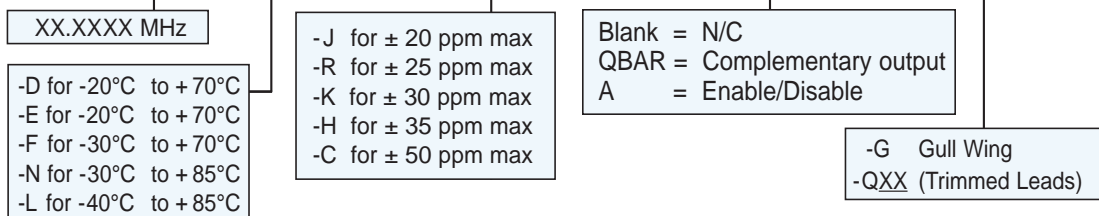
Dimensions: Inches (mm)

PIN NUMBER	FUNCTION
1	NC or \bar{Q} or E/D
7	GND / Case
8	Output
14	V _{dd}

(*) Output must be terminated into 50W to +3VDC or 270W into GND
Environmental and mechanical specifications, Group 1.

ORDERING OPTIONS

ACPE - Frequency - Temperature - Overall Frequency Stability - Pin 1 option - Value Added



NOTE: Left blank if standard • All specifications and markings subject to change without notice

29 Journey • Aliso Viejo, CA 92656 • USA
(949) 448-7070 • FAX: (949) 448-8484

E-MAIL: abinfo@abracon.com • INTERNET ADDRESS: www.abracon.com

