

## HLMP-CW15, HLMP-CW16, HLMP-CW23, HLMP-CW24, HLMP-CW30, HLMP-CW31

# Description

These high intensity white LED lamps are based on InGaN material technology. A blue LED die is coated by a phosphor to produce white. The typical resulting color is described by the coordinates x = 0.32, y = 0.32using the 1931 CIE Chromaticity Diagram.

These T-1<sup>3/</sup>4 lamps are untinted, nondiffused, and incorporate precise optics producing well defined spatial radiation patterns at specific viewing cone angle.

- · Highly luminous white emission
- 15°, 23°, and 30° viewing angle

# Applications

- Electronic signs and signals
- · Small area illumination
- Legend backlighting
- · General purpose indicators

# Benefit

· Reduced power consumption, higher reliability, and increased optical/mechanical design flexibility compared to incandescent bulbs and other alternative white light sources

CAUTION: These devices are Class 2 ESD sensitive. Please observe appropriate precautions during handling and processing. Refer to Agilent Technologies Application Note AN-1142 for additional details.

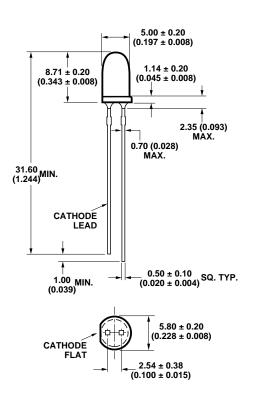


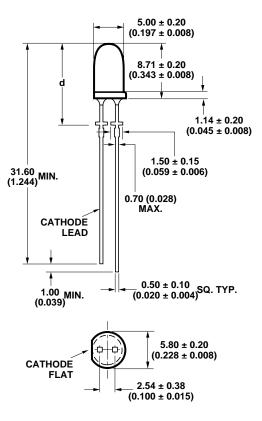
# **Device Selection Guide**

	Viewing Angle	Min. Luminous Intensity Iv (mcd) @ 20 mA			
Part Number	Тур.	Min.	Max.	Standoff Leads	Package Dimension
HLMP-CW15-TW0xx	15°	2500	7200	No	А
HLMP-CW15-VY0xx	15°	4200	12000	No	А
HLMP-CW16-TW0xx	15°	2500	7200	Yes	В
HLMP-CW16-VY0xx	15°	4200	12000	Yes	В
HLMP-CW23-SV0xx	23°	1900	5500	No	А
HLMP-CW23-TW0xx	23°	2500	7200	No	А
HLMP-CW24-SV0xx	23°	1900	5500	Yes	В
HLMP-CW24-TW0xx	23°	2500	7200	Yes	В
HLMP-CW30-PS0xx	30°	880	2500	No	А
HLMP-CW30-SV0xx	30°	1900	5500	No	А
HLMP-CW31-PS0xx	30°	880	2500	Yes	В
HLMP-CW31-SV0xx	30°	1900	5500	Yes	В

Tolerance for each intensity limit is  $\pm$  15%.

## Package Dimensions





PACKAGE DIMENSION A

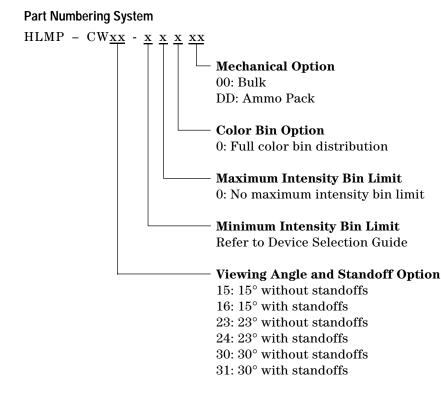
#### PACKAGE DIMENSION B

#### NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS (INCHES).

2. EPOXY MENISCUS MAY EXTEND ABOUT 1 mm (0.040") DOWN THE LEADS.

HLMP-CW16	HLMP-CW24	HLMP-CW31
d = 12.6 ± 0.25	d = 12.52 ± 0.25	d = 11.96 ± 0.25
(0.496 ± 0.010)	(0.493 ± 0.010)	(0.471 ± 0.010)



# Absolute Maximum Ratings

 $T_A = 25^{\circ}C$ 

Units
Units
mA
mA
mW
V
oC
٥C
٥C

Notes:

1. Derate linearly as shown in Figure 5.

2. Duty factor 10%, 1 kHz.

# **Electrical Characteristics**

 $T_A = 25^{\circ}C$ 

	d Voltage, ୭ I <sub>F</sub> = 20 mA	Reverse Breakdown, V <sub>R</sub> (V) @ I <sub>R</sub> = 10 μA	Capacitance, C (pF), V <sub>F</sub> = 0, f = 1 MHz	Thermal Resistance Rq <sub>J-PIN</sub> (°C/W)	
Тур.	Max.	Min.	Тур.	Тур.	
3.6	4.0	5	70	240	

# **Optical Characteristics**

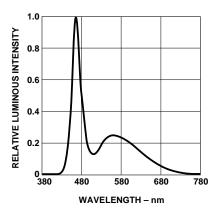
 $T_A = 25^{\circ}C$ 

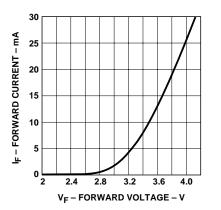
	Typical Chromaticity Coordinates <sup>[1]</sup>			Viewing Angle 201/2 Degrees <sup>[2]</sup>
Part Number	Х	Y	Тур.	
HLMP-CW3x-xxxxx	0.32	0.32	30	
HLMP-CW2x-xxxxx	0.32	0.32	23	
HLMP-CW1x-xxxxx	0.32	0.32	15	

Notes:

1. The chromaticity coordinates are derived from the CIE 1931 Chromaticity Diagram and represent the perceived color of the device.

2.  $\theta_{1/2}$  is the off-axis angle where the luminous intensity is  $\frac{1}{2}$  the peak intensity.





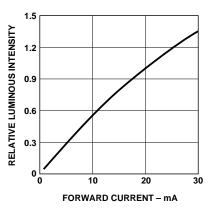


Figure 1. Relative intensity vs. wavelength.

Figure 2. Forward current vs. forward voltage.

Figure 3. Relative Iv vs. forward current.

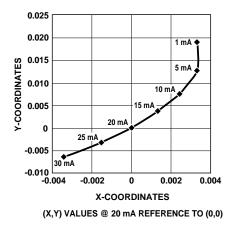


Figure 4. Chromaticity shift vs. current.

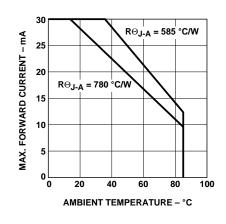


Figure 5. Maximum forward current vs. temperature.

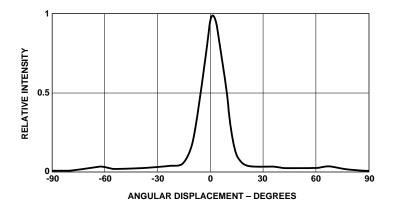


Figure 6a. CW1x spatial radiation pattern.

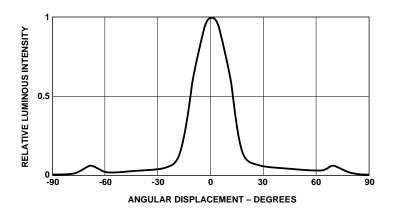


Figure 6b. CW2x spatial radiation pattern.

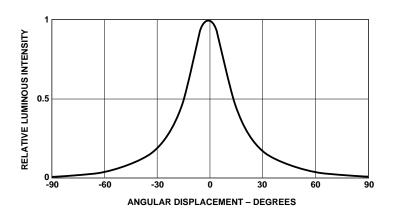


Figure 6c. CW3x spatial radiation pattern.

## **Color Bin Limit Table**

Rank	Limits	(Chromaticity	<b>Coordinat</b> e	s)		
1	Х	0.330	0.330	0.356	0.361	
	у	0.360	0.318	0.351	0.385	
2	х	0.287	0.296	0.330	0.330	
	у	0.295	0.276	0.318	0.339	
3	Х	0.264	0.280	0.296	0.283	
	у	0.267	0.248	0.276	0.305	
4	х	0.283	0.287	0.330	0.330	
	у	0.305	0.295	0.339	0.360	

Tolerance for each bin limit is  $\pm$  0.01.

#### Note:

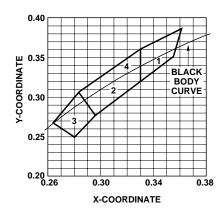
Bin categories are established for classification of products. Products may not be available in all bin categories. Please contact your Agilent representative for information on currently available bins.

Intensity Bin Limits (mcd at 20 mA)

Min.	Max.
520	680
680	880
880	1150
1150	1500
1500	1900
1900	2500
2500	3200
3200	4200
4200	5500
5500	7200
7200	9300
9300	12000
12000	16000
	520 680 880 1150 1500 2500 3200 4200 5500 7200 9300

Tolerance for each bin limit is  $\pm$  15%.

# Color Bin Limits with Respect to CIE 1931 Chromaticity Diagram



#### Precautions

## Lead Forming

- The leads of an LED lamp may be preformed or cut to length prior to insertion and soldering into PC board.
- If lead forming is required before soldering, care must be taken to avoid any excessive mechanical stress induced to LED package. Otherwise, cut the leads of LED to length after soldering process at room temperature. The solder joint formed will absorb the mechanical stress of the lead cutting from traveling to the LED chip die attach and wirebond.
- It is recommended that tooling made to precisely form and cut the leads to length rather than rely upon hand operation.

### Soldering Conditions

- Care must be taken during PCB assembly and soldering process to prevent damage to LED component.
- The closest LED is allowed to solder on board is 1.59 mm below the body (encapsulant epoxy) for those parts without standoff.
- Recommended soldering conditions:

	Wave Soldering	Manual Solder Dipping
Pre-heat Temperature	105 °C Max.	_
Pre-heat Time	30 sec Max.	-
Peak Temperature	250 °C Max.	260 °C Max.
Dwell Time	3 sec Max.	5 sec Max.

- Wave soldering parameter must be set and maintained according to recommended temperature and dwell time in the solder wave. Customer is advised to periodically check on the soldering profile to ensure the soldering profile used is always conforming to recommended soldering condition.
- If necessary, use fixture to hold the LED component in proper orientation with respect to the PCB during soldering process.
- Proper handling is imperative to avoid excessive thermal stresses to LED components when heated. Therefore, the soldered PCB must be allowed to cool to room temperature,  $25^{\circ}$ C, before handling.
- Special attention must be given to board fabrication, solder masking, surface plating and lead holes size and component orientation to assure solderability.
- Recommended PC board plated through hole sizes for LED component leads:

LED Component Lead Size	Diagonal	Plated Through Hole Diameter
0.457 x 0.457 mm (0.018 x 0.018 inch)	0.646 mm (0.025 inch)	0.976 to 1.078 mm (0.038 to 0.042 inch)
0.508 x 0.508 mm	0.718 mm	1.049 to 1.150 mm
(0.020 x 0.020 inch)	(0.028 inch)	(0.041 to 0.045 inch)

**Note:** Refer to application note AN1027 for more information on soldering LED components.

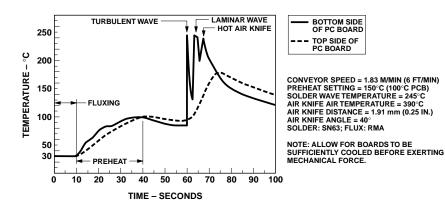


Figure 7. Recommended wave soldering profile.

# www.agilent.com/semiconductors

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