

#### Features:

- PI74FCT373/573T are pin compatible with bipolar FAST™ Series at a higher speed and lower power consumption
- TTL input and output levels
- Low ground bounce outputs
- Extremely low static power
- Hysteresis on all inputs
- Industrial operating temperature range:  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$
- Device models available upon request
- Packaging:
  - PI74FCT373T (Pb-free & Green available)
    - 20-pin TSSOP (L)
    - 20-pin QSOP (Q)
    - 20-pin SOIC (S)

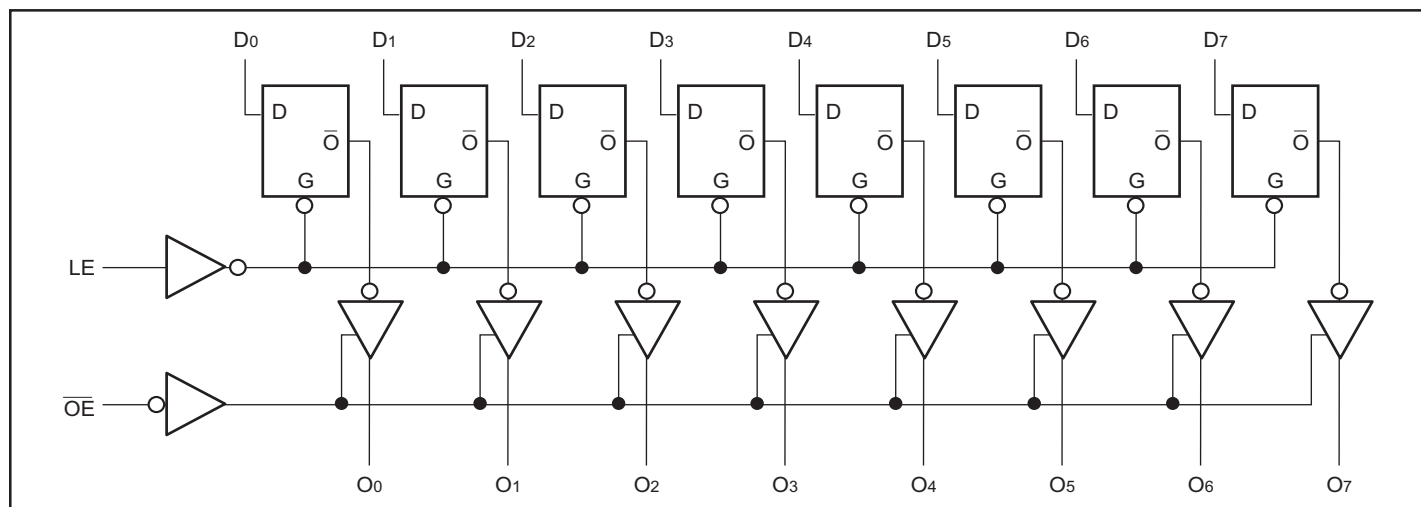
PI74FCT573T (Pb-free & Green available)

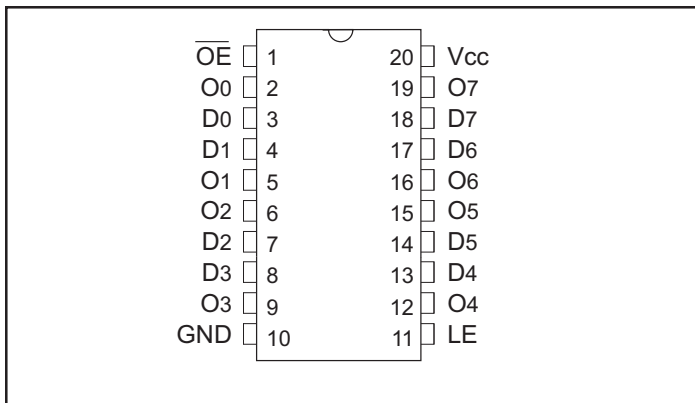
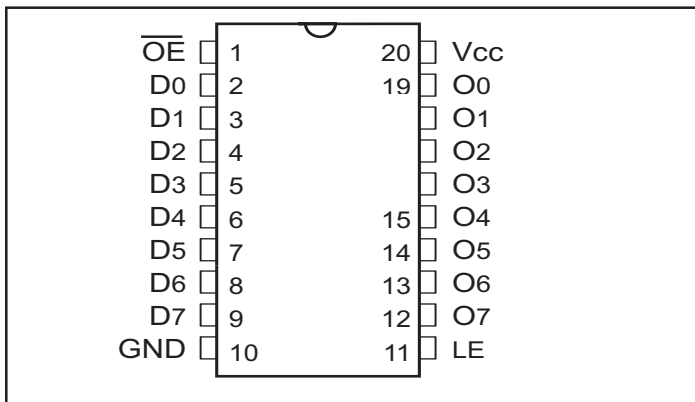
- 20-pin QSOP (Q)
- 20-pin SOIC (S)

#### Description:

Pericom Semiconductor's PI74FCT373T and PI74FCT 573T are 8-bit wide octal transparent latches designed with 3-state outputs and are intended for bus oriented applications. When Latch Enable (LE) is HIGH, the flip-flops appear transparent to the data. The data that meets the set-up time when LE is LOW is latched. When  $\overline{\text{OE}}$  is HIGH, the bus output is in the high impedance state.

#### Block Diagram



**PI74FCT373 Pin Configuration**

**PI74FCT573 Pin Configuration**

**Pin Description**

| Pin Name           | Description                      |
|--------------------|----------------------------------|
| $\overline{OE}$    | Output Enable Input (Active LOW) |
| LE                 | Latch Enable Input (Active HIGH) |
| D0-D7              | Data Inputs                      |
| O0-O7              | 3-State Outputs                  |
| $\overline{O0-O7}$ | Complementary 3-State Outputs    |
| GND                | Ground                           |
| Vcc                | Power                            |

**Truth Table<sup>(1)</sup>**

| Inputs         |    |                 | Outputs        |
|----------------|----|-----------------|----------------|
| D <sub>N</sub> | LE | $\overline{OE}$ | O <sub>N</sub> |
| H              | H  | L               | H              |
| L              | H  | L               | L              |
| X              | X  | H               | Z              |

**Notes:**

1. H = High Voltage Level  
L = Low Voltage Level  
X = Don't Care  
Z = High Impedance

## Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

|                                                               |                 |
|---------------------------------------------------------------|-----------------|
| Storage Temperature .....                                     | –65°C to +150°C |
| Ambient Temperature with Power Applied .....                  | –40°C to +85°C  |
| Supply Voltage to Ground Potential (Inputs & Vcc Only) .....  | –0.5V to +7.0V  |
| Supply Voltage to Ground Potential (Outputs & D/O Only) ..... | –0.5V to +7.0V  |
| DC Input Voltage .....                                        | –0.5V to +7.0V  |
| DC Output Current .....                                       | 120 mA          |
| Power Dissipation .....                                       | 0.5W            |

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## DC Electrical Characteristics (Over the Operating Range, T<sub>A</sub> = –40°C to +85°C, V<sub>CC</sub> = 5.0V ± 5%)

| Parameters       | Description           | Test Conditions <sup>(1)</sup>                                               |                                   | Min. | Typ <sup>(2)</sup> | Max. | Units |
|------------------|-----------------------|------------------------------------------------------------------------------|-----------------------------------|------|--------------------|------|-------|
| V <sub>OH</sub>  | Output HIGH Voltage   | V <sub>CC</sub> = Min., V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> | I <sub>OH</sub> = –15.0 mA        | 2.4  | 3.0                |      | V     |
| V <sub>OL</sub>  | Output LOW Current    | V <sub>CC</sub> = Min., V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> | I <sub>OL</sub> = 64 mA           |      | 0.3                | 0.55 | V     |
| V <sub>IH</sub>  | Input HIGH Voltage    | Guaranteed Logic HIGH Level                                                  |                                   | 2.0  |                    |      | V     |
| V <sub>IL</sub>  | Input LOW Voltage     | Guaranteed Logic LOW Level                                                   |                                   |      |                    | 0.8  | V     |
| I <sub>IH</sub>  | Input HIGH Current    | V <sub>CC</sub> = Max.                                                       | V <sub>IN</sub> = V <sub>CC</sub> |      |                    | 1    | μA    |
| I <sub>IL</sub>  | Input LOW Current     | V <sub>CC</sub> = Max.                                                       | V <sub>IN</sub> = GND             |      |                    | –1   | μA    |
| I <sub>OZH</sub> | High Impedance        | V <sub>CC</sub> = Max.                                                       | V <sub>OUT</sub> = 2.7V           |      |                    | 1    | μA    |
| I <sub>OZL</sub> | Output Current        |                                                                              | V <sub>OUT</sub> = 0.5V           |      |                    | –1   | μA    |
| V <sub>IK</sub>  | Clamp Diode Voltage   | V <sub>CC</sub> = Min., I <sub>IN</sub> = –18 mA                             |                                   |      | –0.7               | –1.2 | V     |
| I <sub>OFF</sub> | Power Down Disable    | V <sub>CC</sub> = GND, V <sub>OUT</sub> = 4.5V                               |                                   |      |                    | 100  | μA    |
| I <sub>OS</sub>  | Short Circuit Current | V <sub>CC</sub> = Max. <sup>(3)</sup> , V <sub>OUT</sub> = GND               |                                   | –60  | –120               |      | mA    |
| V <sub>H</sub>   | Input Hysteresis      |                                                                              |                                   |      | 200                |      | mV    |

### Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

## Capacitance (T<sub>A</sub> = 25°C, f = 1 MHz)

| Parameters <sup>(1)</sup> | Description        | Test Conditions       | Typ | Max. | Units |
|---------------------------|--------------------|-----------------------|-----|------|-------|
| C <sub>IN</sub>           | Input Capacitance  | V <sub>IN</sub> = 0V  | 6   | 10   | pF    |
| C <sub>OUT</sub>          | Output Capacitance | V <sub>OUT</sub> = 0V | 8   | 12   | pF    |

### Notes:

1. This parameter is determined by device characterization but is not production tested.

## Power Supply Characteristics

| Parameters       | Description                                     | Test Conditions <sup>(1)</sup>                                                                                                                                |                                                            | Min. | Typ <sup>(2)</sup> | Max.                | Units      |
|------------------|-------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|------|--------------------|---------------------|------------|
| I <sub>CC</sub>  | Quiescent Power Supply Current                  | V <sub>CC</sub> = Max.                                                                                                                                        | V <sub>IN</sub> = GND or V <sub>CC</sub>                   |      | 0.1                | 500                 | μA         |
| ΔI <sub>CC</sub> | Supply Current per Input @ TTL HIGH             | V <sub>CC</sub> = Max.,                                                                                                                                       | V <sub>IN</sub> = 3.4V <sup>(3)</sup>                      |      | 0.5                | 2.0                 | mA         |
| I <sub>CCD</sub> | Supply Current per Input per MHz <sup>(4)</sup> | V <sub>CC</sub> = Max.,<br>Outputs Open<br>$\overline{OE}$ = GND<br>LE = V <sub>CC</sub><br>One Bit Toggling<br>50% Duty Cycle                                | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 0.15               | 0.25                | mA/<br>MHz |
| I <sub>C</sub>   | Total Power Supply Current <sup>(6)</sup>       | V <sub>CC</sub> = Max.,<br>Outputs Open<br>f <sub>i</sub> = 10 MHz<br>50% Duty Cycle<br>$\overline{OE}$ = GND<br>LE = V <sub>CC</sub><br>One Bit Toggling     | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 1.5                | 3.0 <sup>(5)</sup>  | mA         |
|                  |                                                 |                                                                                                                                                               | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND            |      | 1.8                | 4.5 <sup>(5)</sup>  |            |
|                  |                                                 | V <sub>CC</sub> = Max.,<br>Outputs Open<br>f <sub>i</sub> = 2.5 MHz<br>50% Duty Cycle<br>$\overline{OE}$ = GND<br>LE = V <sub>CC</sub><br>Eight Bits Toggling | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 3.0                | 6.0 <sup>(5)</sup>  |            |
|                  |                                                 |                                                                                                                                                               | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND            |      | 5.0                | 14.0 <sup>(5)</sup> |            |

### Notes:

- For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient.
- Per TTL driven input (V<sub>IN</sub> = 3.4V); all other inputs at V<sub>CC</sub> or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I<sub>CC</sub> formula. These limits are guaranteed but not tested.
- I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> + I<sub>DYNAMIC</sub>  

$$I_C = I_{CC} + \Delta I_{CC} D_H N_T + I_{CCD} (f_{CP}/2 + f_i N_i)$$

I<sub>CC</sub> = Quiescent Current  
ΔI<sub>CC</sub> = Power Supply Current for a TTL High Input (V<sub>IN</sub> = 3.4V)  
D<sub>H</sub> = Duty Cycle for TTL Inputs High  
N<sub>T</sub> = Number of TTL Inputs at D<sub>H</sub>  
I<sub>CCD</sub> = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)  
f<sub>CP</sub> = Clock Frequency for Register Devices (Zero for Non-Register Devices)  
f<sub>i</sub> = Input Frequency  
N<sub>i</sub> = Number of Inputs at f<sub>i</sub>  
All currents are in milliamps and all frequencies are in megahertz.

**PI74FCT373T Switching Characteristics over Operating Range**

| Parameters   | Description                                    | Conditions              | 373T |      | 373AT |     | 373CT |     | 373DT |     | Unit |
|--------------|------------------------------------------------|-------------------------|------|------|-------|-----|-------|-----|-------|-----|------|
|              |                                                |                         | Com. |      | Com.  |     | Com.  |     | Com.  |     |      |
|              |                                                |                         | Min  | Max  | Min   | Max | Min   | Max | Min   | Max |      |
| tPLH<br>tPHL | Propagation Delay <sup>(1)</sup><br>DN to ON   | CL = 50 pF<br>RL = 500Ω | 1.5  | 8.0  | 1.5   | 5.2 | 1.5   | 4.2 | 1.5   | 3.8 | ns   |
| tPLH<br>tPHL | Propagation Delay <sup>(1)</sup><br>LE to ON   |                         | 2.0  | 13.0 | 2.0   | 8.5 | 2.0   | 5.5 | 1.5   | 4.9 | ns   |
| tpZH<br>tpZL | Output Enable Time<br>OE to ON                 |                         | 1.5  | 12.0 | 1.5   | 6.5 | 1.5   | 5.5 | 1.5   | 5.5 | ns   |
| tpHZ<br>tplZ | Output Disable Time <sup>(2)</sup><br>OE to ON |                         | 1.5  | 7.5  | 1.5   | 5.5 | 1.5   | 5.0 | 1.5   | 5.0 | ns   |
| tsU          | Setup Time HIGH or<br>LOW, DN to LE            |                         | 2.0  |      | 2.0   |     | 2.0   |     | 2.0   |     | ns   |
| tH           | Hold Time HIGH or<br>LOW, DN to LE             |                         | 1.5  |      | 1.5   |     | 1.5   |     | 1.5   |     | ns   |
| tw           | LE Pulse Width <sup>(2)</sup><br>HIGH          |                         | 6.0  |      | 5.0   |     | 5.0   |     | 4.0   |     | ns   |

**Notes:**

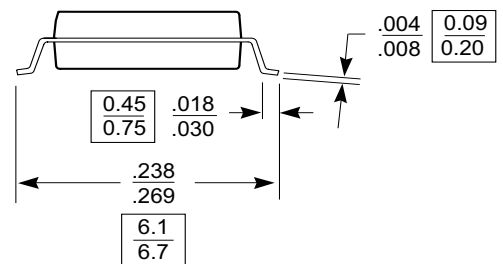
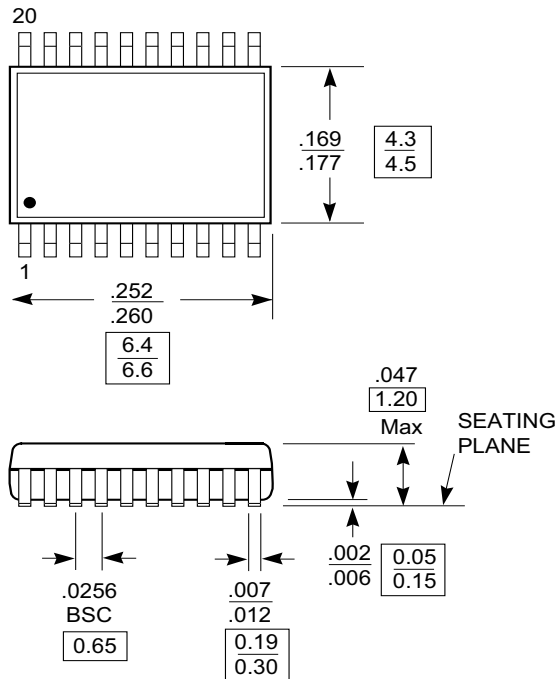
1. Minimum limits are guaranteed but not tested on Propagation Delays.
2. This parameter guaranteed but not production tested.

**PI74FCT573T Switching Characteristics over Operating Range**

| Parameters   | Description                                    | Conditions              | 573T |      | 573AT |     | 573CT |     | 573DT |     | Unit |
|--------------|------------------------------------------------|-------------------------|------|------|-------|-----|-------|-----|-------|-----|------|
|              |                                                |                         | Com. |      | Com.  |     | Com.  |     | Com.  |     |      |
|              |                                                |                         | Min  | Max  | Min   | Max | Min   | Max | Min   | Max |      |
| tPLH<br>tPHL | Propagation Delay <sup>(1)</sup><br>DN to ON   | CL = 50 pF<br>RL = 500Ω | 1.5  | 8.0  | 1.5   | 5.2 | 1.5   | 4.2 | 1.5   | 3.8 | ns   |
| tPLH<br>tPHL | Propagation Delay <sup>(1)</sup><br>LE to ON   |                         | 2.0  | 12.0 | 2.0   | 8.5 | 2.0   | 5.5 | 2.0   | 4.9 | ns   |
| tpZH<br>tpZL | Output Enable Time<br>OE to ON                 |                         | 1.5  | 9.5  | 1.5   | 6.5 | 1.5   | 5.5 | 1.5   | 5.5 | ns   |
| tpHZ<br>tPLZ | Output Disable Time <sup>(2)</sup><br>OE to ON |                         | 1.5  | 6.5  | 1.5   | 5.5 | 1.5   | 5.0 | 1.5   | 5.0 | ns   |
| tsU          | Setup Time HIGH or<br>LOW, DN to LE            |                         | 2.0  |      | 2.0   |     | 2.0   |     | 1.5   |     | ns   |
| tH           | Hold Time HIGH or<br>LOW, DN to LE             |                         | 1.5  |      | 1.5   |     | 1.5   |     | 1.0   |     | ns   |
| tw           | LE Pulse Width <sup>(2)</sup>                  |                         | 6.0  |      | 5.0   |     | 5.0   |     | 3.0   |     | ns   |
|              | HIGH                                           |                         |      |      |       |     |       |     |       |     |      |

**Notes:**

1. Minimum limits are guaranteed but not tested on Propagation Delays.
2. This parameter guaranteed but not production tested.

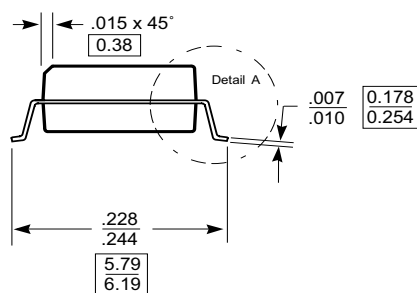
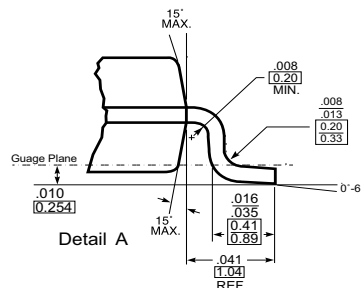
**Packaging Mechanical: 20-Pin TSSOP (L)**
**DOCUMENT CONTROL NO.**  
**PD - 1311**
**REVISION: E**  
**DATE: 03/09/05**

**Note:**

1. Package Outline Exclusive of Mold Flash and Metal Burr
2. Controlling dimensions in millimeters
3. Ref: JEDEC MO-153F/AC


**Pericom Semiconductor Corporation**  
**3545 N. 1st Street, San Jose, CA 95134**  
**1-800-435-2335 • www.pericom.com**
**DESCRIPTION: 20-Pin, 173-Mil Wide, TSSOP**
**PACKAGE CODE: L**



|                                   |
|-----------------------------------|
| DOCUMENT CONTROL NO.<br>PD - 1202 |
| REVISION: H<br>DATE: 10/22/07     |

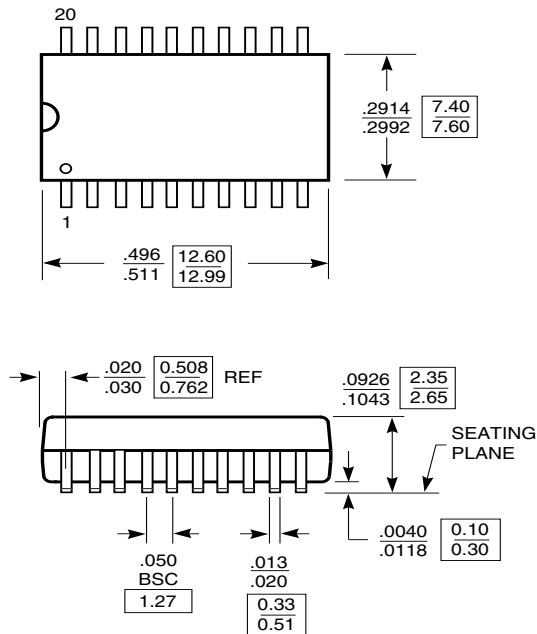


DESCRIPTION: 20-Pin, 150-Mil Wide, QSOP

PACKAGE CODE: Q

08-0291

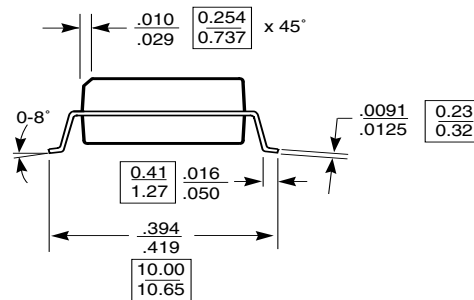
### Packaging Mechanical: 20-pin SOIC (S)



X.XX  
X.XX DENOTES CONTROLLING  
DIMENSIONS IN MILLIMETERS

**Notes:**

- 1) Controlling dimensions in millimeters.  
2) Ref: JEDEC MS-013D/AC



**DOCUMENT CONTROL NO.**  
**PD - 1006**

REVISION: D  
DATE: 03/09/05



**Pericom Semiconductor Corporation**  
3545 N. 1st Street, San Jose, CA 95134  
1-800-435-2335 • [www.pericom.com](http://www.pericom.com)

**DESCRIPTION: 20-Pin, 300-Mil Wide, SOIC**

**PACKAGE CODE: S**

**PI74FCT373T Ordering Information**

| Ordering Code  | Package Code | Speed Grade | Package Type                  |
|----------------|--------------|-------------|-------------------------------|
| PI74FCT373TLE  | L            | Blank       | Pb-free & Green, 20-pin TSSOP |
| PI74FCT373TQ   | Q            | Blank       | 20-pin QSOP                   |
| PI74FCT373TQE  | Q            | Blank       | Pb-free & Green, 20-pin QSOP  |
| PI74FCT373TS   | S            | Blank       | 20-pin SOIC                   |
| PI74FCT373TSE  | S            | Blank       | Pb-free & Green, 20-pin SOIC  |
| PI74FCT373ATQ  | Q            | A           | 20-pin QSOP                   |
| PI74FCT373ATQE | Q            | A           | Pb-free & Green, 20-pin QSOP  |
| PI74FCT373CTL  | L            | C           | 20-pin TSSOP                  |

**PI74FCT573T Ordering Information**

| Ordering Code  | Package Code | Speed Grade | Package Type                 |
|----------------|--------------|-------------|------------------------------|
| PI74FCT573TQE  | Q            | Blank       | Pb-free & Green, 20-pin QSOP |
| PI74FCT573TS   | S            | Blank       | 20-pin SOIC                  |
| PI74FCT573TSE  | S            | Blank       | Pb-free & Green, 20-pin SOIC |
| PI74FCT573ATS  | S            | A           | 20-pin SOIC                  |
| PI74FCT573ATSE | S            | A           | Pb-free & Green, 20-pin SOIC |
| PI74FCT573ATQ  | Q            | A           | 20-pin QSOP                  |
| PI74FCT573ATQE | Q            | A           | Pb-free & Green, 20-pin QSOP |
| PI74FCT573CTS  | S            | C           | 20-pin SOIC                  |
| PI74FCT573CTSE | S            | C           | Pb-free & Green, 20-pin SOIC |
| PI74FCT573CTQ  | Q            | C           | 20-pin QSOP                  |
| PI74FCT573CTQE | Q            | C           | Pb-free & Green, 20-pin QSOP |
| PI74FCT573DTQ  | Q            | D           | 20-pin QSOP                  |
| PI74FCT573DTQE | Q            | D           | Pb-free & Green, 20-pin QSOP |

**Notes:**

- Thermal characteristics can be found on the company web site at [www.pericom.com/packaging/](http://www.pericom.com/packaging/)
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel

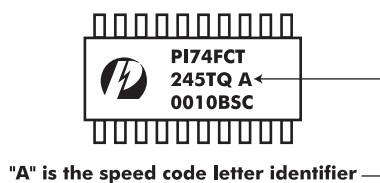
**Part Marking Information**

Pericom's standard product mark follows our standard part number ordering information, except for those products with a speed letter code. For marking purposes, the speed letter code mark is placed after the package code letter, rather than after the device number as it is ordered.

Although all products are marked immediately after assembly to assure material traceability, Pericom does not usually mark the speed code at that time. After electrical test screening and speed binning have been completed, we then perform an "add mark" operation which places the speed code letter at the end of the complete part number.

Please refer to the example shown below:

- Part Number as ordered: PI74FCT245ATQ
- Example of Part Number as marked:

**Notes:**

- 1) 8-pin DIP, 8-pin SOIC, 8-pin TSSOP, 14-pin SOIC, 16-pin QSOP, SC70, MSOP, and SOT23 packages are not marked with the Pericom logo due to space limitations on the package.