

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# RJK4012DPE

Silicon N Channel MOS FET  
High Speed Power Switching

REJ03G1575-0100

Rev.1.00

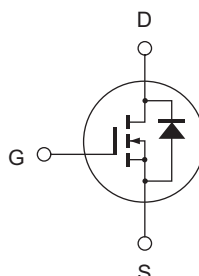
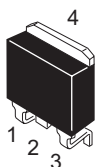
Aug 08, 2007

## Features

- Low on-resistance
- Low leakage current
- High speed switching

## Outline

RENESAS Package code: PRSS0004AE-B  
(Package name: LDKPAK(S)-(1) )



1. Gate
2. Drain
3. Source
4. Drain

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	400	V
Gate to source voltage	$V_{GSS}$	$\pm 30$	V
Drain current	$I_D$	15	A
Drain peak current	$I_{D(pulse)}$ <sup>Note1</sup>	45	A
Body-drain diode reverse drain current	$I_{DR}$	15	A
Body-drain diode reverse drain peak current	$I_{DR(pulse)}$ <sup>Note1</sup>	45	A
Avalanche current	$I_{AP}$ <sup>Note3</sup>	5	A
Avalanche energy	$E_{AR}$ <sup>Note3</sup>	1.4	mJ
Channel dissipation	$P_{ch}$ <sup>Note2</sup>	100	W
Channel to case thermal impedance	$\theta_{ch-c}$	1.25	°C/W
Channel temperature	$T_{ch}$	150	°C
Storage temperature	$T_{stg}$	-55 to +150	°C

Notes: 1.  $PW \leq 10 \mu s$ , duty cycle  $\leq 1\%$

2. Value at  $T_c = 25^\circ C$

3.  $STch = 25^\circ C$ ,  $T_{ch} \leq 150^\circ C$

## Electrical Characteristics

(Ta = 25°C)

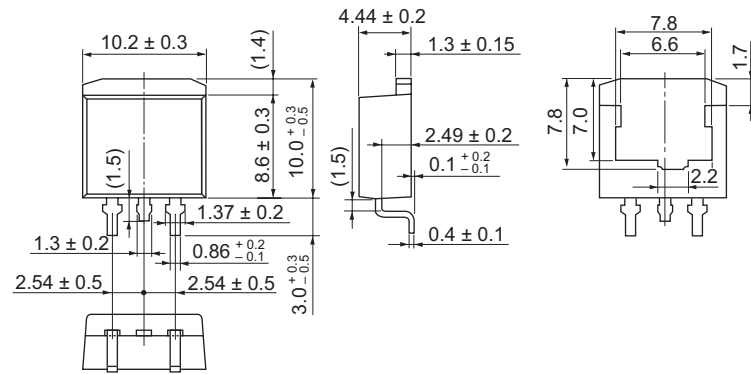
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	400	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 400 \text{ V}$ , $V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 0.1$	$\mu\text{A}$	$V_{GS} = \pm 30 \text{ V}$ , $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.34	0.41	$\Omega$	$I_D = 7.5 \text{ A}$ , $V_{GS} = 10 \text{ V}$ <sup>Note4</sup>
Input capacitance	$C_{iss}$	—	1100	—	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	$C_{oss}$	—	135	—	pF	
Reverse transfer capacitance	$C_{rss}$	—	17	—	pF	
Turn-on delay time	$t_{d(on)}$	—	30	—	ns	$I_D = 7.5 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 26.7 \Omega$ $R_g = 10 \Omega$
Rise time	$t_r$	—	29	—	ns	
Turn-off delay time	$t_{d(off)}$	—	77	—	ns	
Fall time	$t_f$	—	19	—	ns	
Total gate charge	$Q_g$	—	29	—	nC	$V_{DD} = 320 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 15 \text{ A}$
Gate to source charge	$Q_{gs}$	—	5.5	—	nC	
Gate to drain charge	$Q_{gd}$	—	13	—	nC	
Body-drain diode forward voltage	$V_{DF}$	—	0.91	1.55	V	$I_F = 15 \text{ A}$ , $V_{GS} = 0$ <sup>Note4</sup>
Body-drain diode reverse recovery time	$t_{rr}$	—	260	—	ns	$I_F = 15 \text{ A}$ , $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

## Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBAK(S)-(1)	SC-83	PRSS0004AE-B	LDBAK(S)-(1) / LDBAK(S)-(1)V	1.30g

Unit: mm



## Ordering Information

Part No.	Quantity	Shipping Container
RJK4012DPE-00-J3	1000 pcs	Taping

Notes:

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