New Product



Vishay General Semiconductor

Photovoltaic Solar Cell Protection Schottky Rectifier

Ultra Low $V_F = 0.30$ V at $I_F = 5.0$ A



- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- High forward surge capability
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

PRIMARY CHARACTERISTICS			
I _{F(AV)}	20 A		
V _{RRM}	45 V		
I _{FSM}	250 A		
V_F at $I_F = 20$ A	0.42 V		
T _{OP} max.	150 °C		

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: P600

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSB2045	UNIT	
Device marking code		V2045		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)} ⁽¹⁾	20	A	
	I _{F(AV)} ⁽²⁾	6.5		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	250	А	
Operating junction temperature range	T _{OP}	- 40 to + 150	°C	
Storage temperature range	T _{STG}	- 40 to + 175	°C	
Junction temperature in DC forward current without reverse bias, t \leq 1 h (fig. 2)	T _J ⁽³⁾	≤ 200	°C	

Notes

(1) With heatsink

⁽²⁾ Without heatsink, free air

⁽³⁾ Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test



VSB2045



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5.0 A	T _A = 25 °C		0.44	-	V
	I _F = 10 A			0.46	-	
	I _F = 20 A		- V _F ⁽¹⁾	0.50	0.58	
	I _F = 5.0 A	T _A = 125 °C		0.30	-	
	I _F = 10 A			0.35	-	
	I _F = 20 A			0.42	0.50	
Reverse current		T _A = 25 °C	1 (2)	23.4	1200	μA
	V _R = 45 V	T _A = 125 °C	- I _R ⁽²⁾	11.9	35	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	2050	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: 40 ms pulse width

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VSB2045	UNIT	
Thermal resistance	R _{0JA} ⁽¹⁾	55	°C/W	
	$R_{\theta JL}$ ⁽¹⁾	3.5		
Typical thermal resistance	R _{θJL} ⁽²⁾	2.5	°C/W	

Notes

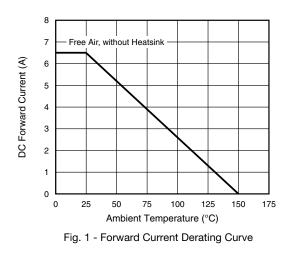
⁽¹⁾ Without heatsink, free air; units mounted on PCB with 2 mm x 2 mm copper pad areas at 9.5 mm lead length

⁽²⁾ Leads clipped at 3 mm lead length from plastic body on 7.0 cm x 2.2 cm x 1.9 cm x 2 heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSB2045-M3/54	1.88	54	800	13" diameter paper tape and reel	
VSB2045-M3/73	1.88	73	300	Ammo pack packaging	

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)



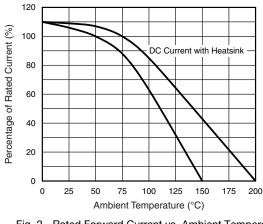


Fig. 2 - Rated Forward Current vs. Ambient Temperature



100 000

10 000

1000

100

10

1

10 000

1000

100

0.1

Junction Capacitance (pF)

20

Instantaneous Reverse Current (µA)



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T_A = 150 °C

T_A = 125 °C

 $T_A = 100 \text{ °C}$

T_A = 25 °C

60

Percent of Rated Peak Reverse Voltage (%)

Fig. 5 - Typical Reverse Leakage Characteristics

80

= 25 °C

1.0 MHz

 $\mathsf{V}_{\mathsf{sig}}$ = 50 mV

10

Reverse Voltage (V)

Fig. 6 - Typical Junction Capacitance

100

100

40

1

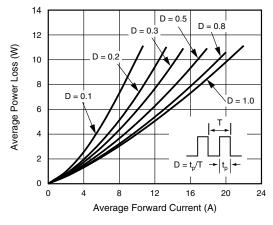


Fig. 3 - Forward Power Loss Characteristics

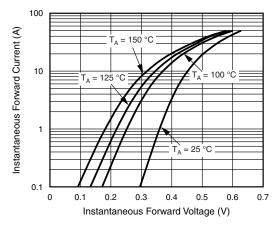
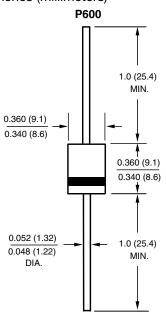
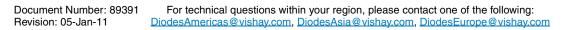


Fig. 4 - Typical Instantaneous Forward Characteristics









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