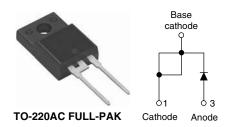


Vishay High Power Products

Input Rectifier Diode TO-220 FULL-PAK, 10 A



PRODUCT SUMMARY		
V _F at 10 A	< 1.1 V	
I _{FSM}	200 A	
V _{RRM}	800/1200 V	

DESCRIPTION

The 10ETS12FPPbF rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.



Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

Fully isolated package (V_{INS} = 2500 V_{RMS}) is UL E78996 approved

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS	
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	12.0	16.0	А	

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Sinusoidal waveform	10	Α	
V_{RRM}	Range	800/1200	V	
I _{FSM}		200	A	
V _F	10 A, T _J = 25 °C	1.1	V	
T _J		- 40 to 150	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
10ETS08FPPbF	800 900		0.5		
10ETS12FPPbF	1200	1300	0.5		

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180 ° conduction half sine wave	10	
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	170	Α
non-repetitive surge current	I _{FSM}	10 ms sine pulse, no voltage reapplied	200	
Maximum I ² t for fusing	l ² t -	10 ms sine pulse, rated V _{RRM} applied	130	A ² s
Waxiiiidiii i-t ioi iusiiig		10 ms sine pulse, no voltage reapplied	145	A-S
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied	1450	A ² √s

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V_{FM}	10 A, T _J = 25 °C		1.1	V
Forward slope resistance	r _t	T _{.1} = 150 °C		20	mΩ
Threshold voltage	V _{F(TO)}	1,J = 150 °C		V	
Maximum rayaraa laakaga aurrant	mum voivoro logico a guirrent	T _J = 25 °C	V _B = Rated V _{BBM}	0.05	mA
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	VR = nateu VRRM	0.50	IIIA

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and stor temperature range	age	T _J , T _{Stg}		- 40 to 150	°C
Maximum thermal resistant junction to case	ce,	R _{thJC}	DC operation	2.5	
Maximum thermal resistand junction to ambient	ce,	R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
Approximate weight			0.07	OZ.	
Mounting torque	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style TO-220 FULL-PAK (94/V0)	220 FULL-PAK (94/V0) 10ETS12FP	

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Input Rectifier Diode TO-220 FULL-PAK, 10 A

Vishay High Power Products

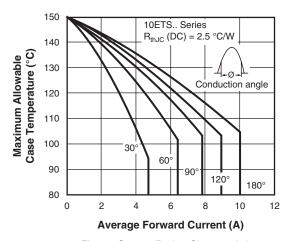


Fig. 1 - Current Rating Characteristics

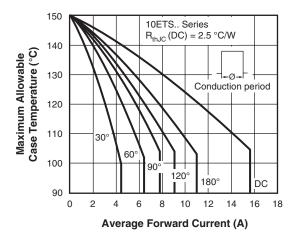


Fig. 2 - Current Rating Characteristics

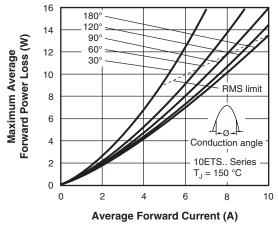


Fig. 3 - Forward Power Loss Characteristics

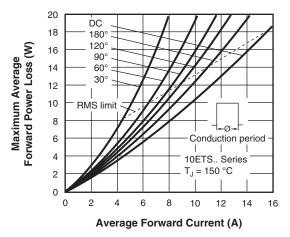


Fig. 4 - Forward Power Loss Characteristics

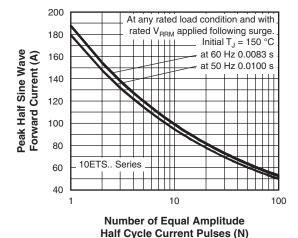


Fig. 5 - Maximum Non-Repetitive Surge Current

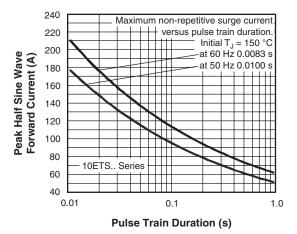


Fig. 6 - Maximum Non-Repetitive Surge Current

Vishay High Power Products

Input Rectifier Diode TO-220 FULL-PAK, 10 A



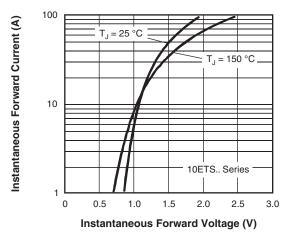


Fig. 7 - Forward Voltage Drop Characteristics

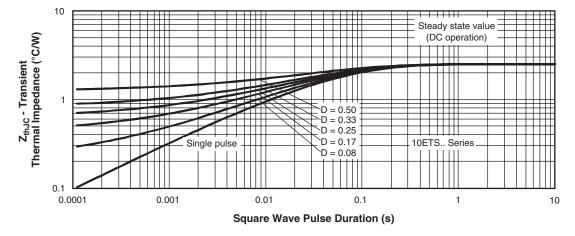


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



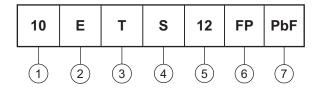
08 = 800 V

12 = 1200 V

Input Rectifier Diode TO-220 FULL-PAK, 10 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



1 - Current rating (10 = 10 A)

2 - Circuit configuration:

E = Single diode

- Package:

T = TO-220AC

4 - Type of silicon:

S = Standard recovery rectifier

5 - Voltage rating —

6 - FULL-PAK

7 - • None = Standard production

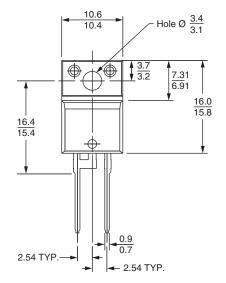
• PbF = Lead (Pb)-free

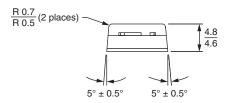
LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95005			
Part marking information	http://www.vishay.com/doc?95009		

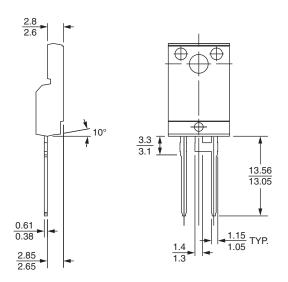
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Vishay Semiconductors

DIMENSIONS in millimeters







Lead assignments

<u>Diodes</u> 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK





Vishay

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