TEMIC Semiconductors

$2 \text{ K} \times 8 \text{ High Speed CMOS SRAM}$

Short description. Please refer to the full datasheet for detailed technical information.

Introduction

The HM 65728B is a high speed CMOSstatic RAM organized as 2048×8 bits. It is manufactured using MHS's high performance CMOS technology.

Access times as fast as 25 ns are available with maximum power consumption of only 600 mW.

The HM 65728B features fully static operation requiring no external clocks or timing strobes. The automatic power-down feature reduces the power consumption by 80 % when the circuit is deselected.

Easy memory expansion is provided by an active low chip select (\overline{CS}) and active low output enable (\overline{OE}) and three state drivers.

All inputs and outputs of the HM 65728 are TTL compatible and operate from single 5V supply thus simplifying system design.

The HM 65728B is 100 % processed following the test methods of MIL STD 883 and/or ESA/SCC 9000 making it ideally suitable for military/space applications that demand superior levels of performance and reliability.

Features

Fast Access Time

Commercial: 25/35/45/55 ns (max) Military: 25/35/45/55 ns (max)

• Low Power Consumption Active :

550 mW (max)

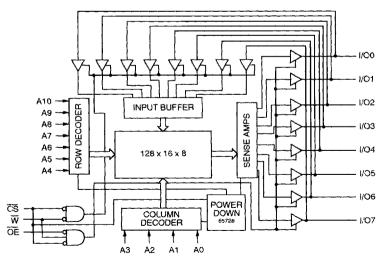
• Wide Temperature Range:

-55°C to + 125°C

- · 300 and 600 Mils Width Package
- TTL Compatible Inputs and Outputs
- Asynchronous
- Capable of Withstanding Greater than 2000 V Electrostatic Discharge
- Single 5 Volt Supply

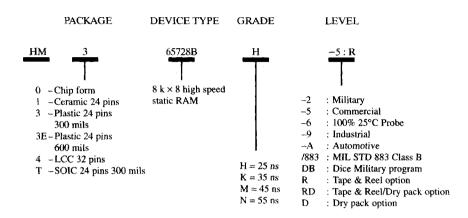
Interface

Block Diagram





Ordering Information



Military Version

The following table gives package/access time/process flow available combinations

| Temp. tange | Parkages | | Access 7 | Tune (ns) | | Std process 659288 |
|-------------|----------|----|----------|-----------|-----|--------------------|
| | | 25 | 35 | 45 | 55 | Mil flows |
| M | 1 | • | • | • | • | • |
| | 0 | X | X X | X X | X X | X X |
| | | | | | ļ | ! |

• = product in production

X = call sales office for availability

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