

HCMOS-Logic

Digital Delay Units

SERIES: **DDU-4C**

5 Outputs
14 pins DIP

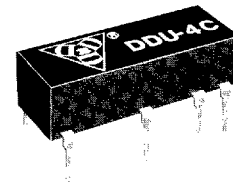


Features:

- Completely interfaced for CMOS.
- No external components required.
- P.C. board space economy achieved.
- Low profile.
- Fits standard 14 pins DIP socket.

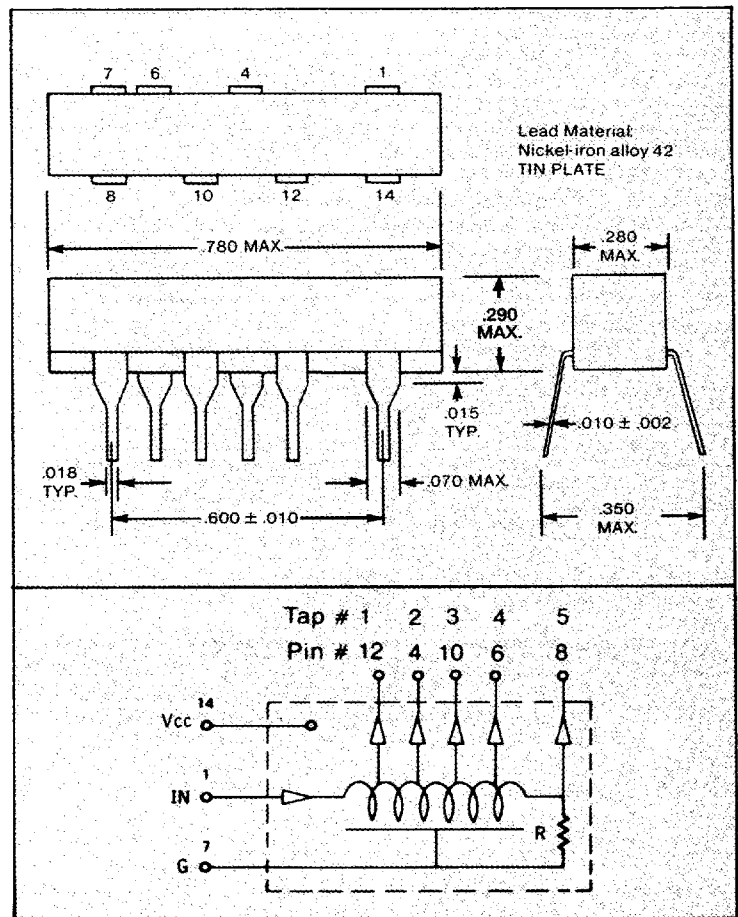
Specifications:

- No. Taps: 5 equally spaced taps.
- Total delay tolerance: $\pm 5\%$ or 2 ns whichever is greater.
- Rise time: 8 ns typ.
- Temperature coefficient: 300 PPM/ $^{\circ}\text{C}$.
- Temperature range: 0°C to $+70^{\circ}\text{C}$.
- Supply Voltage: 5 Vdc $\pm 5\%$.
- Supply current:
 - $I_{\text{CCH}} = 10 \text{ ma}$
 - $I_{\text{CCL}} = 40 \mu\text{a}$
- Fan-out: 10 LSTTL loads min.
- DC Parameters: See HCMOS-Logic Table on Page 6.



Part No.	Total Delay (ns)	Tap Delay (ns)
DDU-4C-5050	50	10 ± 2
DDU-4C-5060	60	12 ± 2
DDU-4C-5075	75	15 ± 2
DDU-4C-5100	100	20 ± 2
DDU-4C-5125	125	25 ± 2
DDU-4C-5150	150	30 ± 3
DDU-4C-5175	175	35 ± 3
DDU-4C-5200	200	40 ± 4
DDU-4C-5250	250	50 ± 5
DDU-4C-5300	300	60 ± 6
DDU-4C-5400	400	80 ± 8
DDU-4C-5500	500	100 ± 10

Note: This series is available with 3.3V supply. Simply add 3 in the P/N. Ex: DDU-4C-5125 becomes DDU-4C3-5125.



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