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PATENTED: THIS PRODUCT IS COVERED BY U.S. PATENT 5,736,910



DOCUMENT / PART NO.: SI-60106-F

TITLE: 10/100BT, TAB DOWN, SHIELDED

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Use new format and change to high temp housing						David Chan		Steve Weller		06317							
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RoHS 2002/95/EC

ELECTRICAL CHARACTERISTICS @ 25°C

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P6-P5-P4): (J6-J3) : 1CT: 1CT± 3% (P3-P2-P1): (J2-J1) : 1CT: 1CT ± 3%

2.0 INDUCTANCE: (P6-P4) : 350uH MIN. @ 0.1V,00KHz, 8mA DC Bias (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT): 0.3 MAX. @ 1MHz P3-P1 (WITH J2 AND J1 SHORT): 0.3 MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3) : 30pf MAX @ 1MHz (P3,P2,P1) TO (J2,J1) : 30pf MAX. @ 1MHZ

5.0 DC RESISTANCE: (J6-J3)=(J2-J1) : 1.2 ohms Max.

RECEIVE

6.0 RETURN LOSS: 1MHz TO 30MHz : 18dB MIN. 60MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).

7.0 DIELECTRIC WITHSTAND: (J1, J2) T0 (P1, P3) : 1500 Vrms (J3, J6) T0 (P4,P6) : 1500 Vrms

8.0 INSERTION LOSES=RL=100 ohms

100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIMERS=100 OHMS AND RL = 100 OHMS

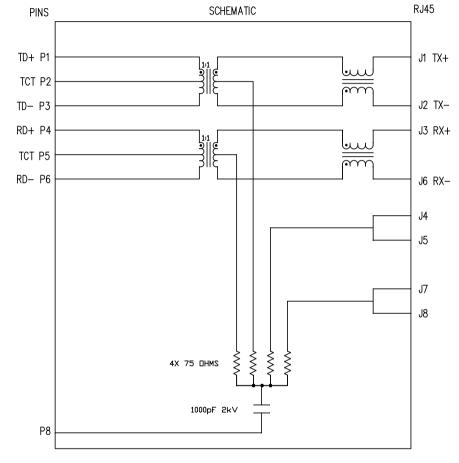
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

10.0CROSS TALK:1MHz TO 100MHz : 40 dB TYP

TITLE

11.0 COMMON TO COMMON MODE ATTENUATION:

30MHz TO 100MHz: 35dB TYP



NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

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DRAWN BY	DATE
Bain Liu	09-01-09

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PART NO. / DRAWING NO.
SI-60106-F

FILE NAME
SI-60106-F.DWG

STANDARD DIM.
TOL. IN INCH
.X
.XX

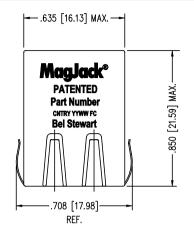
±0.005

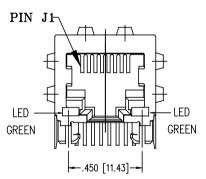
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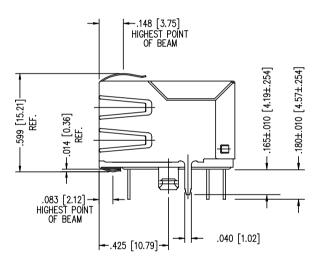
PAGE: 2



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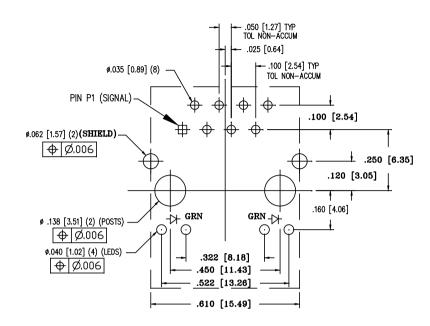






LED SPECIFICATION						
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	*(TYP)			
GREEN	565 nm	2.5 V	2.2 V			

*WITH A FORWARD CURRENT OF 20 mA (TYP)



P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

NOTES:

1. CONNECTOR MATERIALS:

HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD.

50 MICRO-INCHES MIN. IN CONTACT AREA.

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. WAVE SOLDER COMPATIBLE PREHEAT 125°C/90SECS.

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TITLE

SI-60106-F
FILE NAME
SI-60106-F.DV

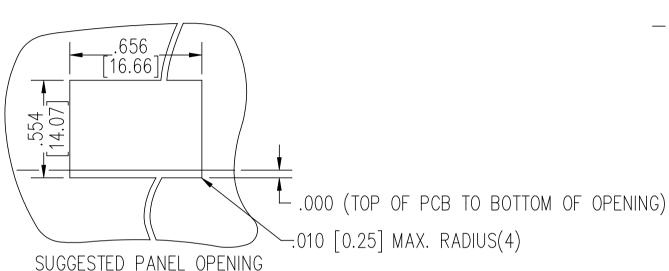
PART NO. / DRAWING NO.	STANDA	RD DIM.	[] METRIC DIM. AS REFERENCE		
SI-60106-F	TOL. IN INCH		UNIT : INCH [mm]	REV.: A0	
FILE NAME	.X		SCALE: N/A	SIZE: A4	
SI-60106-F.DWG	.XX		SCALE. NA	SIZE . A4	
91-00 100-F.DWG	XXX.	±0.005		PAGE: 3	

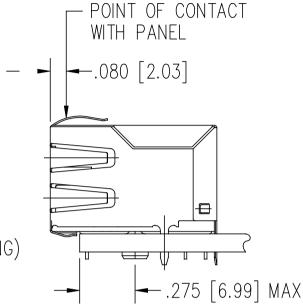


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- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

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	PART NO. / DRAWING NO.	STANDA	ARD DIM.	[] METRIC DIM. AS REFERENCE			
	SI-60106-F	TOL. IN INCH		UNIT : INCH [mm]	REV.: A0		
	FILE NAME	X		SCALE: N/A	SIZE : A4		
SI-60	SI-60106-F.DWG	XX					
	0. 00.00	l .xxx	±0.005		PAGE: 4		

