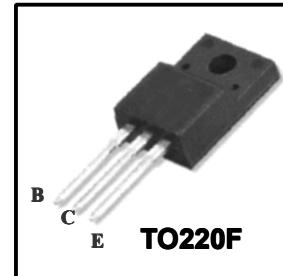


## *High Voltage Fast-Switching NPN Power Transistor*

### Features

- ◆ Very High Switching Speed
- ◆ High Voltage Capability
- ◆ Wide Reverse Bias SOA
- ◆ Isolation Voltage (  $V_{ISO} = 4000V$  AC )



### General Description

This device is designed for high voltage, High speed switching characteristics required such as lighting system ,switching mode power supply.

### Absolute Maximum Ratings

Symbol	Parameter	Test Conditions	Value	Units
$V_{CES}$	Collector-Emitter Voltage	$V_{BE} = 0$	700	V
$V_{CEO}$	Collector-Emitter Voltage	$I_B = 0$	400	V
$V_{EBO}$	Emitter-Base Voltage	$I_C = 0$	9.0	V
$I_C$	Collector Current		8.0	A
$I_{CP}$	Collector pulse Current		16	A
$I_B$	Base Current		4.0	A
$I_{BM}$	Base Peak Current	$t_P = 5ms$	8.0	A
$P_c$	Total Dissipation at $T_c = 25^\circ C$		40	W
$T_J$	Operation Junction Temperature		- 40 ~ 150	°C
$T_{STG}$	Storage Temperature		- 40 ~ 150	°C

Tc: Case temperature (good cooling)

### Thermal Characteristics

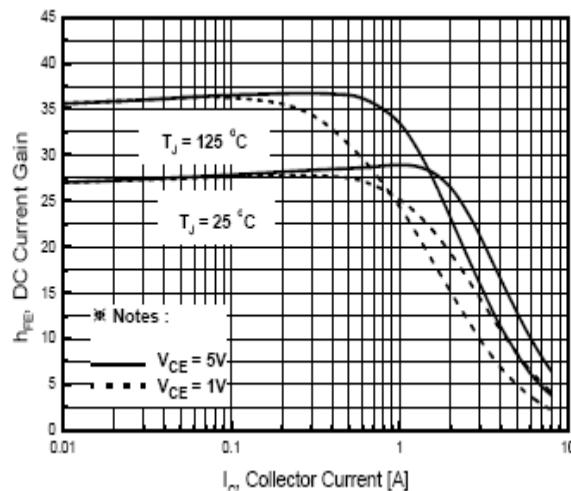
Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance Junction to Case	3.13	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	62.5	°C/W

**Electrical Characteristics** ( $T_c=25^\circ\text{C}$  unless otherwise noted)

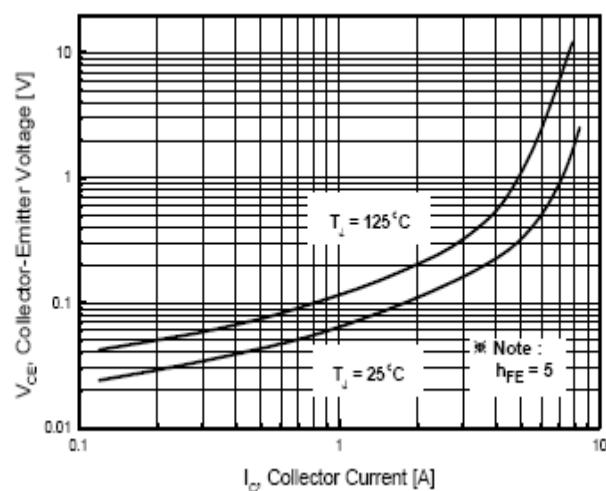
Symbol	Parameter	Test Conditions	Value			Units
			Min	Typ	Max	
$V_{CEO(sus)}$	Collector-Emitter Breakdown Voltage	$I_c=10\text{mA}, I_b=0$	400	-	-	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_c=2.0\text{A}, I_b=0.4\text{A}$	-	-	1.0	
		$I_c=5.0\text{A}, I_b=1.0\text{A}$	-	-	2.0	V
		$I_c=8.0\text{A}, I_b=2.0\text{A}$	-	-	3.0	
	Base-Emitter Saturation Voltage	$I_c=5.0\text{A}, I_b=1.0\text{A}$	-	-	2.5	V
		$T_c=100^\circ\text{C}$	-	-	-	
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_c=2.0\text{A}, I_b=0.4\text{A}$	-	-	1.2	V
		$I_c=5.0\text{A}, I_b=1.0\text{A}$	-	-	1.6	
	Collector-Base Cutoff Current ( $V_{be}=-1.5\text{V}$ )	$I_{CBO}=700\text{V}$	-	-	1.0	mA
		$V_{cb}=700\text{V}, T_c=100^\circ\text{C}$	-	-	5.0	
$I_{EBO}$	Emitter -Base Cutoff Current	$V_{eb}=9\text{V}$	-	-	1.0	mA
$h_{FE}$	DC Current Gain	$V_{ce}=5\text{V}, I_c=2.0\text{A}$	10	-	40	
		$V_{ce}=5\text{V}, I_c=5.0\text{A}$	5	-	40	
$f_T$	Current Gain Bandwidth Product	$V_{CE} = 10\text{V}, I_C = 0.5\text{A}$	4			MHz
$t_{on}$ $t_s$ $t_f$	Tum on Time Storage Time Fall Time	$V_{CC}=125\text{V}, I_C=5\text{A}$ $I_{B1}=I_{B2}=-1.0\text{A}$ $R_L=50\Omega$	-	-	1.6	$\mu\text{s}$
			-	-	3.0	
			-	-	0.7	

**Note:**

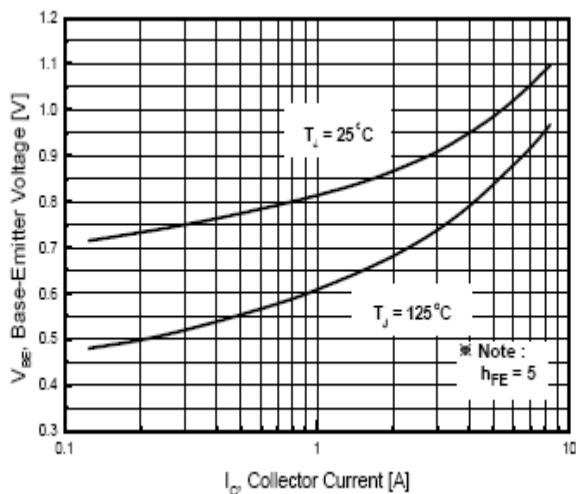
Pulse Test : Pulse width 300, Duty cycle 2%



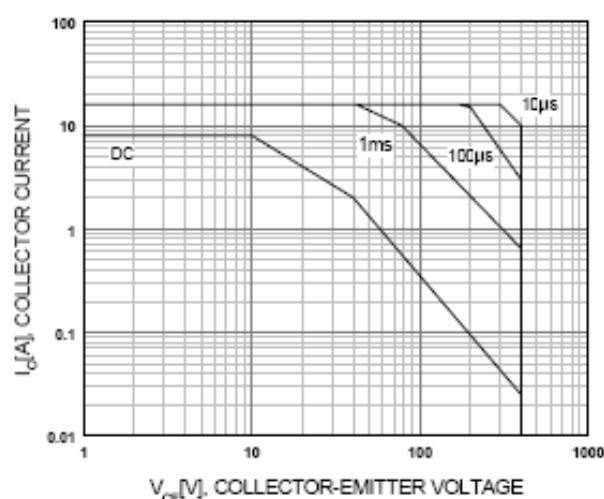
**Fig. 1 DC Current Gain**



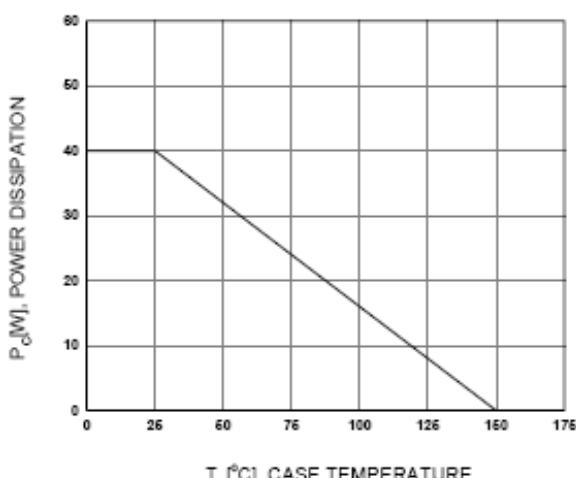
**Fig. 2 Collector-Emitter Saturation Voltage**



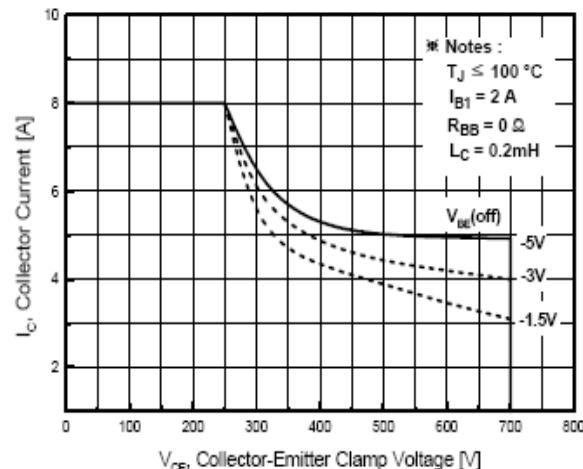
**Fig. 3 Base-Emitter Saturation Voltage**



**Fig. 4 Safe Operation Area**

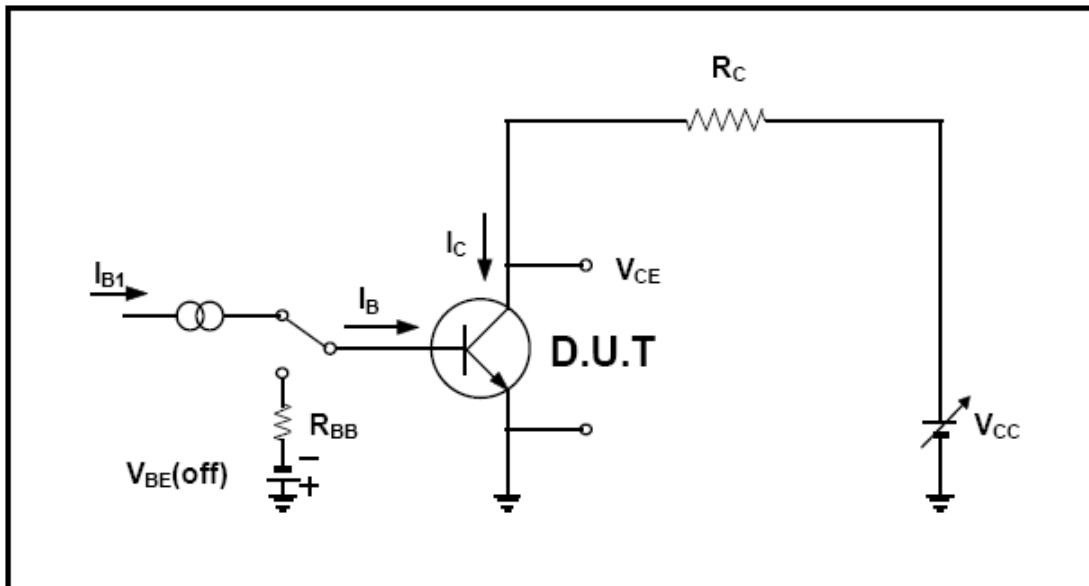


**Fig. 5 Power Derating**

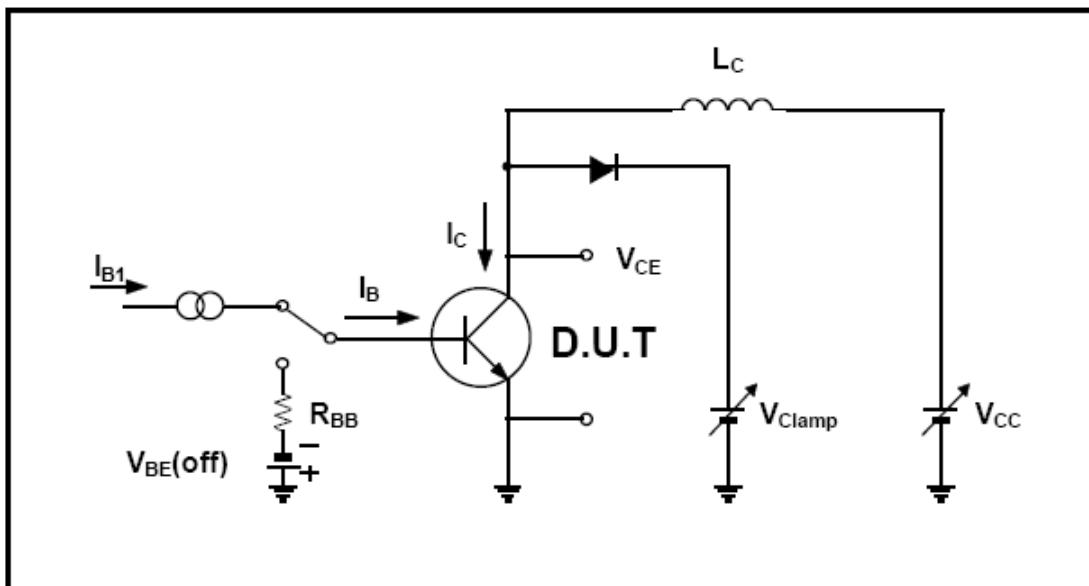


**Fig. 6 Reverse Biased Safe Operation Area**

### Resistive Load Switching Test Circuit



### Inductive Load Switching & RBSOA Test Circuit



## **TO-220F Package Dimension**

