

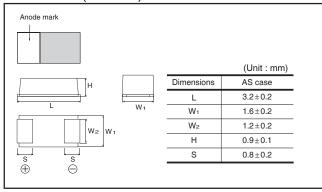
# Chip tantalum capacitors (Bottom surface electrode type : Large capacitance)

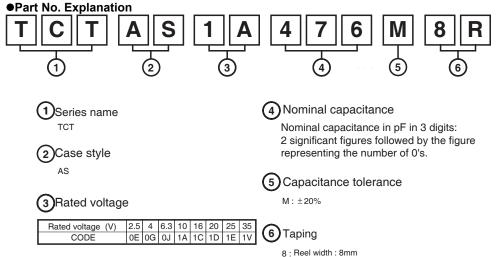
**TCT Series AS Case** 

#### Features (AS)

- 1) Vital for all hybrid integrated circuits board application.
- 2) Wide capacitance range.
- 3) Screening by thermal shock.

•Dimensions (Unit : mm)





R : Positive electrode on the side opposite to sprocket hole

#### • Rated table

(μF)			Ra	ated volt	age (V,D	)C)		
(μι )	2.5	4	6.3	10	16	20	25	35
1.0 (105)								AS
1.5 (155)								AS *
2.2 (225)								AS *
3.3 (335)							AS *	
4.7 (475)							AS	
6.8 (685)						AS *		
10 (106)						AS		
15 (156)					AS *			
22 (226)					AS			
33 (336)				AS				
47 (476)			AS	AS				
68 (686)			AS*					
100 (107)		AS	AS					
150 (157)		AS*						
220 (227)	AS *	AS						

\* Under development

#### Marking

The indications listed below should be given on the surface of a capacitor.

Polarity : The polarity should be shown by □ bar. (on the anode side)
 Rated DC voltage : Due to the small size of AS case, a voltage code is used as shown below.
 Visual typical example (1) voltage code (2) capacitance code

Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
А	10
С	16
D	20
Е	25
V	35

Capacitance Code	Nominal Capacitance (µF)			
A	1.0			
E	1.5			
J	2.2			
N	3.3			
S	4.7			
W	6.8			
а	10			
е	15			
j	22			
n	33			
S	47			
w	68			
ā	100			
ē	150			
j	220			

[AS case]

note 1)

 $\frac{A}{(1)}$   $\frac{s}{(2)}$ 



#### • Characteristics

Iter	m					Pe	rforn	nano	се			Test	con	diti	ons (based o	on JIS C 5101-	1 and JIS C 5101-
Operating Tem		-5	5°C	to +	125	°C						Volta	ige r	red	uction when	temperature ex	ceeds +85°C
Maximum operat temperature with derating	ting i no voltage	+8	5°C														
Rated voltage (	VDC)	2.5	4	6.3	10	16	20	25	3	5		at 85	õ°C				
Category voltag	je (VDC)	1.6	2.5	4	6.3	10	13	16	2	2		at 12	25°C	;			
Surge voltage (	VDC)	3.2	5.0	8	13	20	26	32	4	4		at 85	S°C				
DC Leakage cu	current Shall be satisfied the voltage on " Standard list "						As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage : Rated voltage for 5min										
Capacitance tolerance Shall be satisfied allowance range. ±20%						As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit											
Tangent of loss (Df, tan δ)	angle	<ul> <li>Shall be satisfied the voltage on</li> <li>" Standard list "</li> </ul>						As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit									
Impedance		Shall be satisfied the voltage on " Standard list "					As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency : 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit										
Resistance to Soldering heat	Appearance		ere s e inc								rmality.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3 Dia idea both					
	L.C.	Le	ss th	an 2	2009	% of	i initi	al lir	mit	t		Dip in the solder bath Solder temp : 260±10°C					
	ΔC / C	AS0E227 : Within +20/-30% of initial value AS0G227 : Within +20/-30% of initial value Others:Within ±20% of initial value						Duration       : 5±0.5s         Repetition       : 1         After the specimens, leave it at room temperature for over 24h and then measure the sample.									
	Df (tan δ)	Le	ss th	an 2	2009	% of	initi	al lir	mit	t							
Temperature cycle	Appearance		ere s e inc								rmality.	As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3					
	L.C.	Les	ss th	an :	2009	% of	initi	al lir	mit	t					5 cycles	vithout discontir	ulation
	ΔC / C	AS0E227 : Within ±30% of initial value						ue	1, , ,			Temp.	Time	]			
		AS	0G2	27 :	: Wit	hin	±30°	% of	f ir	nitial v			1	-	–55±3°C	30±3min.	-
		Oth	ners:	Wit	hin ±20% of initial value					2	F	Room temp.	3min. or less	-			
	Df (tan $\delta$ )	Le	ss th	an a	2009	% of	initi	al lir	mit	t			3	+	125±2°C	30±3min.	1
													4	F	Room temp.	3min. or less	1
							After the specimens, leave it at room temperature for over 24h and then measure the sample.										
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.				rmality.	As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3										
	L.C. Less than 200% of initial limit									e under such at							
	ΔC / C	Wi	thin :	+20	% of	init	ial v	alue	Э			condition that the temperature and humidity are 60±2°C and 90 to 95% RH, respectively, for 500±12h					
	Df (tan δ)	AS	0G2	27 :	: Les	s th	nan 3	3009	% (	of init of init itial li	limit	<ul> <li>leave it at room temperature for over 24h and then measure the sample.</li> </ul>					

Iter	n	Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)
Temperature	Temp.	–55°C	As per 4.29 JIS C 5101-1 As per 4.13 JIS C 5101-3
Stability	ΔC / C	AS0E227 : Within 0/-20% of initial value AS0G227 : Within 0/-20% of initial value Others:Within 0/-15% of initial value	As per 4.15 515 C 5101-5
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "	
	L.C.	_	
	Temp.	+85°C	
	ΔC / C	Within +15/0% of initial value	
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "	
	L.C.	Less than 1000% of initial limit	
	Temp.	+125°C	
	ΔC / C	Within +20/0% of initial value	
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "	
	L.C.	Less than 1250% of initial limit	
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1
	L.C.	Less than 200% of initial value	As per 4.14JIS C 5101-3 Apply the specified surge voltage via the serial resistance of
	ΔC / C	Within ±20% of initial value	1kΩ every 5±0.5 min. for 30±5 s. each time in the atmospheric condition of $85\pm2^{\circ}$ C.
	Df (tan δ)	Less than 200% of initial limit	Repeat this procedure 1,000 times. After the specimens, leave it at room temperature for over 24h and then measure the sample.
Loading at	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1
High temperature	L.C.	Less than 200% of initial limit	As per 4.15 JIS C 5101-3 After applying the rated voltage for 1000+36/0 h without
	ΔC / C	AS0E227 : Within +20/-30% of initial value AS0G227 : Within +20/-30% of initial value Others:Within ±20% of initial value	discontinuation via the serial resistance of $3\Omega$ or less at a temperature of $85\pm2^{\circ}$ C, leave the sample at room temperature / humidity for over 24h and measure the value.
	Df (tan δ)	AS0E227 : Less than 300% of initial limit AS0G227 : Less than 300% of initial limit Others:Less than 200% of initial limit	-
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below) (Unit : mm) f(Apply force) thickness=1.6mm f(Apply force)

lt	em	Performance	Test conditions (JIS C 5101-1 and JIS C 5101-3)		
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.		
			Apply force a circuit board		
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.		
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.		
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration : 3±0.5s Solder : M705 Flux : Rosin 25% IPA 75%		
Vibration Capacitance Measure value should not fluctuate during the measurement.		Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm		
	Appearance	There should be no significant abnormality.	Time : 2h each in X and Y directions Mounting : The terminal is soldered on a print circuit board.		

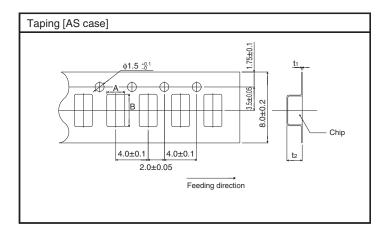
### • Standard products list, TCT series AS Case

Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C	lo	ingent ss ang 120Hz (%)	le	Impedance 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.5min (µA)	-55°C	25°C 85°C	125°C	(Ω)
* TCT AS 0E 227M8R	2.5	1.6	3.2	220	±20	27.5	35	20	25	2.5
TCT AS 0G 107M8R	4	2.5	5	100	±20	20	35	20	25	3
* TCT AS 0G 157M8R	4	2.5	5	150	±20	30	35	20	25	2.7
TCT AS 0G 227M8R	4	2.5	5	220	±20	88	80	30	40	2.5
TCT AS 0J 476M8R	6.3	4	8	47	±20	6	35	20	25	4
* TCT AS 0J 686M8R	6.3	4	8	68	±20	21.5	35	20	25	4
TCT AS 0J 107M8R	6.3	4	8	100	±20	31.5	34	18	24	3
TCT AS 1A 336M8R	10	6.3	13	33	±20	6.6	30	15	20	4
TCT AS 1A 476M8R	10	6.3	13	47	±20	9.4	35	20	25	4
* TCT AS 1C 156M8R	16	10	20	15	±20	4.8	30	15	20	4
TCT AS 1C 226M8R	16	10	20	22	±20	7.1	35	20	25	4
* TCT AS 1D 685M8R	20	13	26	6.8	±20	2.8	30	15	20	8
TCT AS 1D 106M8R	20	13	26	10	±20	4	30	15	20	4
* TCT AS 1E 335M8R	25	16	32	3.3	±20	1.7	30	15	20	8
TCT AS 1E 475M8R	25	16	32	4.7	±20	2.4	30	15	20	8
TCT AS 1V 105M8R	35	22	44	1	±20	0.7	30	15	20	8
* TCT AS 1V 155M8R	35	22	44	1.5	±20	1.1	30	15	20	8
* TCT AS 1V 225M8R	35	22	44	2.2	±20	1.6	30	15	20	8

\*=Under development

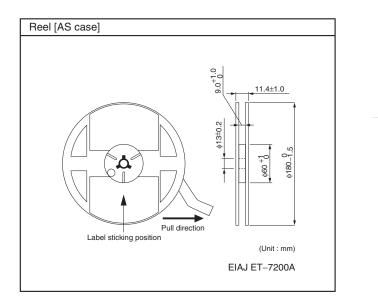
#### • Packaging specifications

e i dona;	ging op	oomout		(Unit : mm)
Case code	A <u>+</u> 0.1	B <u>+</u> 0.1	t1 <u>+</u> 0.05	t2 <u>+</u> 0.1
AS	1.9	3.5	0.25	1.1



#### • Packaging style

Case code	Packaging	Packag	ging style	Symbol	Basic ordering units
AS case	Taping	plastic taping	¢180mm Reel	R	3,000pcs



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