

NPN small signal transistor

MMSTA13

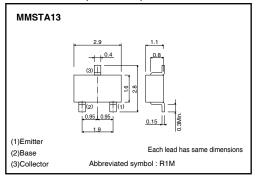
Features

(1) High Current Gain.

Packaging specifications

Туре	Package	Taping
	Code	T146
	Basic ordering unit (pieces)	3000
MMSTA13		0

●Dimensions (Unit : mm)



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	30	V	
Collector-emitter voltage	Vces	30	V	
Emitter-base voltage	VEBO	10	V	
Collector current	lc	0.3	Α	
Collector power dissipation	Pc	0.2	W	
Collector power dissipation	FC	0.35	W *	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to 125	°C	

^{*} Mounted on a 7×5×0.6 mm CERAMIC SUBSTRATE

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVces	30	-	_	V	Ic= 100μA
Collector-emitter breakdown voltage	BVceo	30	_	_	V	Ic= 10μA
Emitter-base breakdown voltage	ВУево	10	_	_	V	Iε= 10μA
Collector-base cutoff current	Ісво	_	-	0.1	μΑ	VcB= 30V
Emitter-base cutoff current	ICEO	-	_	0.1	μΑ	V _{EB} = 10V
Collector-emitter saturation voltage	VCE(sat)	-	-	1.5	V	Ic/Iв= 100mA/ 100μA
Base-emitter voltage	V _{BE(on)}	-	-	2.0	V	VcE= 5V, Ic= 100mA *
DC current transfer ratio	hfe	5000	-	_	-	VcE= 5V, Ic= 10mA
DC current transfer fatto		10000	_	_		VcE= 5V, Ic= 100mA *
Transition frequency	f⊤	125	_	_	MHz	Vc= 5V, I=-10mA, f=100MHz
Collector output capacitance	Cob	_	5.4	_	pF	Vcв= 10V, f=100kHz, I∈= 0

^{*} Pulsed

MMSTA13 Data Sheet

•Electrical characteristics curves

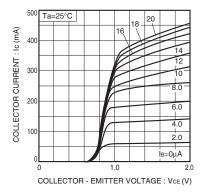


Fig.1 Typical output characteristics (I)

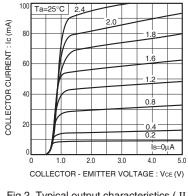


Fig.2 Typical output characteristics (II)

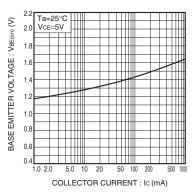


Fig.3 Base emitter 'ON' voltage vs. collector current

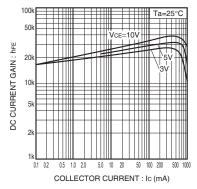


Fig.4 DC current gain vs. collector current ($\rm I$)

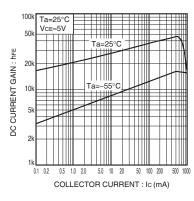


Fig.5 DC current gain vs. collector current (II)

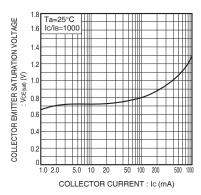


Fig.6 Collector emitter saturation voltage vs. collector current

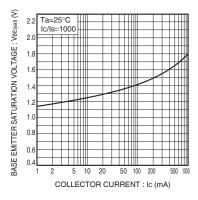


Fig.7 Base emitter saturation voltage vs. collector current

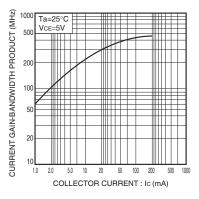


Fig.8 Current gain-bandwidth product vs. collector current

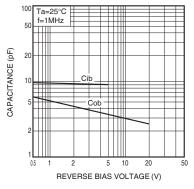


Fig.9 Capacitance vs. reverse bias voltage

Notes

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