

- DIL-8 pin package compatible
- Hermetically sealed metal package
- Enable/Disable feature
- Case ground 4-pin for minimizing RF radiation

Supply voltage
Input voltage
Output voltage
Input current
Output current
Storage temperature

Specifications

Parameter	Symbol	Min.	Typ.	Max.	Unit
Frequency range	F_0	1.25	—	70	MHz
Frequency stability	$\Delta F/F_0$	-100	—	100	ppm
		-50	—	50	ppm
		-25	—	25	ppm
Operating temperature	T_{opr}	0	25	70	°C
Operating voltage	V_{CC}	4.5	5.0	5.5	V
Operating current	I_{CC}	—	—	See Table A	mA
Input voltage	V_{IH}	3.5	—	—	V
	V_{IL}	—	—	1.5	V
Output voltage	V_{OH}	4.0	—	—	V
	V_{OL}	—	—	0.4	V
Symmetry	SYM	45	50	55	%
Rise/Fall time	t_r, t_f	—	—	See Table A	ns
Fanout	n	—	—	10	—
		—	—	5	—
Start-up time	t_{st}	—	—	4	ms
		—	—	10	ms

*1 Inclusive of calibration tolerance at 25°C, operating temperature, operating voltage range.

*2 Rise time (0 to 4.5V) of $V_{CC} > 150\mu s$

Table A

Freq.	1.25+ to 10	10+ to 26	26+ to 50	50+ to 70	MHz
I_{CC}	10	15	35	50	mA
t_r, t_f	5	5	5	5	ns

Test Circuit

See page 5 TEST-2.

Package outline

