



2N7002W

Preliminary

Power MOSFET

300mA, 60V N-CHANNEL POWER MOSFET

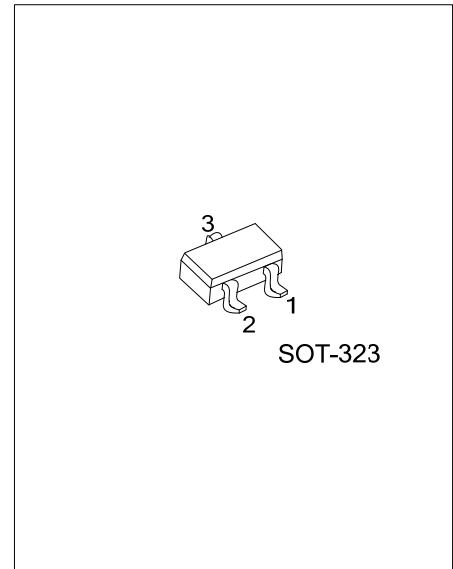
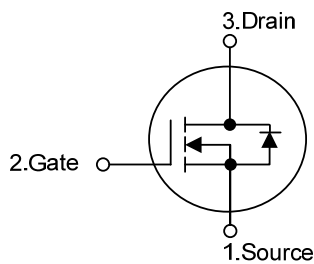
DESCRIPTION

The UTC **2N7002W** uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * High Density Cell Design for Low $R_{DS(ON)}$.
- * Voltage Controlled Small Signal Switch
- * Rugged and Reliable
- * High Saturation Current Capability

SYMBOL

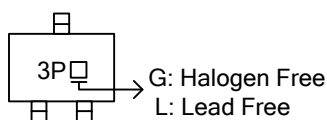


ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
2N7002WL-AL3-R	2N7002WG-AL3-R	SOT-323	S	G	D	Tape Reel

2N7002WG-AL3-R	(1)Packing Type	(1) R: Tape Reel
	(2)Package Type	(2) AL3: SOT-323
	(3)Halogen Free	(3) G: Halogen Free, L: Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise specified.)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	60	V
Drain-Gate Voltage ($R_{GS} \leq 1M\Omega$)	V_{DGR}	60	V
Gate Source Voltage	Continuous	± 20	V
	Non Repetitive($t_P < 50\mu\text{s}$)	± 40	
Drain Current	Continuous	300	mA
	Pulsed	800	
Power Dissipation	P_D	200	mW
Derated Above 25°C		1.6	mW/ $^\circ\text{C}$
Junction Temperature	T_J	+ 150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	625 (Note1)	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=10\mu\text{A}$	60			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$			1	μA
Gate-Source Leakage Current	I_{GSSF}	$V_{GS}=20V, V_{DS}=0V$			100	nA
	I_{GSSR}	$V_{GS}=-20V, V_{DS}=0V$			-100	nA
ON CHARACTERISTICS (Note2)						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D=250\mu\text{A}$	1	2.1	2.5	V
Drain-Source On-Voltage	$V_{DS(ON)}$	$V_{GS} = 10V, I_D=300\text{mA}$		0.6	3.75	V
		$V_{GS} = 5.0V, I_D=50\text{mA}$		0.09	1.5	
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=300\text{mA}, T_J=125^\circ\text{C}$			13.5	Ω
		$V_{GS}=5.0V, I_D=50\text{mA}$			7.5	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{DS}=25V, V_{GS}=0V, f=1.0\text{MHz}$		20	50	pF
Output Capacitance	C_{OSS}			11	25	pF
Reverse Transfer Capacitance	C_{RSS}			4	5	pF
Turn-On Time	t_{ON}	$V_{DD}=30V, R_L=150\Omega, I_D=200\text{mA}, V_{GS}=10V, R_{GEN}=25\Omega$			20	nS
Turn-Off Time	t_{OFF}	$V_{DD}=30V, R_L=25\Omega, I_D=200\text{mA}, V_{GS}=10V, R_{GEN}=25\Omega$			20	nS
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=300\text{mA}$ (Note)		0.88	1.5	V
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				0.8	A
Maximum Continuous Drain-Source Diode Forward Current	I_S				300	mA

Note: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. Minimum land pad size.

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2.0\%$

■ TEST CIRCUIT AND WAVEFORM

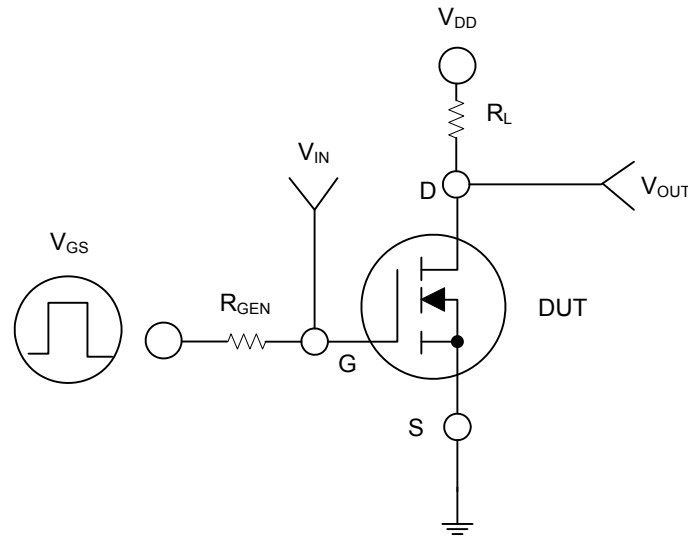


Fig. 1

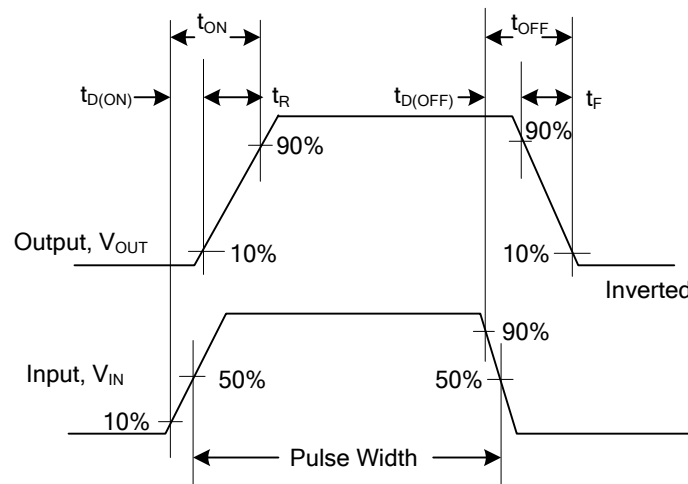


Fig. 2 Switching Waveforms

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