





20V NPN LOW SATURATION TRANSISTOR AND 40V, 1A SCHOTTKY DIODE COMBINATION DUAL

Features

- PNP Transistor
 - V_{CFO} = 20V
 - R_{SAT} = 47mΩ
 - I_C = 4.5A
- Schottky Diode
 - V_R = 40V
 - V_F= 500mv (@1A)
 - I_C = 1A
- I_C = 4.5A Continuous Collector Current
- Low Saturation Voltage (150mV @ 1A)
- hFE characterized up to 6A
- Low V_F, fast switching Schottky
- Lead, Halogen, and Antimony Free/RoHS Compliant (Note 1)
- "Green" Devices (Note 2)

Mechanical Data

- Case: DFN3020B-8
- Terminals: Pre-Plated NiPdAu leadframe
- Nominal package height: 0.8mm
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Solderable per MIL-STD-202, Method 208
- Weight: 0.013 grams (approximate)

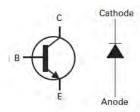
Applications

- DC DC Converters
- Charging circuits
- · Mobile phones
- Motor control

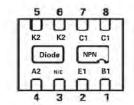
DFN3020B-8







Device symbol



Pin Configuration

Ordering Information

ĺ	Product	Status	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
	ZXTNS618MCTA	Active	DFN3020B-8	BS1	7	8	3000

Notes:

- 1. No purposefully added lead. Halogen and Antimony Free.
- 2. Diodes Inc's "Green" Policy can be found on our website https://www.diodes.com

Marking Information



BS1 = Product type Marking Code Dot Denotes Pin 1





Maximum Ratings, Transistor

Parameter	Symbol	Limit	Unit
Collector-Base Voltage	V _{CBO}	40	V
Collector-Emitter Voltage	V _{CEO}	20	V
Emitter-Base Voltage	V _{EBO}	7.5	V
Peak Pulse Current	I _{CM}	12	Α
Continuous Collector Current (Notes a and f)	Ic	4.5	Α
Continuous Collector Current (Notes b and f)	Ic	5	Α
Base Current	I _B	1	А

Thermal Characteristics, Transistor

Characteristic	Symbol	Value	Unit
Power Dissipation at T _A = 25°C (Notes a and f) Linear Derating Factor	P _D	1.5 12	W mW/°C
Power Dissipation at $T_A = 25^{\circ}$ C (Notes b and f) Linear Derating Factor	P _D	2.45 19.6	W mW/°C
Power Dissipation at T _A = 25°C (Notes c and f) Linear Derating Factor	P _D	1 8	W mW/°C
Power Dissipation at T _A = 25°C (Notes d and f) Linear Derating Factor	P _D	1.13 9	W mW/°C
Power Dissipation at T _A = 25°C (Notes d and g) Linear Derating Factor	P _D	1.7 13.6	W mW/°C
Power Dissipation at T _A = 25°C (Notes e and g) Linear Derating Factor	P _D	3 24	W mW/°C
Junction to Ambient (Notes a and f)	$R_{ hetaJA}$	83	°C/W
Junction to Ambient (Notes b and f)	$R_{ hetaJA}$	51	°C/W
Junction to Ambient (Notes c and f)	$R_{ heta JA}$	125	°C/W
Junction to Ambient (Notes d and f)	$R_{ heta JA}$	111	°C/W
Junction to Ambient (Notes d and g)	$R_{ heta JA}$	73.5	°C/W
Junction to Ambient (Notes e and g)	$R_{ heta JA}$	41.7	°C/W
Junction Temperature	TJ	150	°C
Operating and Storage Temperature Range	T _{STG}	-55 to +150	°C

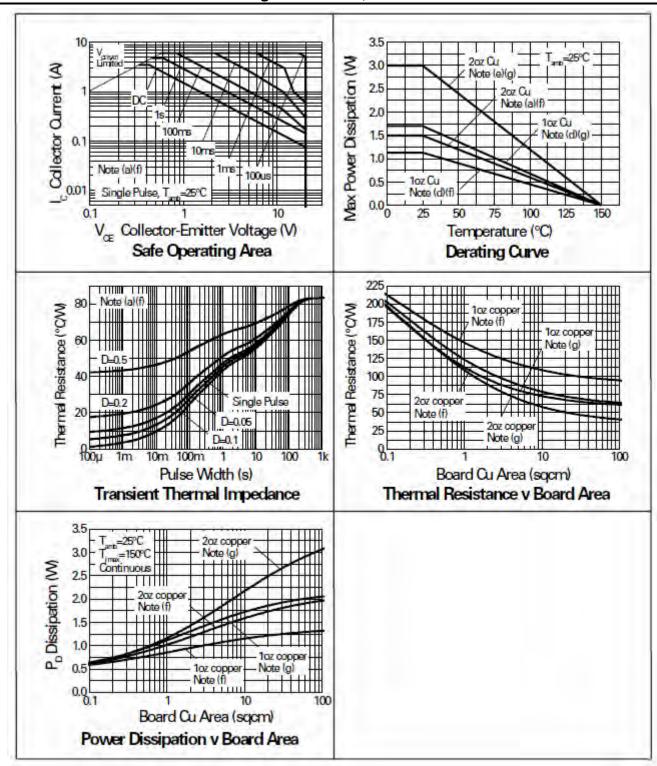
Notes:

- a. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- b. Measured at t <5 secs for a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- c. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with minimal lead connections only.
- d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- e. For a dual device surface mounted on 85 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- f. For a dual device with one active die.
- g. For dual device with 2 active die running at equal power.





Thermal Characteristics and Derating information, Transistor







Maximum Ratings, Schottky Diode

Parameter	Symbol	Limit	Unit
Continuous Reverse Voltage	V _R	40	V
Forward Voltage @ I _F = 1000mA (typ)	V _F	425	mV
Forward Current	I _F	1850	mA
Average Peak Forward Current D=50%	I _{FAV}	3	Α
Non Repetitive Forward Current t≤ 100µs	1	12	Α
t≤ 10ms	IFSM	7	Α

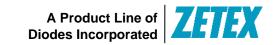
Thermal Characteristics, Schottky Diode

Characteristic	Symbol	Value	Unit
Power Dissipation at T _A = 25°C (Note a and f)	1	1.2	W
Linear Derating Factor	P _D	12	mW/°C
Power Dissipation at T _A = 25°C (Note b and f)	0	2	W
Linear Derating Factor	P _D	20	mW/°C
Power Dissipation at T _A = 25°C (Note c and f)	D-	0.8	W
Linear Derating Factor	P _D	8	mW/°C
Power Dissipation at T _A = 25°C (Note d and f)	P _D	0.9	W
Linear Derating Factor	FD	9	mW/°C
Power Dissipation at T _A = 25°C (Note d and g)	P _D	136	W
Linear Derating Factor	FD	13.6	mW/°C
Power Dissipation at T _A = 25°C (Note e and g)	P_{D}	2.4	W
Linear Derating Factor	FD	24	mW/°C
Junction to Ambient (Note a and f)	$R_{ hetaJA}$	83	°C/W
Junction to Ambient (Note b and f)	$R_{ hetaJA}$	51	°C/W
Junction to Ambient (Note c and f)	$R_{ hetaJA}$	125	°C/W
Junction to Ambient (Note d and f)	$R_{ hetaJA}$	111	°C/W
Junction to Ambient (Note d and g)	$R_{ hetaJA}$	73.5	°C/W
Junction to Ambient (Note e and g)	$R_{ hetaJA}$	41.7	°C/W
Junction Temperature	TJ	125	°C
Operating and Storage Temperature Range	T _{STG}	-55 to +150	°C

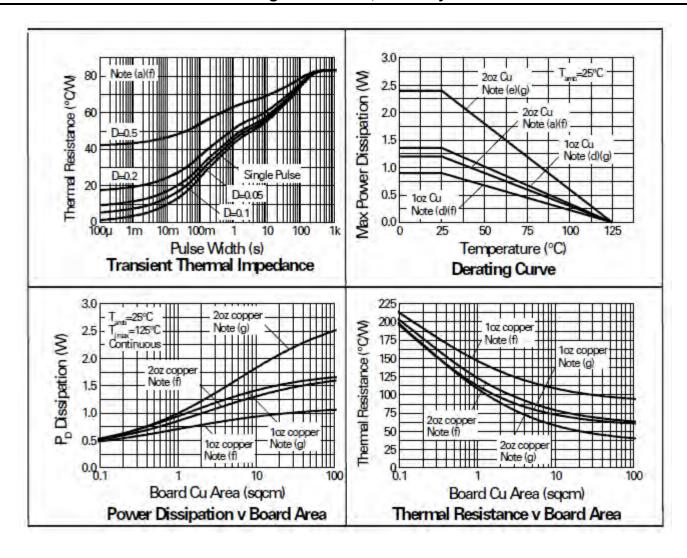
Notes:

- a. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- b. Measured at t <5 secs for a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
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- d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- e. For a dual device surface mounted on 85 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
- f. For a dual device with one active die.
- g. For dual device with 2 active die running at equal power.





Thermal Characteristics and Derating information, Schottky Diode







Electrical Characteristics, Transistor @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V _{(BR)CBO}	40	100	-	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 3)	V _{(BR)CEO}	20	27	-	V	I _C = 10mA
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	7.5	8.2	-	V	I _E = 100μA
Collector Cutoff Current	I _{CBO}	-	-	25	nA	V _{CB} = 32V
Emitter Cutoff Current	I _{EBO}	-	-	25	. nA	V _{EB} = 6V
Collector Emitter Cutoff Current	I _{CES}	-	-	25	nA	V _{CES} = 16V
Static Forward Current Transfer Ratio (Note 3)	h _{FE}	200 300 200 100	400 450 360 180	- - -	-	$\begin{split} &I_{C} = 10 \text{mA}, \ V_{CE} = 2 \text{V} \\ &I_{C} = 200 \text{mA}, \ V_{CE} = 2 \text{V} \\ &I_{C} = 2 \text{A}, \ V_{CE} = 2 \text{V} \\ &I_{C} = 6 \text{A}, \ V_{CE} = 2 \text{V} \end{split}$
Collector-Emitter Saturation Voltage (Note 3)	V _{CE(sat)}		8 90 115 190 210	15 150 135 250 270	mV	$\begin{split} I_C = & 0.1A, \ I_B = 10 mA \\ I_C = & 1A, \ I_B = 10 mA \\ I_C = & 2A, \ I_B = 50 mA \\ I_C = & 3A, \ I_B = 100 mA \\ I_C = & 4.5A, \ I_B = 125 mA \end{split}$
Base-Emitter Turn-On Voltage (Note 3)	V _{BE(on)}	-	0.88	-0.95	V	I _C = 4.5A, V _{CE} = 2V
Base-Emitter Saturation Voltage (Note 3)	V _{BE(sat)}	-	0.98	-1.05	V	I _C = 4.5A, I _B = -125mA
Output Capacitance	C _{obo}	-	23	30	pF	V _{CB} = 10V. f = 1MHz
Transition Frequency	f⊤	100	140	-	MHz	V _{CE} = 10V, I _C = 50mA, f = 100MHz
Turn-on Time	t _{on}	-	170	-	ns	V _{CC} =10V, I _C =3A
Turn-off Time	t _{off}	-	400	-	ns	$I_{B1} = I_{B2} = 10\text{mA}$

Electrical Characteristics, Schottky Diode @TA = 25°C unless otherwise specified

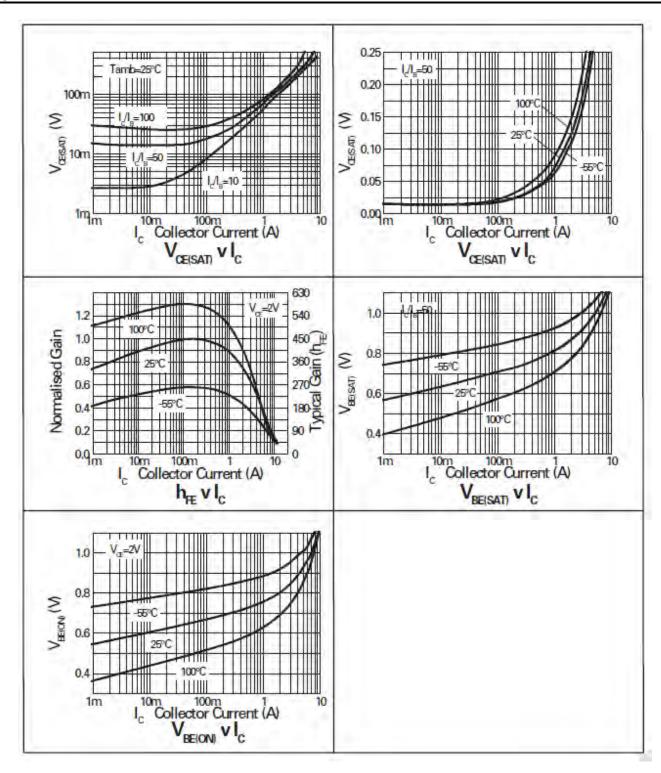
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage	V _{(BR)R}	40	60		V	$I_R = -300 \mu A$
Forward Voltage (Note 3)	V _F		240 265 305 355 390 425 495 420	270 290 340 400 450 500 600	mV	I _F = 50mA I _F = 100mA I _F = 250mA I _F = 500mA I _F = 750mA I _F = 1000mA I _F = 1500mA I _F = 1000mA, T _A = 100°C
Reverse Current	I _R	-	50	100	μΑ	$V_R = 30V$
Diode Capacitance	C_{D}	-	25	-	pF	$V_R = 25V$, $f = 1MHz$
Reverse Recovery Time	t _{rr}	-	12	-	ns	switched from $I_F = 500$ mA to $I_R = 500$ mA Measured at $I_R = 50$ mA

Notes: 3. Measured under pulsed conditions.





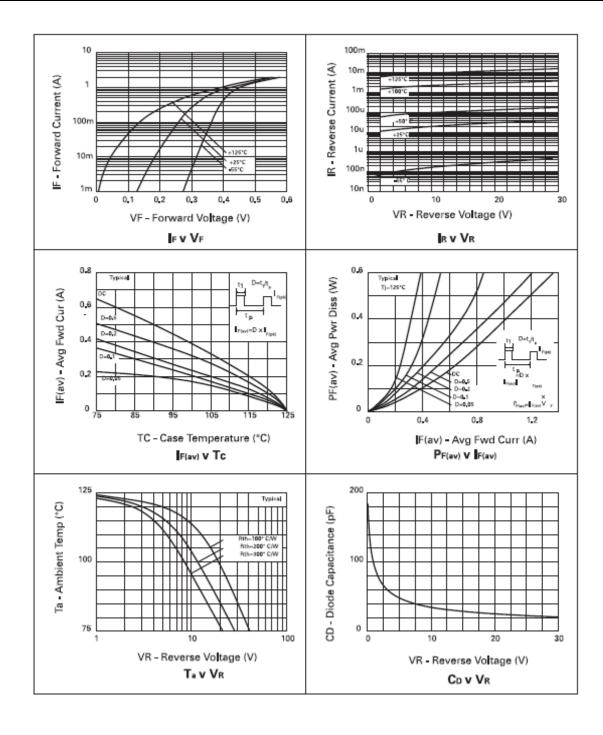
Typical Characteristics, Transistor







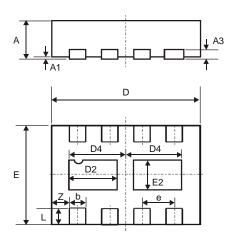
Typical Characteristics, Schottky Diode





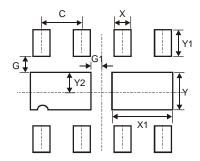


Package Outline Dimensions



DFN3020B-8						
Dim	Min	Max	Тур			
Α	0.77	0.83	0.80			
A1	0	0.05	0.02			
A3	1	-	0.15			
b	0.25	0.35	0.30			
D	2.95	3.075	3.00			
D2	0.82	1.02	0.92			
D4	1.01	1.21	1.11			
е	-	-	0.65			
Е	1.95	2.075	2.00			
E2	0.43	0.63	0.53			
L	0.25	0.35	0.30			
Z	-	-	0.375			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
С	0.650
G	0.285
G1	0.090
Х	0.400
X1	1.120
Y	0.730
Y1	0.500
٧2	0.365





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