HIGH POWERED MULTI-LINE LOW CAPACITANCE TVS ARRAY



DESCRIPTION

The SMLCxxC-2 Series are high powered multi-line low capacitance transient voltage suppressor arrays that provide board level protection for standard TTL and MOS bus line applications against the damaging effects of ESD, tertiary lightning and switching transients.

This series has a peak pulse power rating of 400 Watts for an $10/1000\mu$ s waveshape. This device series meets the IEC 61000-4-2, IEC 61000-4-4 and IEC 61000-4-5 requirements.

FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A 5/50ns
- Compatible with IEC 61000-4-5 (Surge): 100A (Applies to 6.5V Only)
- 400 Watts Peak Pulse Power per Line (tp = 10/1000µs)
- 3,900 Watts Peak Pulse Power per Line (tp = 8/20µs)
- ITKU.20 Ι_{pp} @ 40A (5/310μs) (Applies to 6.5V Only)
- Bidirectional Configuration
- High Surge Capability
- Available in Multiple Voltages
- Protects 2 Bidirectional Lines
- Low Capacitance < 30pF per Line Pair
- RoHS Compliant
- REACH Compliant

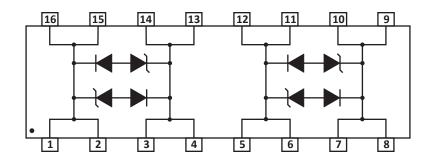
MECHANICAL CHARACTERISTICS

- Molded JEDEC SO-16 Package
- Approximate Weight: 0.15 grams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature:
- Pure-Tin Sn, 100: 260-270°C
- 16mm Tape and Reel Per EIA Standard 481
- Flammability Rating UL 94V-0

APPLICATIONS

- T1/E1
- RS-422, RS-423 & RS-485
- SDH/SONET, ATM Equipment & Systems
- Industrial Control and Monitoring
- Cable Modem Intra-Building Protection
- Customer Premise Equipment (CPE)

PIN CONFIGURATION



TYPICAL DEVICE CHARACTERISTICS

05105

MAXIMUM RATINGS @ 25°C Unless Otherwise Specified									
PARAMETER SYMBOL VALUE UNITS									
Operating Temperature	TL	-55 to 150	°C						
Storage Temperature	T _{stg}	-55 to 150	°C						
Peak Pulse Power (tp = 8/20µs) - See Figure 1	P _{PP}	3,900	Watts						
Peak Pulse Power (tp = 10/1000μs) - See Figure 1	P _{pp}	400	Watts						

ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER	RATED STAND-OFF VOLTAGE	MINIMUM BREAKDOWN VOLTAGE	MAXIMUM CLAMPING VOLTAGE (Fig. 3)	MAXIMUM LEAKAGE CURRENT	TYPICAL CAPACITANCE				
	V _{WM} VOLTS	@1mA V _(BR) VOLTS	@ 8/20μS V _c @ Ι _{թթ}	@V _{wM} Ι _D μΑ	@0V, 1MHz C pF				
SMLC6.5C-2	6.5	7.2	28.0V @ 150.0A	300	30				
SMLC12C-2	12.0	13.3	35.0V @ 140.0A	2	30				

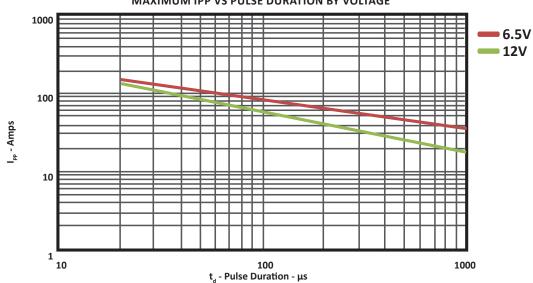
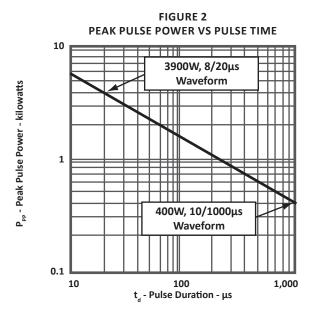
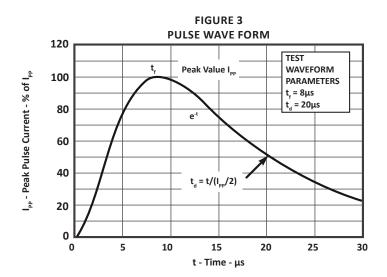
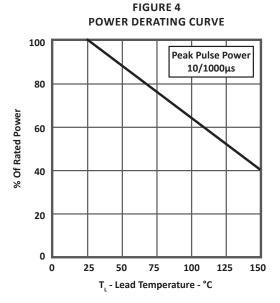


FIGURE 1 MAXIMUM IPP VS PULSE DURATION BY VOLTAGE

TYPICAL DEVICE CHARACTERISTICS





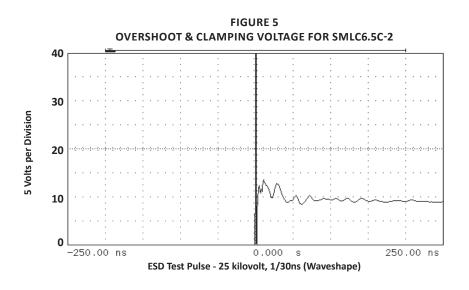


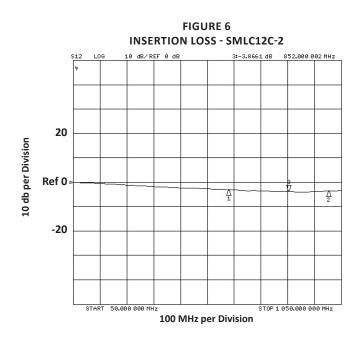
TYPICAL DEVICE CHARACTERISTICS

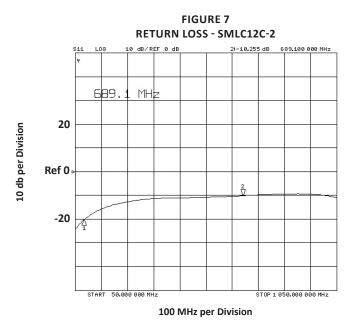
PROJEK DEV

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SPICE MODEL

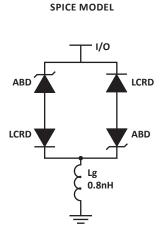


FIGURE 1

ABD - Avalanche Breakdown Diode (TVS) LCRD: Low Capacitance Rectifier Diode Lg - Lead Inductance

TABLE 1 - SPICE PARAMETERS							
PARAMETER	UNIT	ABD(TVS)	LCRD				
BV	V	See Table 2	200				
IBV	μΑ	1	0.01				
C _{jo}	pF	See Table 2	5				
I _s	А	See Table 2	1E-14				
Vj	V	0.6	0.6				
м	-	0.33	0.33				
Ν	-	1	1				
R _s	Ohms	See Table 2	0.31				
TT	S	1E-8	1E-9				
EG	eV	1.11	1.11				

TABLE 2 - ABD SPECIFIC SPICE PARAMETERS								
PART NUMBER B _v (VOLTS) C _{io} (pF) I _s (AMPS) Rs(OHMS)								
SMLC6.5-2	7.2	2600	1E-11	0.075				
SMLC12C-2	SMLC12C-2 13.3 1150 1E-13 0.080							

APPLICATION INFORMATION

PROJEK DEVICES

Only One Name Means ProTek'Tion

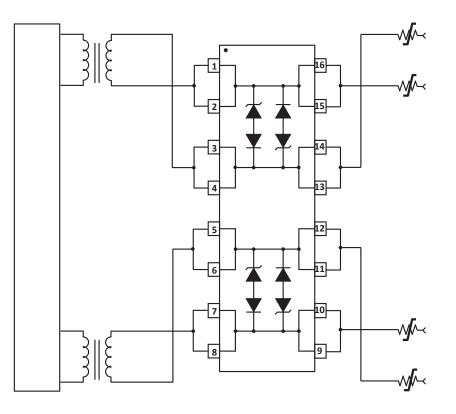


FIGURE 1 - BIDIRECTIONAL DIFFERENTIAL PROTECTION FOR T1/E1

Circuit connectivity is as follows:

- Line 1 connected to pins 1, 2, 15 and 16.
- Line 2 connected to pins 3, 4, 13 and 14.
- Line 3 connected to pins 5, 6, 11 and 12.
- Line 4 connected to pins 7, 8, 9 and 10.

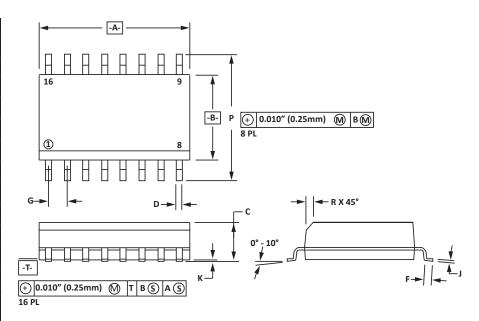
CIRCUIT BOARD RECOMMENDATIONS

Circuit board layout is critical for electromagnetic compatibility protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

SO-16 PACKAGE INFORMATION

OUTLINE DIMENSIONS								
DIM	MILLIN	1ETERS	INCHES					
DIIVI	MIN	MAX	MIN	MAX				
А	9.80	10.00	0.386	0.393				
В	3.80	4.00	0.150	0.157				
С	1.35	1.35 1.75		0.068				
D	0.35	0.49	0.014	0.019				
F	0.40 1.25		0.016	0.049				
G	1.27	BSC	0.05	BSC				
J	0.18	0.25	0.007	0.009				
к	0.10	0.25	0.004	0.008				
Р	5.80	6.20	0.229	0.244				
R	0.25	0.50	0.010	0.019				



NOTES

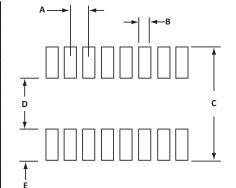
1. -T- = Seating plane and datum surface.

- 2. Dimensions "A" and "B" are datum.
- 3. Dimensions "A" and "B" do not include mold protrusion.
- 4. Maximum mold protrusion is 0.015" (0.380mm) per side.

5. Dimensioning and tolerances per ANSI Y14.5M, 1982.

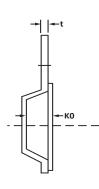
6. Dimensions are exclusive of mold flash and metal burrs.

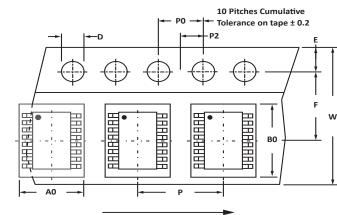
PAD LAYOUT DIMENSIONS								
	MILLIN	IETERS	INCHES					
DIM	MIN	MAX	MIN	MAX				
А	1.14	1.40	0.045	0.055				
В	0.64 0.89		0.025	0.035				
С	6.22 -		0.245	-				
D	3.94	4.17	0.155	0.165				
E 1.02 1.27 0.040 0.050								
	NOTES 1. Controlling dimension: inches.							



TAPE AND REEL

05105





User Direction of Feed

SPECIFICATIONS												
REEL DIA.	TAPE WIDTH	A0	В0	КО	D	E	F	W	PO	P2	Р	tmax
178mm (7")	16mm	6.50 ± 0.10	10.30 ± 0.10	2.10 ± 0.10	1.50 ± 0.10	1.75 ± 0.10	3.50 ± 0.05	16.00 ± 0.30	4.00 ± 0.12	2.00 ± 0.10	4.00 ± 0.10	0.25
2. Surface moun	NOTES Image: Constraint of the second seco											

4. Suffix - T13 = 13" Reel - 2,500 pieces per 16mm tape.

5. Bulk product shipped in tubes of 48 pieces per tube.

6. Marking on Part - part number, date code, logo and pin one defined by dot on top of package.

Package outline per document number 06007.R3 1/11.

ORDERING INFORMATION								
BASE PART NUMBER (xx = Voltage) LEADFREE SUFFIX TAPE SUFFIX QTY/REEL REEL SIZE TUBE QTY								
SMLCxxC-2	-LF	-T7	1,000	7″	48			
SMLCxxC-2	-LF	-T13	2,500	13"	48			

COMPANY INFORMATION

COMPANY PROFILE

ProTek Devices, based in Tempe, Arizona USA, is a manufacturer of Transient Voltage Suppression (TVS) products designed specifically for the protection of electronic systems from the effects of lightning, Electrostatic Discharge (ESD), Nuclear Electromagnetic Pulse (NEMP), inductive switching and EMI/RFI. With over 25 years of engineering and manufacturing experience, ProTek designs TVS devices that provide application specific protection solutions for all electronic equipment/systems.

ProTek Devices Analog Products Division, also manufactures analog interface, control, RF and power management products.

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