



# STD7NB20 STD7NB20-1

N-CHANNEL 200V - 0.3Ω - 7A DPAK/IPAK  
PowerMESH™ MOSFET

| TYPE       | V <sub>DSS</sub> | R <sub>DS(on)</sub> | I <sub>D</sub> |
|------------|------------------|---------------------|----------------|
| STD7NB20   | 200 V            | < 0.40 Ω            | 7 A            |
| STD7NB20-1 | 200 V            | < 0.40 Ω            | 7 A            |

- TYPICAL R<sub>DS(on)</sub> = 0.3 Ω
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- VERY LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED
- ADD SUFFIX "T4" FOR ORDERING IN TAPE & REEL

## DESCRIPTION

Using the latest high voltage MESH OVERLAY™ process, STMicroelectronics has designed an advanced family of power MOSFETs with outstanding performances. The new patent pending strip layout coupled with the Company's proprietary edge termination structure, gives the lowest R<sub>DS(on)</sub> per area, exceptional avalanche and dv/dt capabilities and unrivalled gate charge and switching characteristics.

## APPLICATIONS

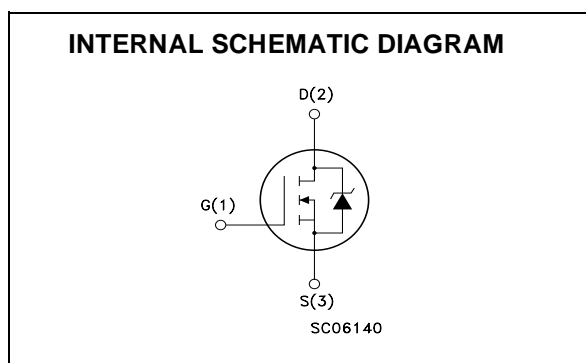
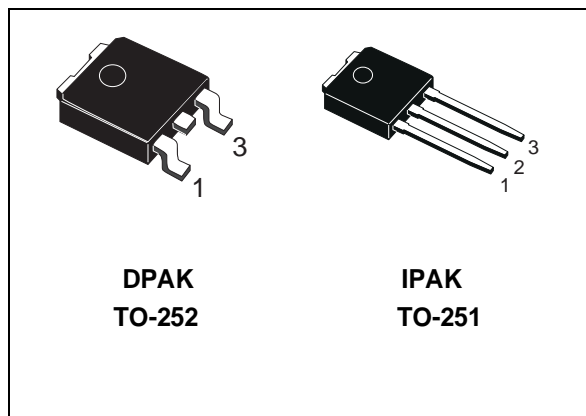
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-DC CONVERTERS FOR TELECOM, INDUSTRIAL, AND LIGHTING EQUIPMENT

## ABSOLUTE MAXIMUM RATINGS

| Symbol              | Parameter  | Value       | Unit |
|---------------------|--|-------------|------|
| V <sub>DS</sub>     | Drain-source Voltage (V <sub>GS</sub> = 0)           | 200         | V    |
| V <sub>DGR</sub>    | Drain-gate Voltage (R <sub>GS</sub> = 20 kΩ)         | 200         | V    |
| V <sub>GS</sub>     | Gate- source Voltage                                 | ± 30        | V    |
| I <sub>D</sub>      | Drain Current (continuous) at T <sub>C</sub> = 25°C  | 7           | A    |
| I <sub>D</sub>      | Drain Current (continuous) at T <sub>C</sub> = 100°C | 5           | A    |
| I <sub>DM</sub> (*) | Drain Current (pulsed)                               | 28          | A    |
| P <sub>TOT</sub>    | Total Dissipation at T <sub>C</sub> = 25°C           | 55          | W    |
|                     | Derating Factor                                      | 0.44        | W/°C |
| dv/dt (1)           | Peak Diode Recovery voltage slope                    | 5.5         | V/ns |
| T <sub>stg</sub>    | Storage Temperature                                  | - 65 to 150 | °C   |
| T <sub>j</sub>      | Max. Operating Junction Temperature                  | 150         | °C   |

(\*) Pulse width limited by safe operating area

(1) I<sub>SD</sub> ≤ 7A, di/dt ≤ 200 A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>j</sub> ≤ T<sub>jMAX</sub>



## STD7NB20 / STD7NB20-1

### THERMAL DATA

|                |  |      |      |
|----------------|--|------|------|
| Rthj-case      | Thermal Resistance Junction-case Max           | 2.27 | °C/W |
| Rthj-amb       | Thermal Resistance Junction-ambient Max        | 100  | °C/W |
| T <sub>I</sub> | Maximum Lead Temperature For Soldering Purpose | 275  | °C   |

### AVALANCHE CHARACTERISTICS

| Symbol          | Parameter  | Max Value | Unit |
|-----------------|--|-----------|------|
| I <sub>AR</sub> | Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T <sub>j</sub> max)                                | 7         | A    |
| E <sub>AS</sub> | Single Pulse Avalanche Energy (starting T <sub>j</sub> = 25 °C, I <sub>D</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 50 V) | 100       | mJ   |

### ELECTRICAL CHARACTERISTICS (TCASE = 25 °C UNLESS OTHERWISE SPECIFIED)

OFF

| Symbol               | Parameter   | Test Conditions   | Min. | Typ. | Max.    | Unit     |
|----------------------|---|---|------|------|---------|----------|
| V <sub>(BR)DSS</sub> | Drain-source Breakdown Voltage                        | I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0  | 200  |      |         | V        |
| I <sub>DSS</sub>     | Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0) | V <sub>DS</sub> = Max Rating<br>V <sub>DS</sub> = Max Rating, T <sub>C</sub> = 125 °C |      |      | 1<br>10 | μA<br>μA |
| I <sub>GSS</sub>     | Gate-body Leakage Current (V <sub>DS</sub> = 0)       | V <sub>GS</sub> = ± 30V   |      |      | ±100    | nA       |

ON (1)

| Symbol              | Parameter                         | Test Conditions  | Min. | Typ. | Max. | Unit |
|---------------------|-----------------------------------|--|------|------|------|------|
| V <sub>GS(th)</sub> | Gate Threshold Voltage            | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA | 3    | 4    | 5    | V    |
| R <sub>DS(on)</sub> | Static Drain-source On Resistance | V <sub>GS</sub> = 10V, I <sub>D</sub> = 3.5 A              |      | 0.30 | 0.40 | Ω    |

DYNAMIC

| Symbol              | Parameter                    | Test Conditions   | Min. | Typ. | Max. | Unit |
|---------------------|------------------------------|---|------|------|------|------|
| g <sub>fs</sub> (1) | Forward Transconductance     | V <sub>DS</sub> > I <sub>D(on)</sub> × R <sub>DS(on)max</sub> ,<br>I <sub>D</sub> = 3.5 A | 2    | 3    |      | S    |
| C <sub>iss</sub>    | Input Capacitance            | V <sub>DS</sub> = 25V, f = 1 MHz, V <sub>GS</sub> = 0                                     |      | 470  | 650  | pF   |
| C <sub>OSS</sub>    | Output Capacitance           |   |      | 135  | 190  | pF   |
| C <sub>rSS</sub>    | Reverse Transfer Capacitance |   |      | 22   | 30   | pF   |

**ELECTRICAL CHARACTERISTICS (CONTINUED)**

**SWITCHING ON**

| Symbol      | Parameter          | Test Conditions  | Min. | Typ. | Max. | Unit |
|-------------|--------------------|--|------|------|------|------|
| $t_{d(on)}$ | Turn-on Delay Time | $V_{DD} = 100\text{ V}, I_D = 5\text{ A}$<br>$R_G = 4.7\Omega, V_{GS} = 10\text{ V}$ |      | 10   | 14   | ns   |
| $t_r$       | Rise Time          | (see test circuit, Figure 3)   |      | 15   | 20   | ns   |
| $Q_g$       | Total Gate Charge  | $V_{DD} = 160\text{ V}, I_D = 10\text{ A},$<br>$V_{GS} = 10\text{ V}$                |      | 17   | 24   | nC   |
| $Q_{gs}$    | Gate-Source Charge |  |      | 7.5  |      | nC   |
| $Q_{gd}$    | Gate-Drain Charge  |  |      | 5.5  |      | nC   |

**SWITCHING OFF**

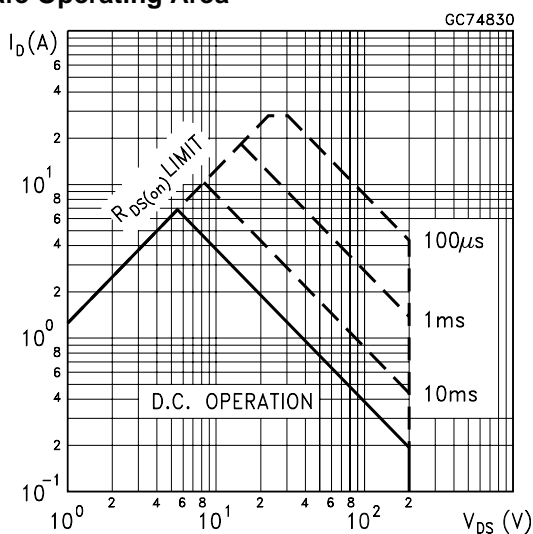
| Symbol        | Parameter             | Test Conditions  | Min. | Typ. | Max. | Unit |
|---------------|-----------------------|--|------|------|------|------|
| $t_{r(Voff)}$ | Off-Voltage Rise Time | $V_{DD} = 160\text{ V}, I_D = 10\text{ A},$<br>$R_G = 4.7\Omega, V_{GS} = 10\text{ V}$ |      | 8    | 11   | ns   |
| $t_f$         | Fall Time             | (see test circuit, Figure 3)   |      | 10   | 14   | ns   |
| $t_c$         | Cross-over Time       |  |      | 20   | 28   | ns   |

**SOURCE DRAIN DIODE**

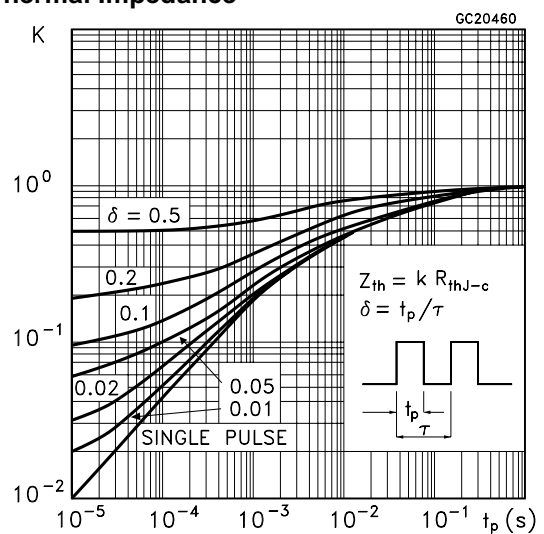
| Symbol       | Parameter                     | Test Conditions   | Min. | Typ. | Max. | Unit |
|--------------|-------------------------------|---|------|------|------|------|
| $I_{SD}$     | Source-drain Current          |   |      |      | 7    | A    |
| $I_{SDM(2)}$ | Source-drain Current (pulsed) |   |      |      | 28   | A    |
| $V_{SD(1)}$  | Forward On Voltage            | $I_{SD} = 7\text{ A}, V_{GS} = 0$   |      |      | 1.5  | V    |
| $t_{rr}$     | Reverse Recovery Time         | $I_{SD} = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$<br>$V_{DD} = 50\text{ V}, T_J = 150^\circ\text{C}$ |      | 170  |      | ns   |
| $Q_{rr}$     | Reverse Recovery Charge       | (see test circuit, Figure 5)  |      | 980  |      | nC   |
| $I_{RRM}$    | Reverse Recovery Current      |   |      | 11.5 |      | A    |

Note: 1. Pulsed: Pulse duration = 300  $\mu\text{s}$ , duty cycle 1.5 %.  
2. Pulse width limited by safe operating area.

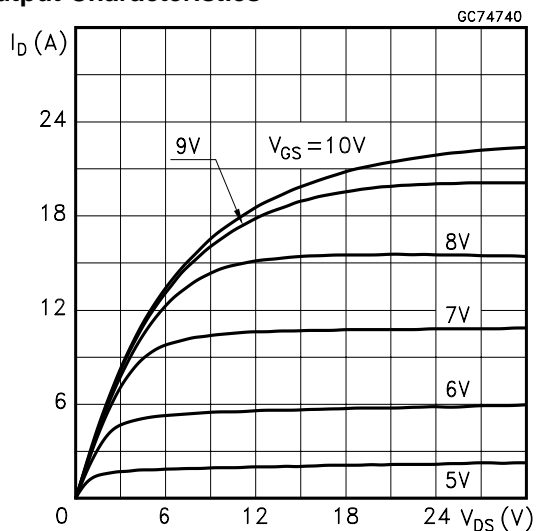
**Safe Operating Area**



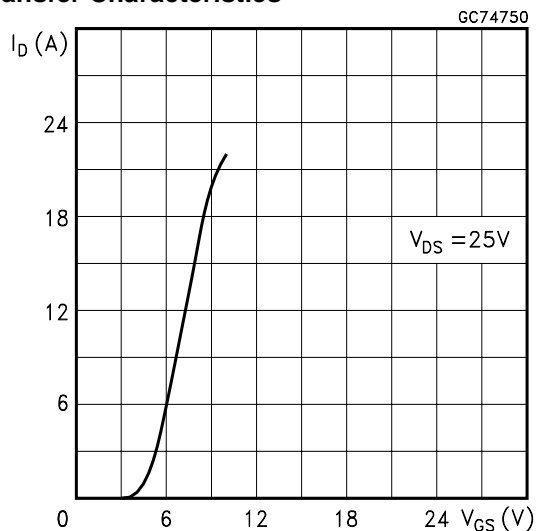
**Thermal Impedance**



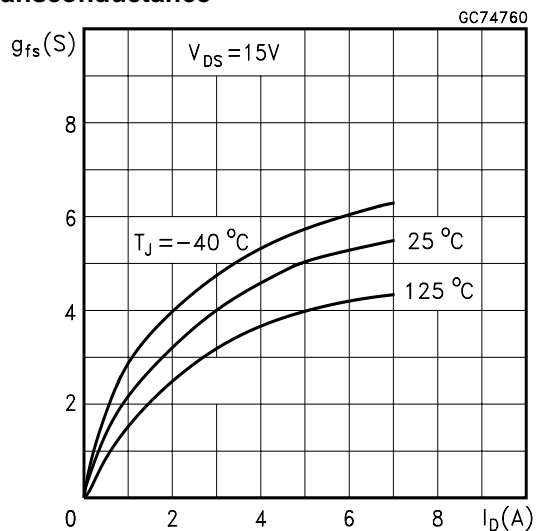
Output Characteristics



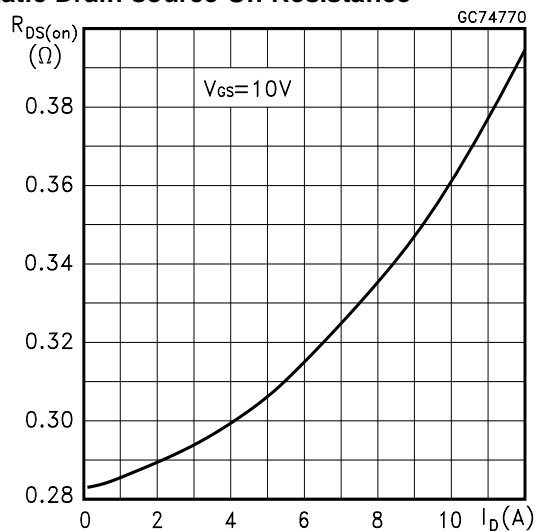
Transfer Characteristics



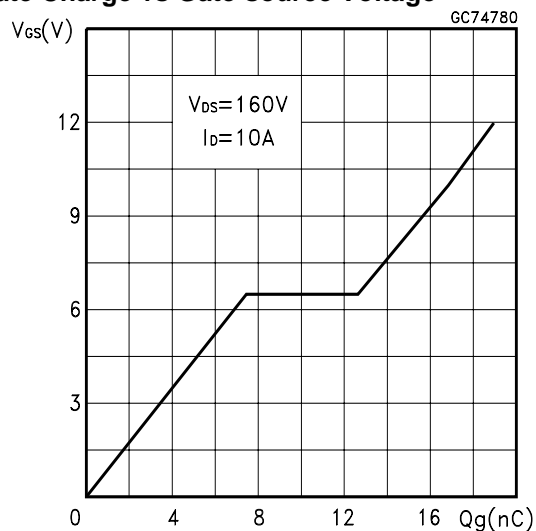
Transconductance



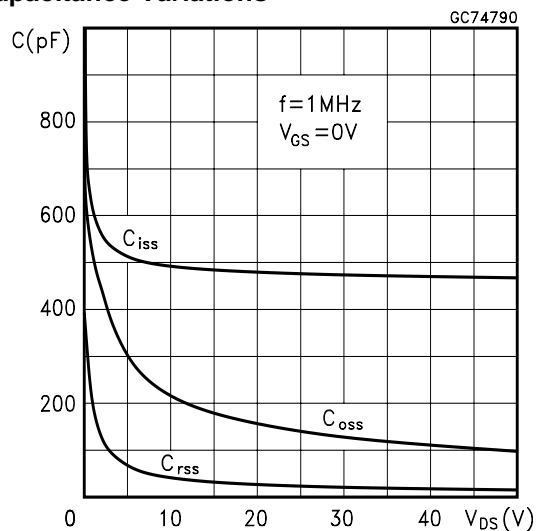
Static Drain-source On Resistance



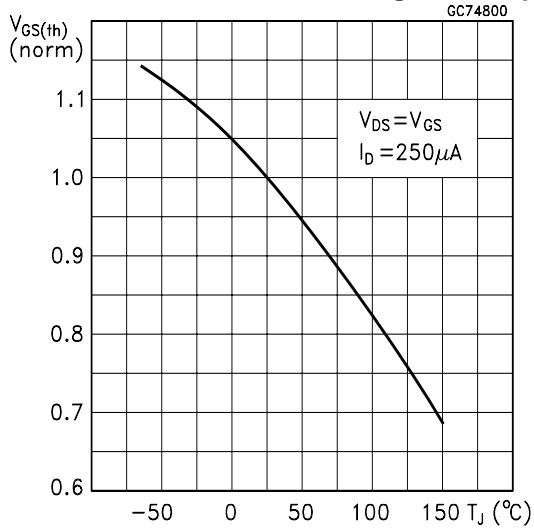
Gate Charge vs Gate-source Voltage



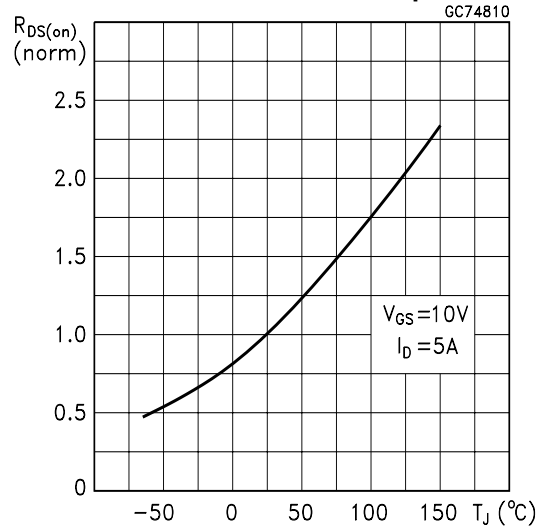
Capacitance Variations



Normalized Gate Threshold Voltage vs Temp.



Normalized On Resistance vs Temperature



Source-drain Diode Forward Characteristics

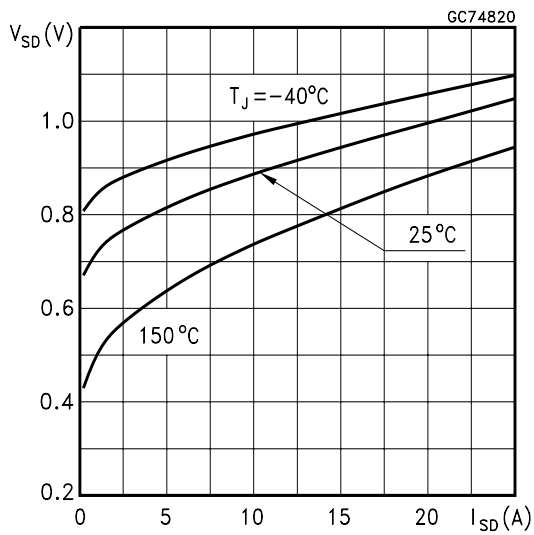


Fig. 1: Unclamped Inductive Load Test Circuit

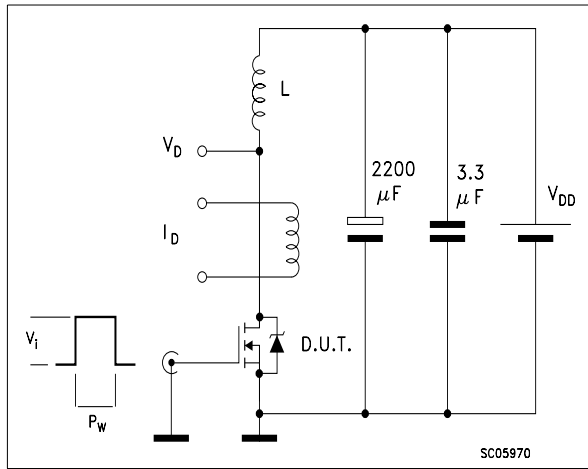


Fig. 2: Unclamped Inductive Waveform

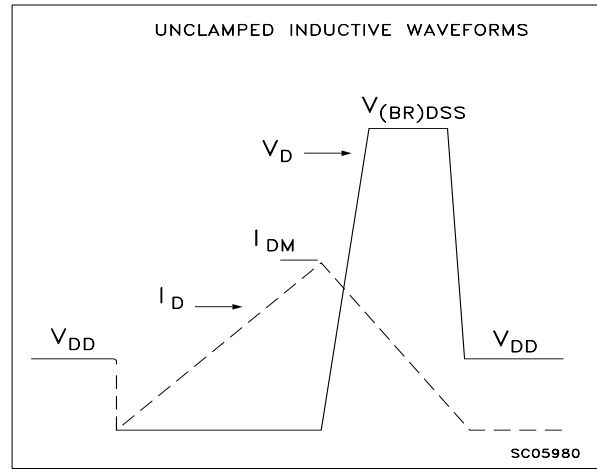


Fig. 3: Switching Times Test Circuit For Resistive Load

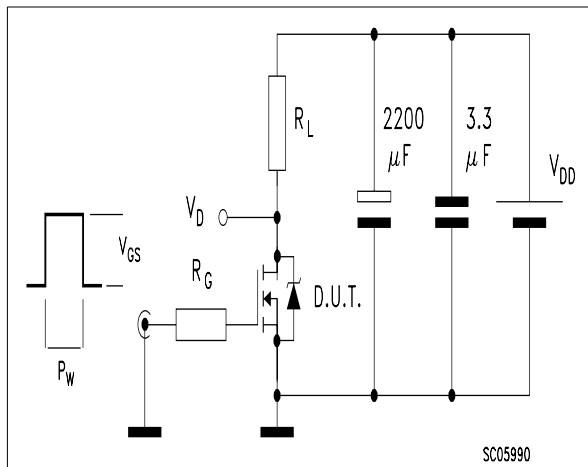


Fig. 4: Gate Charge test Circuit

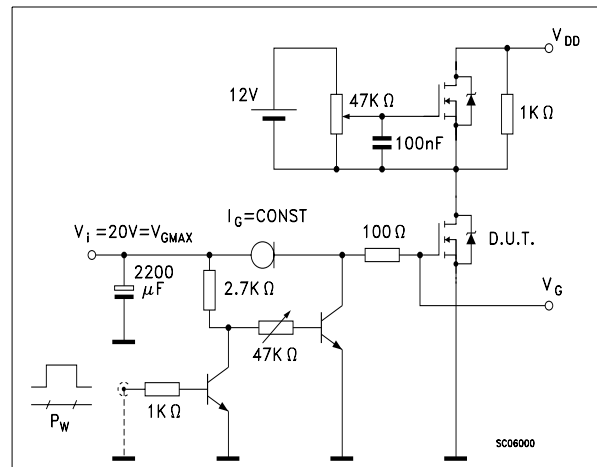
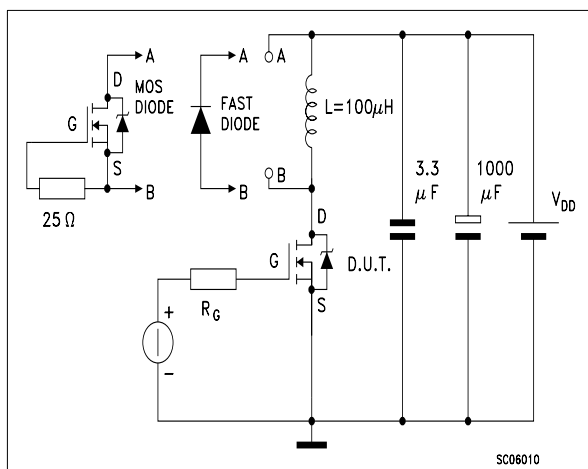
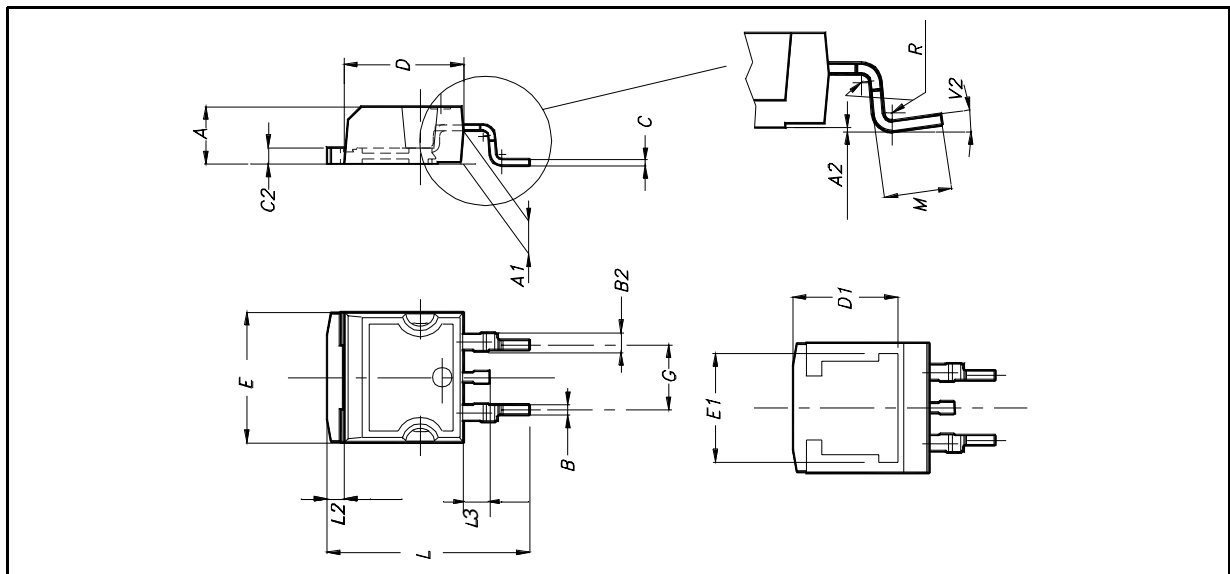


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times



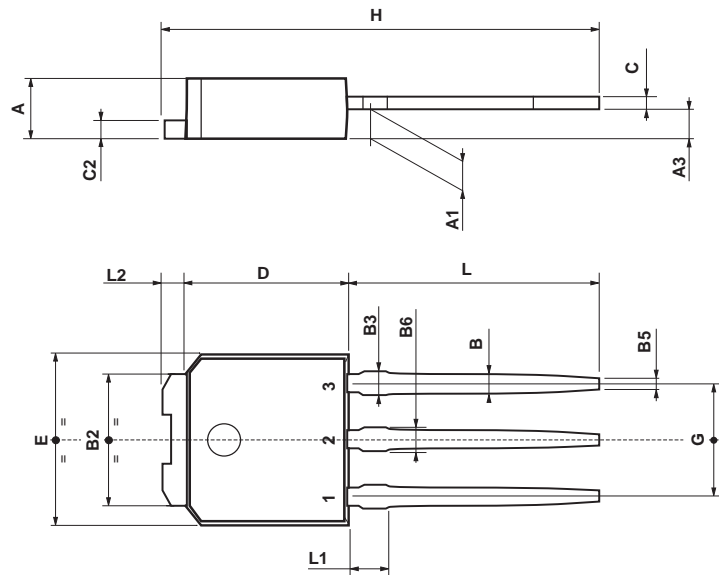
**D<sup>2</sup>PAK MECHANICAL DATA**

| DIM. | mm.  |     |       | inch  |       |       |
|------|------|-----|-------|-------|-------|-------|
|      | MIN. | TYP | MAX.  | MIN.  | TYP.  | MAX.  |
| A    | 4.4  |     | 4.6   | 0.173 |       | 0.181 |
| A1   | 2.49 |     | 2.69  | 0.098 |       | 0.106 |
| A2   | 0.03 |     | 0.23  | 0.001 |       | 0.009 |
| B    | 0.7  |     | 0.93  | 0.027 |       | 0.036 |
| B2   | 1.14 |     | 1.7   | 0.044 |       | 0.067 |
| C    | 0.45 |     | 0.6   | 0.017 |       | 0.023 |
| C2   | 1.23 |     | 1.36  | 0.048 |       | 0.053 |
| D    | 8.95 |     | 9.35  | 0.352 |       | 0.368 |
| D1   |      | 8   |       |       | 0.315 |       |
| E    | 10   |     | 10.4  | 0.393 |       |       |
| E1   |      | 8.5 |       |       | 0.334 |       |
| G    | 4.88 |     | 5.28  | 0.192 |       | 0.208 |
| L    | 15   |     | 15.85 | 0.590 |       | 0.625 |
| L2   | 1.27 |     | 1.4   | 0.050 |       | 0.055 |
| L3   | 1.4  |     | 1.75  | 0.055 |       | 0.068 |
| M    | 2.4  |     | 3.2   | 0.094 |       | 0.126 |
| R    |      | 0.4 |       |       | 0.015 |       |
| V2   | 0°   |     | 8°    |       |       |       |



TO-251 (IPAK) MECHANICAL DATA

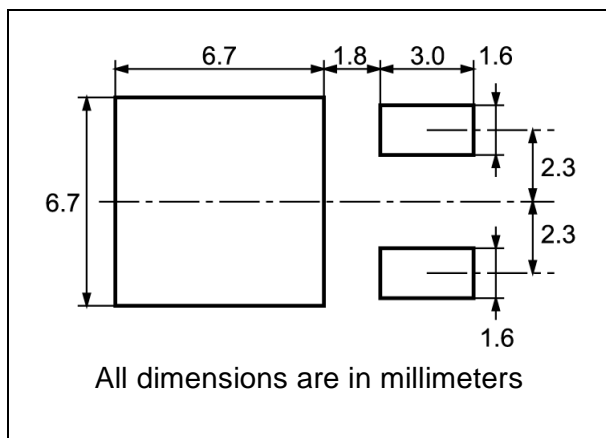
| DIM. | mm   |      |      | inch  |       |       |
|------|------|------|------|-------|-------|-------|
|      | MIN. | TYP. | MAX. | MIN.  | TYP.  | MAX.  |
| A    | 2.2  |      | 2.4  | 0.086 |       | 0.094 |
| A1   | 0.9  |      | 1.1  | 0.035 |       | 0.043 |
| A3   | 0.7  |      | 1.3  | 0.027 |       | 0.051 |
| B    | 0.64 |      | 0.9  | 0.025 |       | 0.031 |
| B2   | 5.2  |      | 5.4  | 0.204 |       | 0.212 |
| B3   |      |      | 0.85 |       |       | 0.033 |
| B5   |      | 0.3  |      |       | 0.012 |       |
| B6   |      |      | 0.95 |       |       | 0.037 |
| C    | 0.45 |      | 0.6  | 0.017 |       | 0.023 |
| C2   | 0.48 |      | 0.6  | 0.019 |       | 0.023 |
| D    | 6    |      | 6.2  | 0.236 |       | 0.244 |
| E    | 6.4  |      | 6.6  | 0.252 |       | 0.260 |
| G    | 4.4  |      | 4.6  | 0.173 |       | 0.181 |
| H    | 15.9 |      | 16.3 | 0.626 |       | 0.641 |
| L    | 9    |      | 9.4  | 0.354 |       | 0.370 |
| L1   | 0.8  |      | 1.2  | 0.031 |       | 0.047 |
| L2   |      | 0.8  | 1    |       | 0.031 | 0.039 |



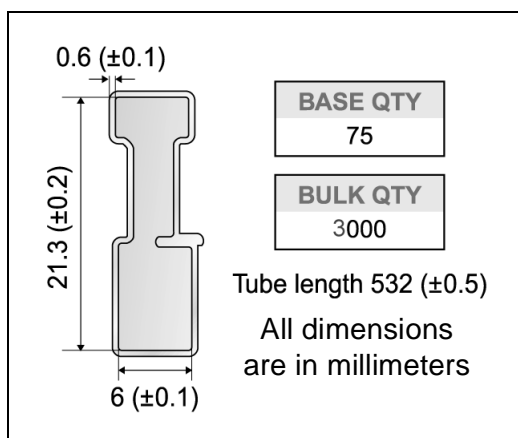
0068771-E



**DPAK FOOTPRINT**



**TUBE SHIPMENT (no suffix)\***



**TAPE AND REEL SHIPMENT (suffix "T4")\***

40 mm min. Access hole at slot location

Full radius

Tape slot in core for tape start 2.5mm min. width

**REEL MECHANICAL DATA**

| DIM. | mm   |      | inch  |        |
|------|------|------|-------|--------|
|      | MIN. | MAX. | MIN.  | MAX.   |
| A    |      | 330  |       | 12.992 |
| B    | 1.5  |      | 0.059 |        |
| C    | 12.8 | 13.2 | 0.504 | 0.520  |
| D    | 20.2 |      | 0.795 |        |
| G    | 16.4 | 18.4 | 0.645 | 0.724  |
| N    | 50   |      | 1.968 |        |
| T    |      | 22.4 |       | 0.881  |

| BASE QTY | BULK QTY |
|----------|----------|
| 2500     | 2500     |

**TAPE MECHANICAL DATA**

| DIM. | mm   |      | inch  |       |
|------|------|------|-------|-------|
|      | MIN. | MAX. | MIN.  | MAX.  |
| A0   | 6.8  | 7    | 0.267 | 0.275 |
| B0   | 10.4 | 10.6 | 0.409 | 0.417 |
| B1   |      | 12.1 |       | 0.476 |
| D    | 1.5  | 1.6  | 0.059 | 0.063 |
| D1   | 1.5  |      | 0.059 |       |
| E    | 1.65 | 1.85 | 0.065 | 0.073 |
| F    | 7.4  | 7.6  | 0.291 | 0.299 |
| K0   | 2.55 | 2.75 | 0.100 | 0.108 |
| P0   | 3.9  | 4.1  | 0.153 | 0.161 |
| P1   | 7.9  | 8.1  | 0.311 | 0.319 |
| P2   | 1.9  | 2.1  | 0.075 | 0.082 |
| R    | 40   |      | 1.574 |       |
| W    | 15.7 | 16.3 | 0.618 | 0.641 |

For machine ref. only including draft and radii concentric around B0

10 pitches cumulative tolerance on tape +/- 0.2 mm

User Direction of Feed

FEED DIRECTION

Bending radius R min.

\* on sales type

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