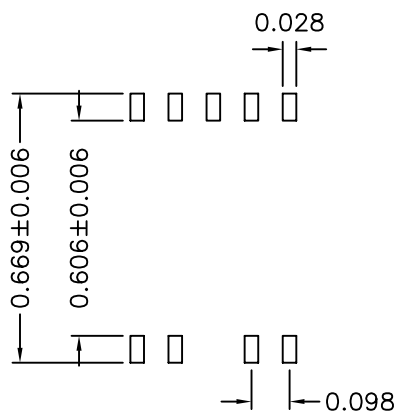
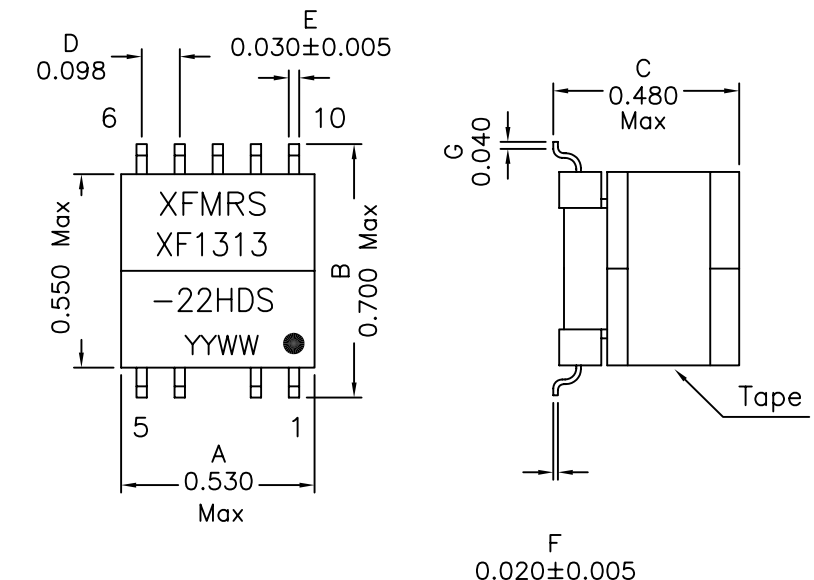


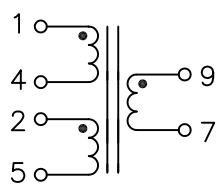
1. Dimensions:



Suggested PCB Layout

*Pin #3 to be removed.

2.Schematic:



3.Electrical Specifications: @25°C

- PIN1-5 OCL: 2.07mH ±4% @10KHz 0.1V, 0ADC, Ls (Tie PIN2-4)
- PIN1-5 OCL: 2.07mH ±4% @10KHz 0.1V, 100mADC, Ls (Tie PIN2-4)
- PIN1-5 LL: 15uH Max @100KHz 100mV, Ls (Tie PIN2-4, PIN7-9)
- URNS RATIO: (1-5):(9-7) Tie Pin2-4 = 4:1 ±1% @10KHz, 0.1VAC
- DC Res.: 1.45 Ohms ±10% (Line)
- DC Res.: 0.450 Ohm ±10% (IC)
- CW/W: Pins 1-9 65pF Max @100KHz, 0.1VAC, Cs, Tie (1+5, 2+4, 9+7)
- THD: 75dB Min @5KHz, 2.0Vrms (5.66 Vp-p), Line : IC
- RETURN LOSS: 15.0dB Min. @40KHz-500KHz
- INSERTION LOSS: 0.5dB Max @100KHz
- LONGITUDINAL BALANCE: 55dB Min. @40KHz-500KHz, GND pin 9.
- FREQ RESP: ±1.00dB, @40KHz-1MHz, 100KHz Reference
- ISOLATION VOLTAGE: 1875Vrms Pin1-9 tie Pin2-4
- Designed to reflect 135 Ohms on the Line side with 8.4 Ohms on the IC side.

Notes:

- 1. Solderability: Leads shall meet MIL-STD-202, Method 208D for solderability.
- 2. Flammability: UL94V-0
- 3. ASTM oxygen index: > 28%
- 4. Temperature rating: 155°C. UL file E151556
- 5. Operating Temperature Range: -40°C to +85°C

XFMRS Inc		Title: TRANSFORMER		
UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010 Dimensions in INCH	P/N: XF1313-22HDS		REV. 00	
	DWN.	李小锋	Jun-26-02	
	CHK.	廖玉坤	Jun-26-02	
SHEET 1 OF 1		APP.	Isaiah M	Jun-26-02