

# Schottky Barrier Diode

# RBQ20NS65A

#### Applications

General rectification

#### Features

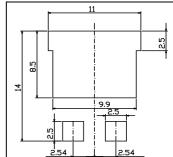
1)Cathode Common Dual type.(LPDS) 2)Low  $I_{R.}$ 

#### Construction

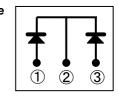
Silicon epitaxial planer

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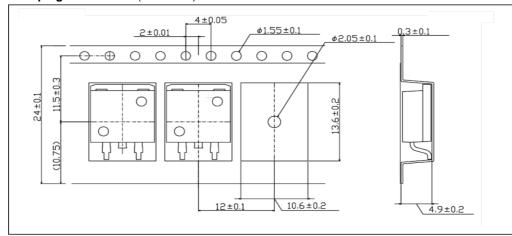
# ●Land size figure (Unit : mm)



●Structure



### ● Taping dimensions (Unit: mm)



# ●Absolute maximum ratings (Tc=25°C)

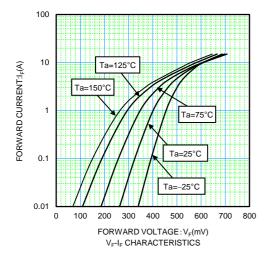
Parameter	Symbol	Symbol Limits	
Reverse voltage (repetitive)	$V_{RM}$	65	V
Reverse voltage (DC)	VR	65	V
Average rectified forward current (*1)	lo	20	Α
Forward current surge peak (60Hz • 1cyc) (*2)	I <sub>FSM</sub>	100	Α
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-40 to +150	°C

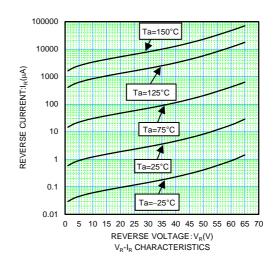
<sup>(\*1) 60</sup>Hz half sin wave, 1/2 lo per diode.

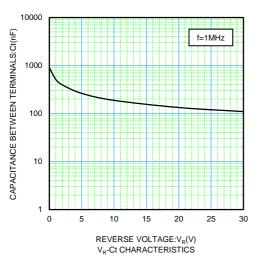
## ●Electrical characteristics (Tj=25°C)

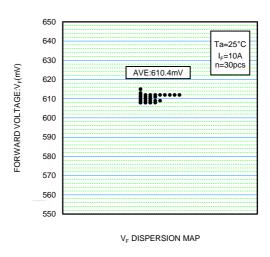
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	$V_{F}$	-	-	0.69	V	I <sub>F</sub> =10A
Reverse current	I <sub>R</sub>	-	-	0.3	mA	V <sub>R</sub> =65V

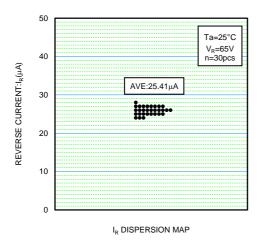
<sup>(\*2) 60</sup>Hz half sin wave, one cycle, non-repetitive at Tj=25°C.

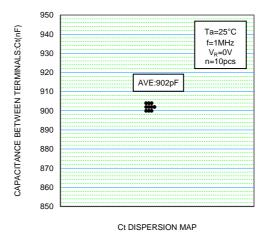


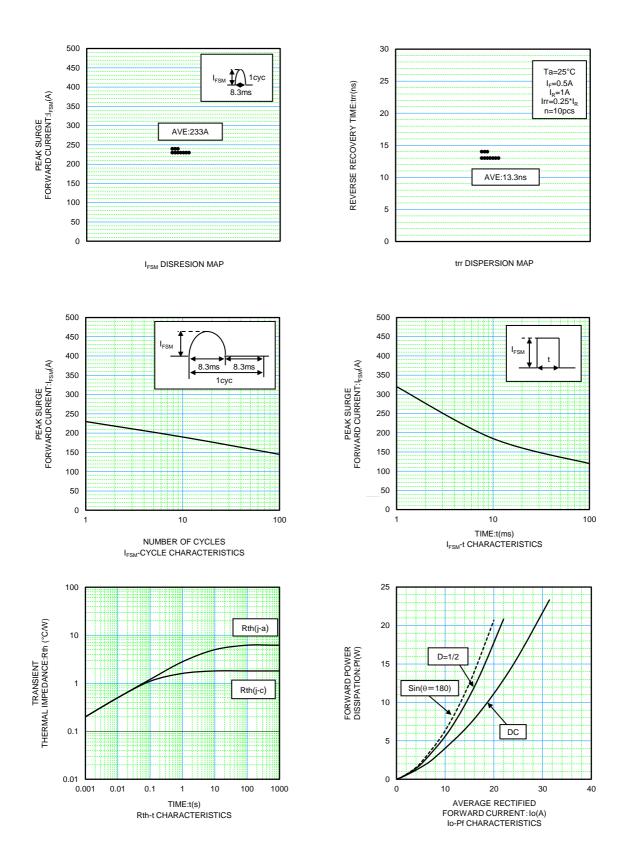


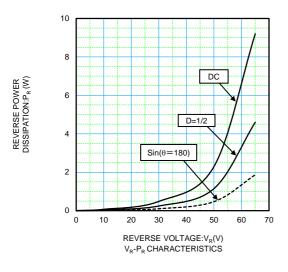


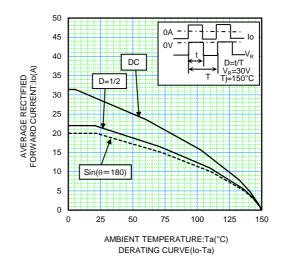


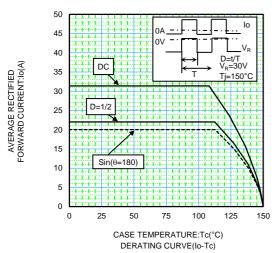


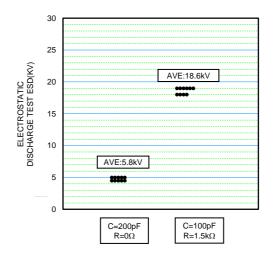












ESD DISPERSION MAP

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