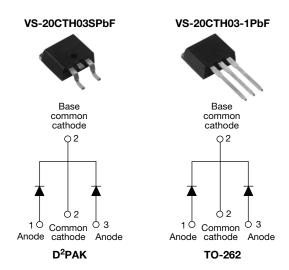


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HALOGEN

FREE

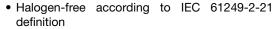
### Hyperfast Rectifier, 2 x 10 A FRED Pt®

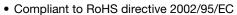


PRODUCT SUMMARY				
t <sub>rr</sub> (maximum)	35 ns			
I <sub>F(AV)</sub>	2 x 10 A			
$V_{R}$	300 V			

### **FEATURES**

- Hyperfast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C





• AEC-Q101 qualified



Vishay HPP's 300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage		$V_{RRM}$		300	V		
Average rectified forward current	per diode	I	T <sub>C</sub> = 160 °C	10			
Average rectified forward current	per device	I <sub>F(AV)</sub>		20	Α		
Non-repetitive peak surge current		I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120			
Operating junction and storage temp	peratures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C		

ELECTRICAL SPEC	<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	300	-	-			
Forward voltage	VF	I <sub>F</sub> = 10 A	-	1.05	1.25	V		
Torward voitage	۷F	I <sub>F</sub> = 10 A, T <sub>J</sub> = 125 °C	-	0.85	0.95			
Poverse leekage ourrent	_	$V_R = V_R$ rated	-	-	20			
Reverse leakage current	I <sub>R</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	=	6	200	μΑ		
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 300 V	-	30	-	pF		
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH		

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A, } dI_F/dt =$	$50 \text{ A/}\mu\text{s}, \text{ V}_{\text{R}} = 30 \text{ V}$	ı	-	35	
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A/}\mu\text{s}, \text{ V}_R = 30 \text{ V}$		ı	-	30	no
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	31	-	ns
		T <sub>J</sub> = 125 °C		-	42	-	
Peak recovery current	1	T <sub>J</sub> = 25 °C	$I_F = 10 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	2.4	-	A
reak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	5.6	-	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	36	=	nC
neverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	120	-	TIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per diode	R <sub>thJC</sub>		-	-	1.5	°C/W
Mainlet			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Moulting device		Case style D <sup>2</sup> PAK		20CT	H03S	
Marking device		Case style TO-262		20CT	H03-1	

www.vishay.com

For technical questions, contact: diodestech@vishay.com

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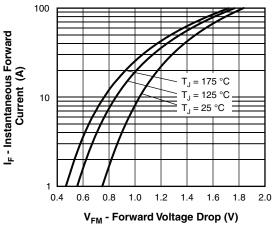


Fig. 1 - Maximum Forward Voltage Drop Characteristics

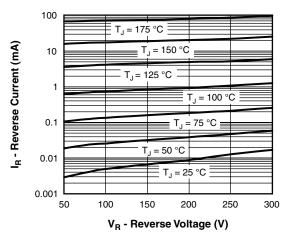


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

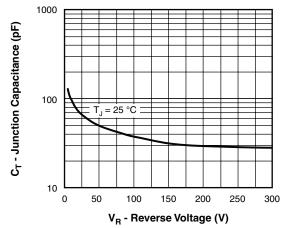


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

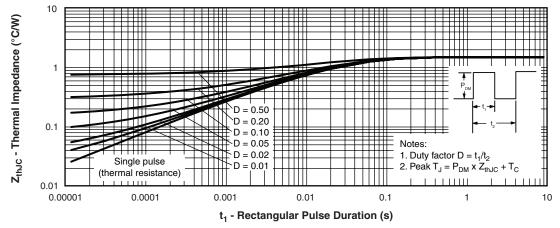


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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t<sub>rr</sub> (ns)

100

10

100



1000

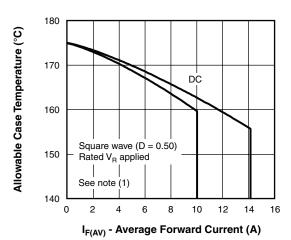
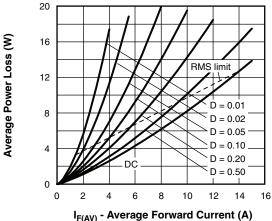


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



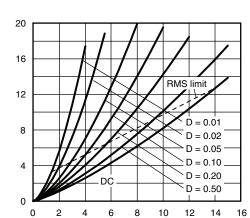
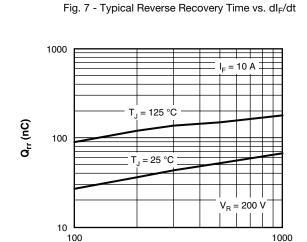


Fig. 6 - Forward Power Loss Characteristics



dl<sub>F</sub>/dt (A/µs)

T<sub>J</sub> = 25 °C

V<sub>R</sub> = 200 V

T<sub>J</sub> = 125 °C

dl<sub>E</sub>/dt (A/µs) Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 

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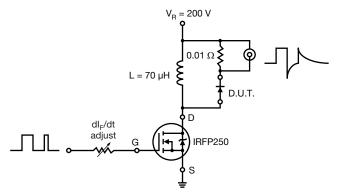
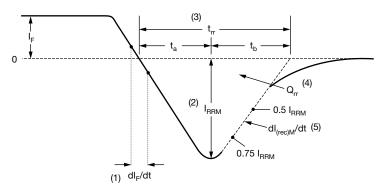


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm l_F$  to point where a line passing through 0.75  $\rm l_{RRM}$  and 0.50  $\rm l_{RRM}$  extrapolated to zero current.
- (4)  $\rm Q_{rr}$  area under curve defined by  $\rm t_{rr}$  and  $\rm I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**

**Device code** 

VS-	20	С	T	Н	03	S	TRL	PbF
1	2	3	4	5	6	7	8	9

- 1 HPP product suffix
- 2 Current rating (20 A)
- 3 C = Common cathode
- 4 T = TO-220, D<sup>2</sup>PAK
- 5 H = Hyperfast rectifier
- 6 Voltage rating (03 = 300 V)
- 7 • S = D<sup>2</sup>PAK
  - -1 = TO-262
- None = Tube (50 pieces)
  - TRL = Tape and reel (left oriented, for D<sup>2</sup>PAK package)
  - TRR = Tape and reel (right oriented, for D<sup>2</sup>PAK package)
- 9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95014</u>						
Part marking information	www.vishay.com/doc?95008					
Packaging information	www.vishay.com/doc?95032					

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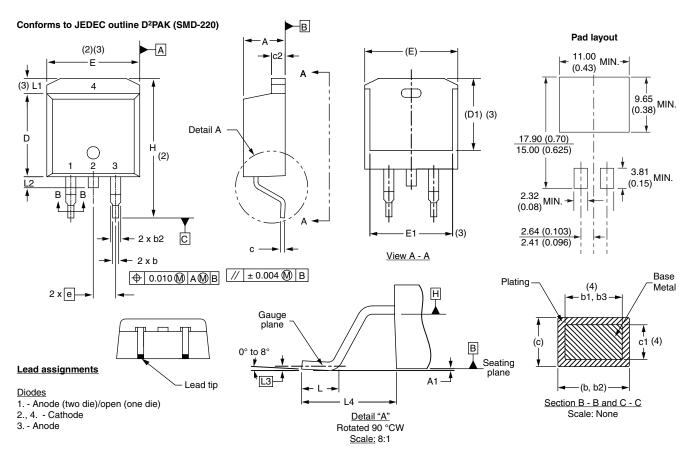
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### Vishay High Power Products

## **D<sup>2</sup>PAK**, **TO-262**

### **DIMENSIONS FOR D<sup>2</sup>PAK** in millimeters and inches



CVMDOL	MILLIM	MILLIMETERS		INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

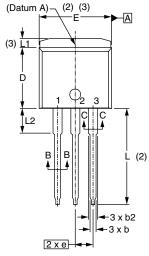
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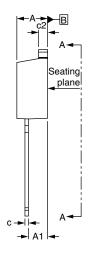
D<sup>2</sup>PAK, TO-262

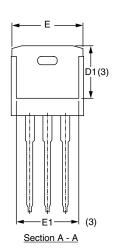


### **DIMENSIONS FOR TO-262** in millimeters and inches

## Modified JEDEC outline TO-262 (Datum A) (2) (3)







**⊕** 0.010**⋒**|A**⋒**|B

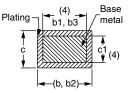
#### Lead assignments



#### **Diodes**

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

OVMDOL	MILLIMETERS		INC	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100	) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline





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