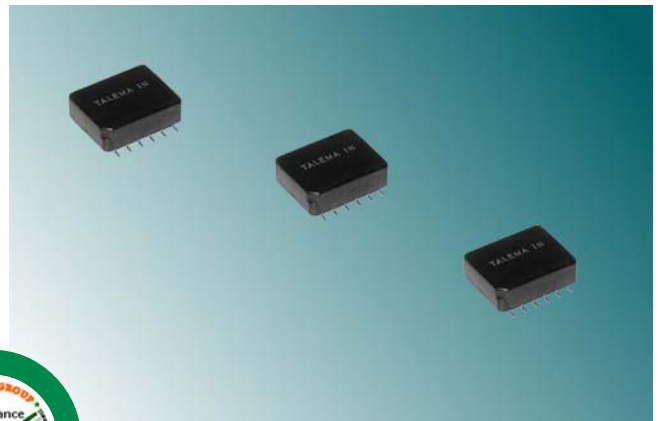




E1/T1/ PRI/CEPT Dual SMD Transformer Modules

Features

- controlled parameters ensure full compliance with ITU-T G.703 when matched with recommended IC
- 1500 Vrms minimum isolation voltage
- ideal for all 1.544 and 2.048 Mbs interface applications
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- 1500Vrms minimum isolation voltage
- extended operating temperature: -4 0° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C



Electrical Specifications @ 25°C

Dual Module with both Transmit and Receive Transformers

Part Number	Turns Ratio		OCL Pins	L _L (μH max)	C _{WW} (pF max)	DCR		V _P Vrms
	±2%	Pins				(Ohms max)	Pins	
82219	1:2	(12-10:1-3) & (4-6:9-7)	1.2mH Min.	0.6	40	0.50	(12-10) & (9-7)	1500
	0.395:1	12-11:1-3	(10-12)			0.85	(1-3)	
	1.58:1	9-8:4-6	(9-7)			0.30	(4-6)	

Test Conditions:

Inductance (OCL): Windings 9-7 = 12-10 = 1.2mH Min. @ 10kHz, 100mV

Leakage (L_L): Windings 10-12 = 0.6μH Max. @ 100kHz, 10mV
Windings 9-7 = 0.8μH Max. @ 100kHz, 10mV

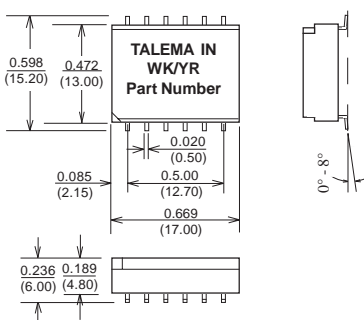
Turns Ratio: Windings 12-10:1-3 = 1:2 ±2%
12-11:1-3 = 0.395:1 ±2%
9-7:4-6 = 2:1 ±2%
9-8:4-6 = 1:1.583 ±2%

Winding Capacitance (C_{WW}): Windings 12-11:1-3 and 9-8:4-6 = 40pF Max. @ 100kHz, 100mV

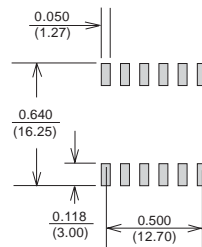
Resistance (DCR): Windings 9-7 = 12-10 = 0.50 Ohms Max.
4-6 = 0.30 Ohms Max.
1-3 = 0.85 Ohms Max.

Dielectric Strength (V_p): Winding to winding = 1.5kVrms

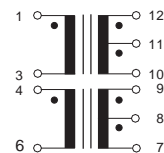
Package



Suggested Pad Layout



Schematic



Surface Coplanarity will be 0.004(0.10) maximum

Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise