

HITK0302MP

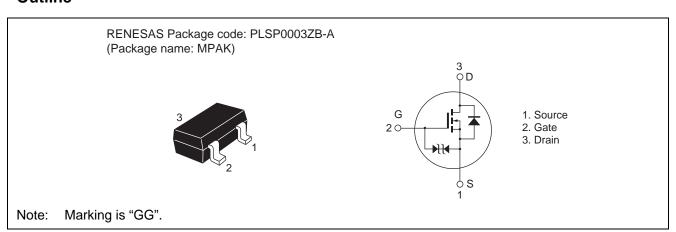
Silicon N Channel MOS FET Power Switching

R07DS0483EJ0100 Rev.1.00 Jun 22, 2011

Features

- Low on-resistance
 - $R_{DS(on)} = 92 \text{ m}\Omega \text{ typ } (V_{GS} = 10 \text{ V}, I_D = 1.3 \text{ A})$
- Low drive current
- High speed switching
- 4.5 V gate drive

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	2.7	Α
Drain peak current	I _{D(Pulse)} Note1	5	Α
Body - drain diode reverse drain current	I _{DR}	2.7	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: $40 \times 40 \times 1$ mm)

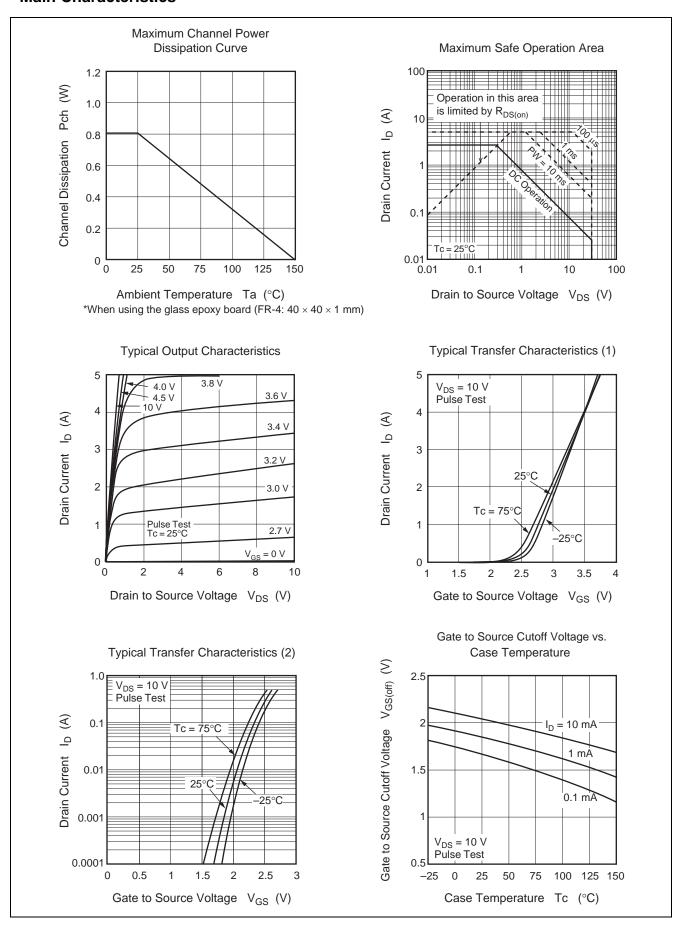
Electrical Characteristics

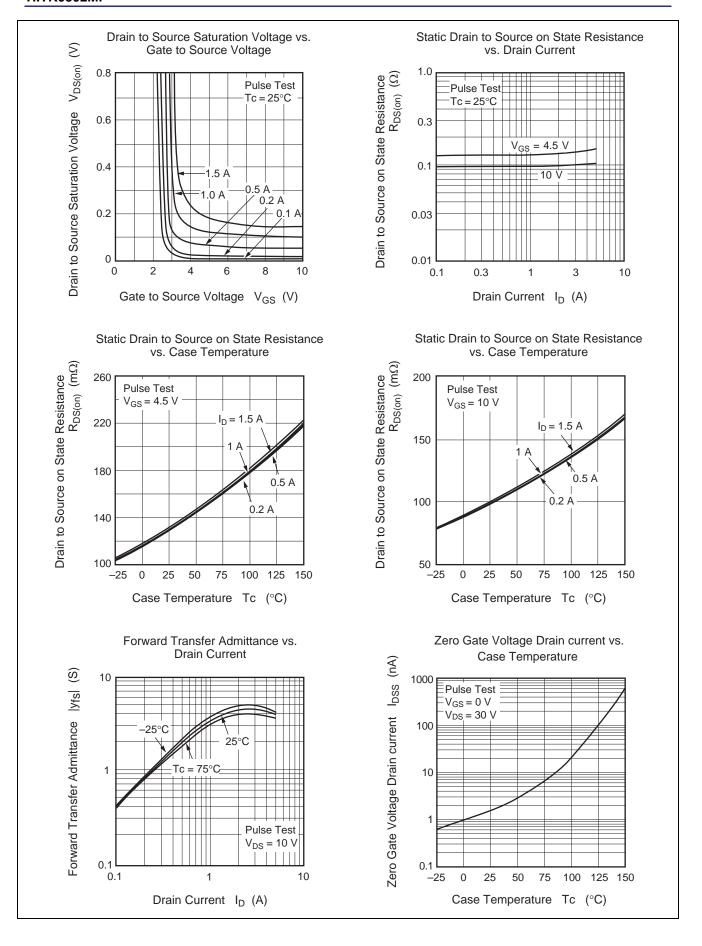
 $(Ta = 25^{\circ}C)$

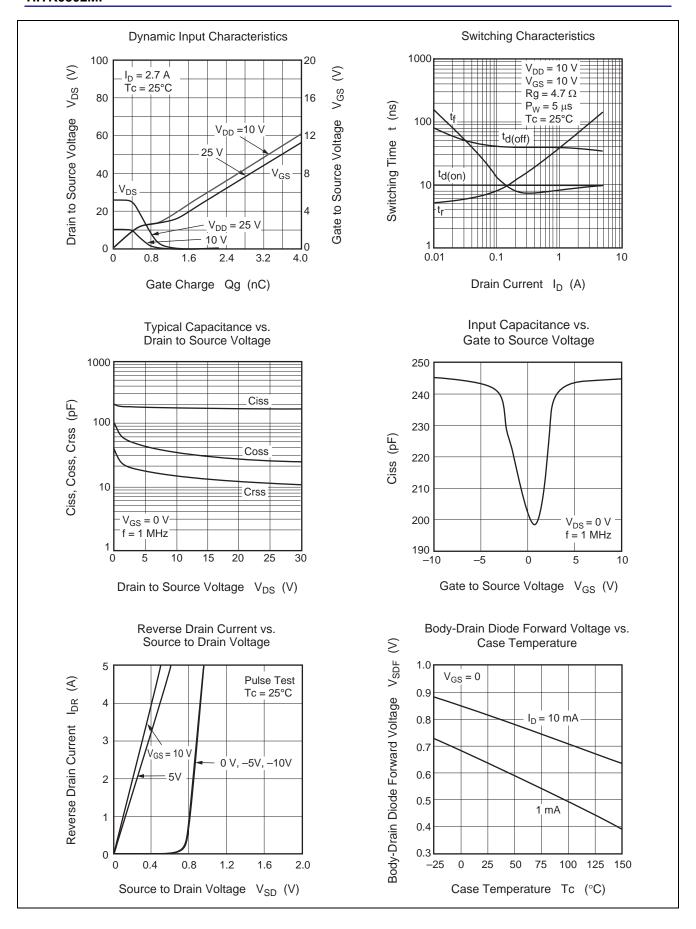
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	30			V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20			V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$	
Gate to source leak current	I _{GSS}	_		±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	
Drain to source leak current	I _{DSS}	_		1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Drain to source on state resistance	R _{DS(on)}	_	92	115	mΩ	$I_D = 1.3 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note3}}$	
	R _{DS(on)}	_	122	171	mΩ	$I_D = 1.3 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note3}}$	
Forward transfer admittance	y _{fs}	2.1	3.5	_	S	$I_D = 1.3 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$	
Input capacitance	Ciss	_	175	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss	_	34	_	pF	f = 1 MHz	
Reverse transfer capacitance	Crss	_	15	_	pF		
Turn - on delay time	t _{d(on)}	_	9.5		ns	$I_D = 1 A, V_{GS} = 10 V,$	
Rise time	t _r	_	37		ns	$R_L = 10 \Omega$, $Rg = 4.7 \Omega$	
Turn - off delay time	$t_{d(off)}$	_	38		ns		
Fall time	t _f	_	8.2	_	ns		
Total gate charge	Qg	_	3.3	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$	
Gate to source charge	Qgs	_	0.6	_	nC	$I_D = 2.7A$	
Gate to drain charge	Qgd	_	0.5		nC		
Body - drain diode forward voltage	V_{DF}	_	0.9	_	V	$I_F = 1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$	

Notes: 3. Pulse test

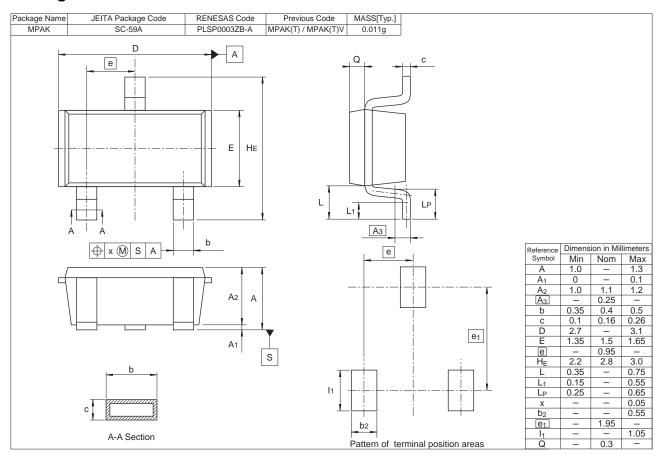
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
HITK0302MPTL-HQ	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

Note: This product is designed for consumer use and not for automotive.

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