

3.3 VOLT LOGIC CLOCK OSCILLATOR

SPECIFICATIONS	A53AA	A52AA	A53RA	A52RA
Frequency Range	13MHz to 125MHz			
Frequency Stability	±100ppm	±50ppm	±100ppm	±50ppm
	(Inclusive of calibration tolerance at 25°C, operating temperature range, input voltage change, load change, aging, shock and vibration)			
Temperature Range	0°C to +70°C			
Output	Waveform	CMOS Squarewave , TTL Compatible		
	Load	50pF Maximum		
	Voltage Voh	Vcc - .5V Minimum		
	Vol	.44V Maximum		
	Current Ioh	-24mA		
	Iol	24mA		
	Duty Cycle	45/55 Maximum Measured at 1.5V		
Rise/Fall Time	2nS Maximum			
Phase Jitter	5pS RMS Maximum			
Input	Enable/Disable	N/A		Pin 1
	Output Enable/Disable Time	N/A		100nS
	Enable Vih	N/A		2.1V Minimum
	Disable	N/A		0.8V Maximum
	Oscillator output is enabled with no connection on pin 1			
Start Up Time	10mS Maximum			
Supply Voltage	3.3Vdc ±5%			
Supply Current	100mA Maximum			
Package	All metal, hermetically sealed, welded package			

