

SE1086

1.5A Low Dropout Positive Adjustable or Fixed-Mode Regulator

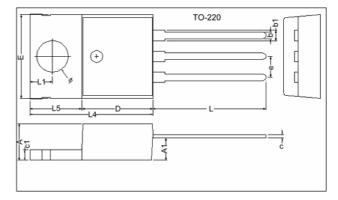
RoHS Compliant Product

Description

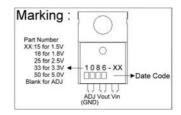
The SE1086 is a low dropout at positive adjustable or fixed-mode regulator with min. of 1.5A output current capability. The product is specifically designed to provide well-regulated supply for low voltage IC applications such as high-speed bus termination and low current 3.3V logic supply. SE1086 is also well suited for other applications such as VGA cards. SE1086 is guaranteed to have lower than 1.4V dropout at full load current making it ideal to provide well regulated outputs of 1.25 to 5V with 4.7 to 12V input supply.

Features

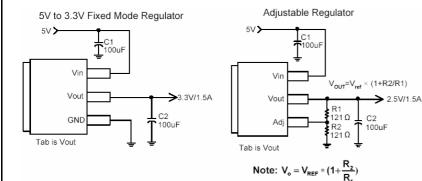
- * 1.4V Max. Dropout Full Load Current
- * 3-Terminal Adjustable or Fixed 1.5V,1.8V,2.5V,3.3V,5.0V
- * Output Current Limiting
- * Fast Transient Response
- * Built-in Thermal Shutdown



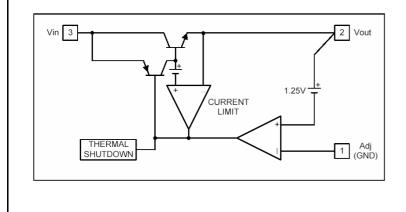
REF.	Millimeter		REF.	Millimeter		
	Min.	Max.	NEF.	Min.	Max.	
А	4.40	4.80	c1	1.25	1.45	
b	0.76	1.00	b1	1.17	1.47	
С	0.36	0.50	L	13.25	14.25	
D	8.60	9.00	е	2.54 REF.		
E	9.80	10.4	L1	2.60	2.89	
L4	14.7	15.3	Ø	3.71	3.96	
L5	6.20	6.60	A1	2.60	2.80	



Typical Circuit



Block Diagram





Elektronische Bauelemente

SE1086

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Pin Descriptions

Name	I/O	PIN#	FUNCTION
Adj (GND)		1	Adjustable (Ground only for fixed mode)
Vout	0	2	The output of the regulator. A minimum of 10uF capacitor must be connected from this pin to ground to insure stability.
Vin	I	3	The input pin of regulator .Typically a large storage capacitor is connected from this pin to ground to insure that the input voltage does not sag below the min. dropout voltage during the load transient response .This pin must always be 1.5V higher than Vout in order for the device to regulate properly.

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit	
DC Supply Voltage	VIN	-0.3 to 12	V	
Power Dissipation	PD	Internally Limited		
Storage Temperature Range	Тsт	-65~+150	°C	
Operating Juction Temperature Range	Тор	0~+150	C	

Electrical Characteristics

Parameter	Conditions		MIN	ТҮР	MAX	UNIT	
Reference Voltage	SE1086-Adj	lo=10mA, TJ =25℃, (Vin-Vout)=1.5V	1.225	1.250	1.275	V	
Output Voltage	SE1086-1.5	lo=10mA, TJ =25℃, 3V≤Vin≤12V	1.470	1.500	1.530	V	
	SE1086-1.8	lo=10mA, TJ =25℃, 3.3V≤Vin≤ 12V	1.764	1.800	1.836	V	
	SE1086-2.5	lo=10mA, TJ =25℃, 4V≤Vin≤12V	2.450	2.500	2.550	V	
	SE1086-3.3	lo=10mA, TJ =25℃, 4.8V≤Vin≤ 12V	3.235	3.300	3.365	V	
	SE1086-5.0	lo=10mA, TJ =25℃, 6.5V≤Vin≤ 12V	4.900	5.000	5.100	V	
Line Regulator	SE1086-XXX	Io=10mA, Vout+1.5 <vin<12< td=""><td>-</td><td>-</td><td>0.2</td><td>%</td></vin<12<>	-	-	0.2	%	
	SE1086ADJ	Vin=3.3V, 0mA <io<1.5a, (note="" 1,2)<="" td="" tj="25°C"><td>-</td><td>-</td><td>1</td><td>%</td></io<1.5a,>	-	-	1	%	
	SE1086-1.5	Vin=3.0V, 0mA <io<1.5a, (note="" 1,2)<="" td="" tj="25°C"><td>-</td><td>12</td><td>15</td><td>mV</td></io<1.5a,>	-	12	15	mV	
Load Regulation	SE1086-1.8	Vin=3.3V, 0mA <lo<1.5a, (note="" 1,2)<="" td="" tj="25°C"><td>-</td><td>15</td><td>18</td><td>mV</td></lo<1.5a,>	-	15	18	mV	
Loau negulation	SE1086-2.5	Vin=4.0V, 0mA <io<1.5a, (note="" 1,2)<="" td="" tj="25°C"><td>-</td><td>20</td><td>25</td><td>mV</td></io<1.5a,>	-	20	25	mV	
	SE1086-3.3	Vin=5.0V, 0mA <io<1.5a, (note="" 1,2)<="" td="" tj="25°C"><td>-</td><td>26</td><td>33</td><td>mV</td></io<1.5a,>	-	26	33	mV	
	SE1086-5.0	Vin=8.0V, 0mA <io<1.5a, (note="" 1,2)<="" td="" tj="25°C"><td>-</td><td>40</td><td>50</td><td>mV</td></io<1.5a,>	-	40	50	mV	
Dropout Voltage (Vin-Vout)	SE1086-XXX	Io=1.5A(∆ Vout=1% Vout)	-	1.3	1.4	V	
Current Limit	SE1086-XXX	Vin-Vout=5V	1.6	-	-	А	
Minimum Load Current	SE1086-XXX	0°C≤ TJ ≤125°C	-	5	10	mA	
Thermal Regulation	Ta=25°C, 30ms pulse		-	0.008	0.04	%/W	
Disala Daisatian	F=120Hz, Cout=25uF, Tantalum, Io=1.5A						
Ripple Rejection	SE1086-XXX	Vin=Vout+3V	-	60	70	dB	
Temperature Stability	lo=10mA		-	0.5	-	%	
Thermal Resistance Junction–to-Ambient (No heat sink ;No air flow)			-	86	-	°C/W	
Thermal Resistance Junction-to-Case	Control Circuitry/Power Transistor		-	0.65/2.7	-	°C/W	

Note 1: See thermal regulation specifications for changes in output voltage due to heating effects. Line and load regulation are measured at a constant junction Temperature by low duty cycle pulse testing. Load regulation is measured at the output lead =1/18" from the package.

Note 2: Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the difference in input and output and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

Note 3: Quiescent current is defined as the minimum output current required in maintaining regulation. At 12V input/output differential the device is guaranteed to regulate if the output current is greater than 10mA.

http://www.SeCoSGmbH.com/



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Functional Description

Introduction

The SE1086 adjustable or fixed-mode Low Dropout (LDO) regulator is a 3 terminal device which can easily be programmed by internal mask change to any voltage within the range of 1.25 to Vin-1.4V. The SE1086 only needs 1.4V differential between Vin and Vout to maintain output regulation, in addition, the output voltage tolerances are also extremely tight and they include the transient response as port of the specification. For example, Intel VRE specification calls for a total of +/-100mV including initial tolerance, load regulation and 0 to 1.5A load step. The SE1086 is specifically designed to meet the fast current transient needs as well as providing an accurate initial voltage, reducing the overall system cost with the need for fewer output capacitors.

Load Regulation

Since the SE1086 is only a 3 terminal device, it is not possible to provide true remote sensing of the output voltage at the load. But it can supply good load regulation by internal feedback bypass the external loss such as adjustable mode. **Stability**

The SE1086 requires the use of an output capacitor as part of the frequency compensation in order to make the regulator stable .For most applications a minimum of 10uF aluminum electrolytic capacitor insures both stability and good transient response.

Thermal Design

The SE1086 incorporates an internal shutdown that protects the device when the junction temperature exceeds the maximum allowable junction temperatures. Although this device can operate with junction temperatures in the range of 150° C, it is recommended that the selected heat sink be chosen such that during maximum continuous load operation the junction temperature is kept below the temperature.

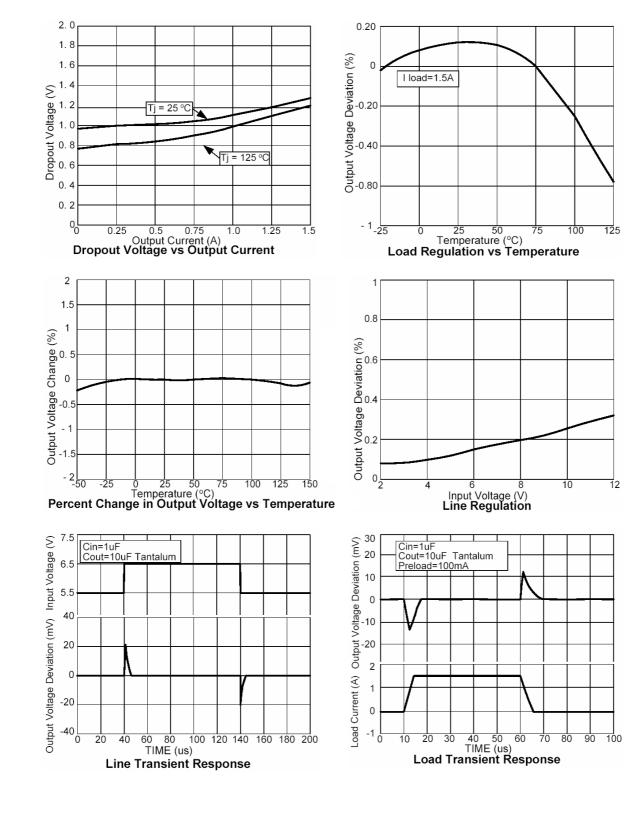
Layout Consideration

The output capacitors must be located as close to the Vout terminal of the device as possible .It is recommended to use a section of a layer of the PC board as a plane to connect the Vout pin to the output capacitors to prevent any high frequency oscillation that may result due to excessive trace inductance.



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Performance Characteristics

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