

STTH802-Y

Automotive ultrafast recovery diode

Datasheet – production data

Features

- Very low conduction losses
- Negligible switching losses
- Low forward and reverse recovery time
- High junction temperature
- AEC-Q101 qualified

Description

The STTH802-Y uses ST's new 200 V planar Pt doping technology, and is specially suited for switching mode base drive and transistor circuits.

Packaged in DPAK, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection for automotive application.

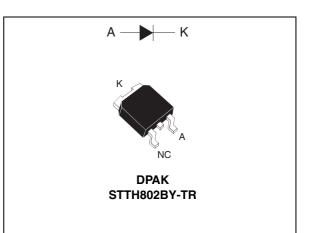


Table 1. Device summary			
I _{F(AV)}	8 A		
V _{RRM}	200 V		
T _{j (max)}	175 °C		
V _F (typ)	0.8 V		
t _{rr} (typ)	17 ns		

This is information on a product in full production.

1 Characteristics

Table 2.Absolute ratings (limiting values at T_i = 25 °C, unless otherwise specified)

Symbol	Parameter			Unit
V _{RRM}	Repetitive peak reverse voltage			V
I _{F(RMS)}	Forward rms current	16	А	
I _{F(AV)}	Average forward current, $\delta = 0.5$ T _c = 145 °C		8	А
I _{FSM}	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$		100	А
T _{stg}	Storage temperature range			°C
Тj	Operating junction temperature range			°C

Table 3.Thermal parameters

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case	3.2	°C/W

Table 4.Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I _B ⁽¹⁾	Povorso lookago ourront	T _j = 25 °C	V _ V			6	
IR ⁽¹⁾ Reverse leakage current	T _j = 125 °C	V _R = V _{RRM}		6	60	μA	
V _F ⁽²⁾ Forward voltage drop		T _j = 25 °C	1 _ 9 A		0.95	1.05	V
	Forward voltage drop	T _j = 150 °C	I _F = 8 A		0.8	0.90	v

1. Pulse test: $t_p = 5 \text{ ms}, \delta < 2\%$

2. Pulse test: $t_p = 380 \ \mu s, \ \delta < 2\%$

To evaluate the conduction losses use the following equation:

$$P = 0.73 \text{ x } I_{F(AV)} + 0.021 I_{F}^{2}(RMS)$$

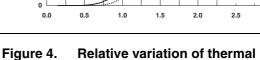
Table 5.	Dynamic characteristics
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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
+	Reverse recovery time	$\label{eq:lf} \begin{array}{l} I_F = 1 \ A, \ dI_F/dt = \text{-50 } A/\mus, \\ V_R = 30 \ V, \ T_j = 25 \ ^\circC \end{array}$		25	30	ns
		$\label{eq:lf} \begin{array}{l} I_{F} = 1 \ A, \ dI_{F}/dt = \text{-100 } A/\mus, \\ V_{R} = 30 \ V, \ T_{j} = 25 \ ^{\circ}C \end{array}$		17	22	
I _{RM}	Reverse recovery current	I_F = 8 A, dI_F/dt = -200 A/µs, V_R = 160 V, T_j = 125 °C		5.5	7	А
t _{fr}	Forward recovery time	$\label{eq:FR} \begin{array}{l} I_{F} = 8 \ A, \ dI_{F}/dt = 50 \ A/\mu s \\ V_{FR} = 1.1 \ x \ V_{Fmax}, \ T_{j} = 25 \ ^{\circ}C \end{array}$		150		ns
V _{FP}	Forward recovery voltage	$I_F = 8 \text{ A, } dI_F/dt = 50 \text{ A/}\mu\text{s},$ $T_j = 25 ^\circ\text{C}$		1.5		V



I_M(A) 100 $I_{\rm M}$ 80 δ=tp 60 P = 5 W40 P = 1 W 20 δ 0 0.0 0.1 0.2 0.3 0.5 0.6 0.7 0.4 0.8 0.9 1.0





Z_{th(j-c)}/R_t

1.0

impedance, junction to case, versus pulse duration

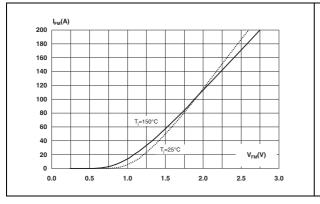


Figure 5. Junction capacitanceversus reverse applied voltage (typical values)



Figure 6. Reverse recovery charges versus dl_F/dt (typical values)

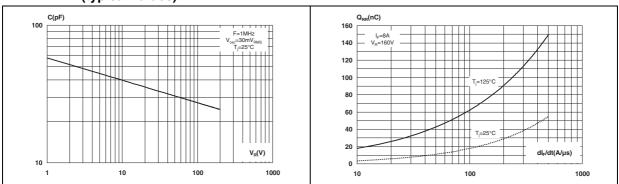
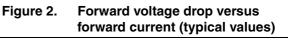


Figure 1. Peak current versus duty cycle



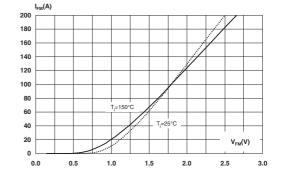


Figure 7. Reverse recovery time versus dl_F/dt Figure 8. (typical values)

Peak reverse recovery current versus dl_F/dt (typical values)

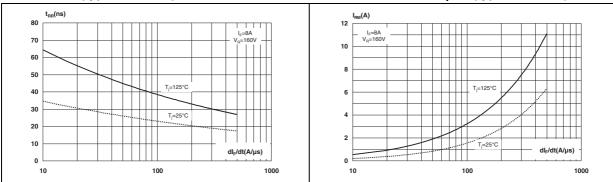
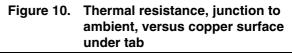
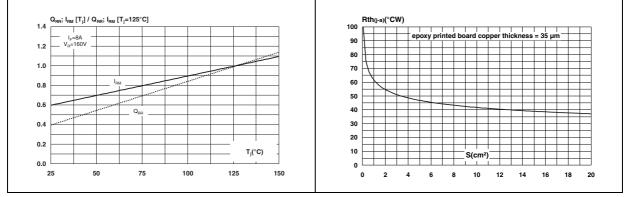


Figure 9. Dynamic parameters versus junction temperature







57

2 Package information

- Epoxy meets UL94, V0
- Lead-free package

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Table 6. DPAK dimensions

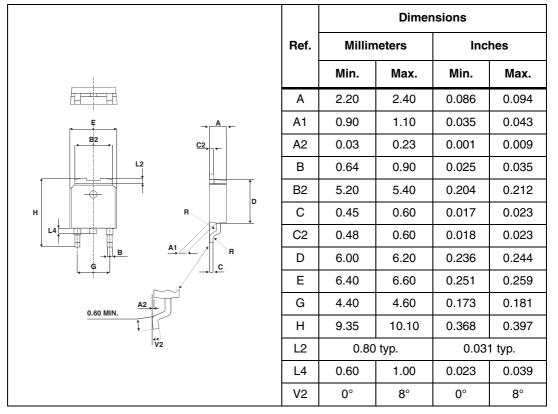
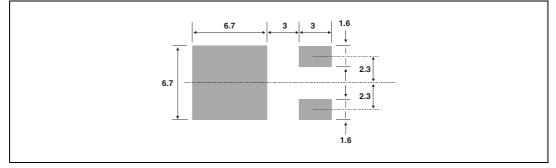


Figure 11. Footprint (dimensions in mm)



3 Ordering information

Table 7.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STTH802BY-TR	STTH802Y	DPAK	0.3 g	2500	Tape and reel

4 Revision history

Table 8.Document revision history

Date	Revision	Changes	
10-Mar-2011	1	First issue.	
24-Oct-2012	2	Updated operating temperature range in Table 2.	



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